

Garuda and Pari: Faster and Smaller SNARKs via Equiffficient Polynomial Commitments



Michel Dellepere

Ava Labs



Pratyush Mishra

UPenn

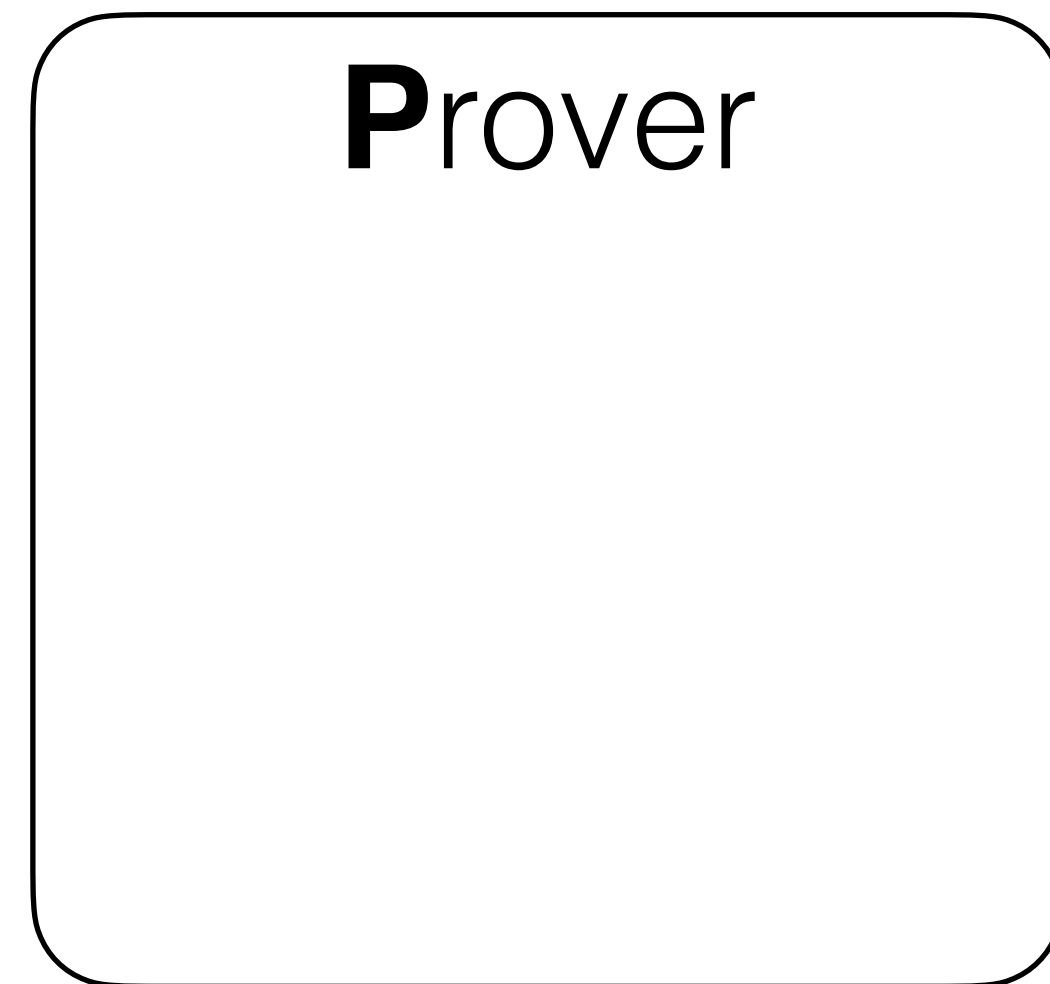


Alireza Shirzad

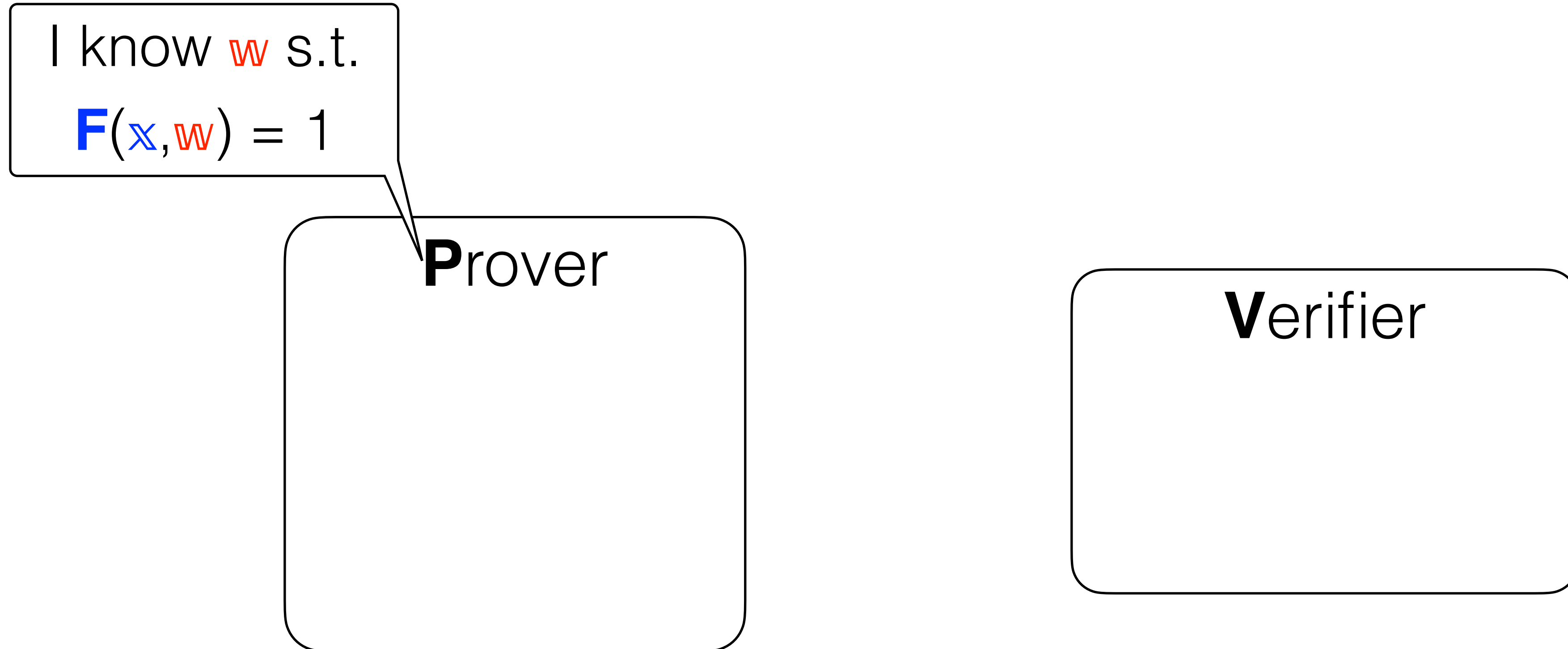
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Succinct Non-Interactive Argument of Knowledge (SNARK)

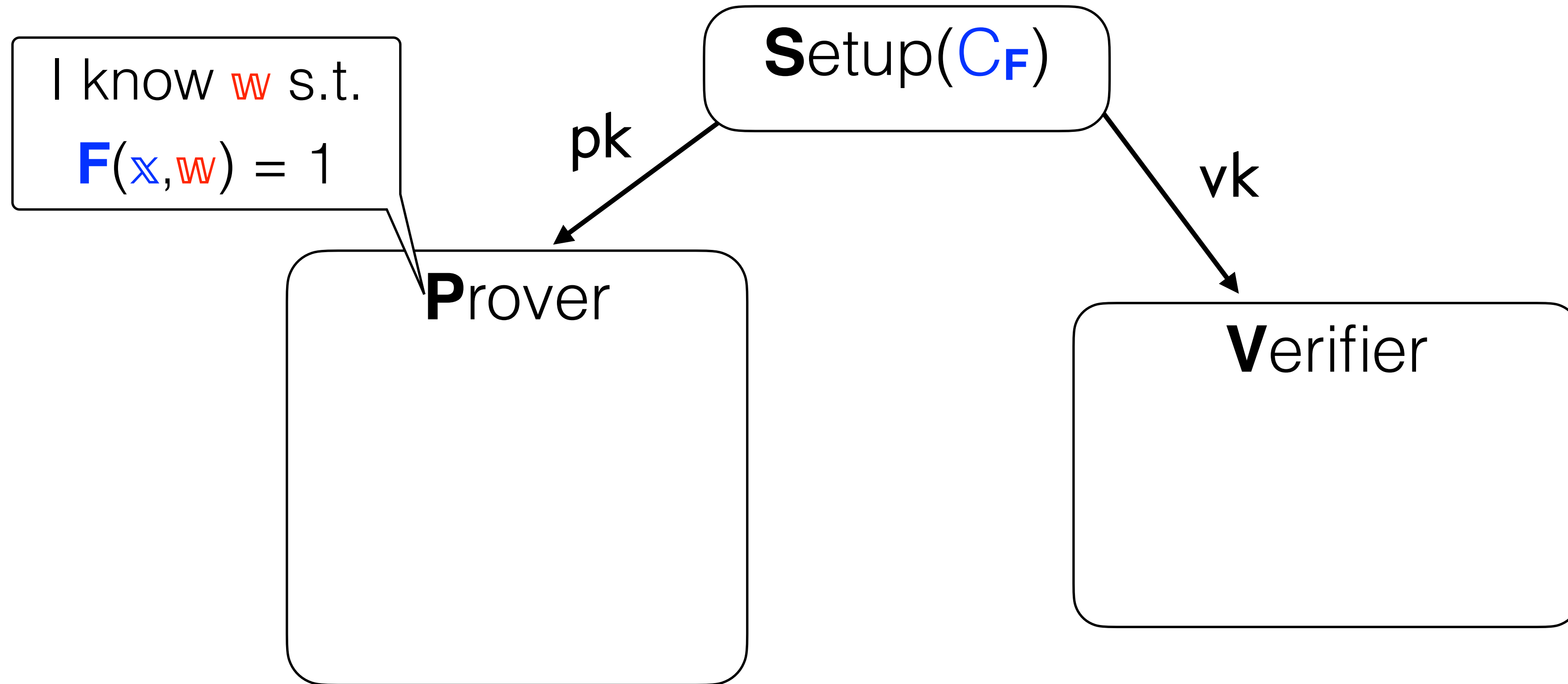
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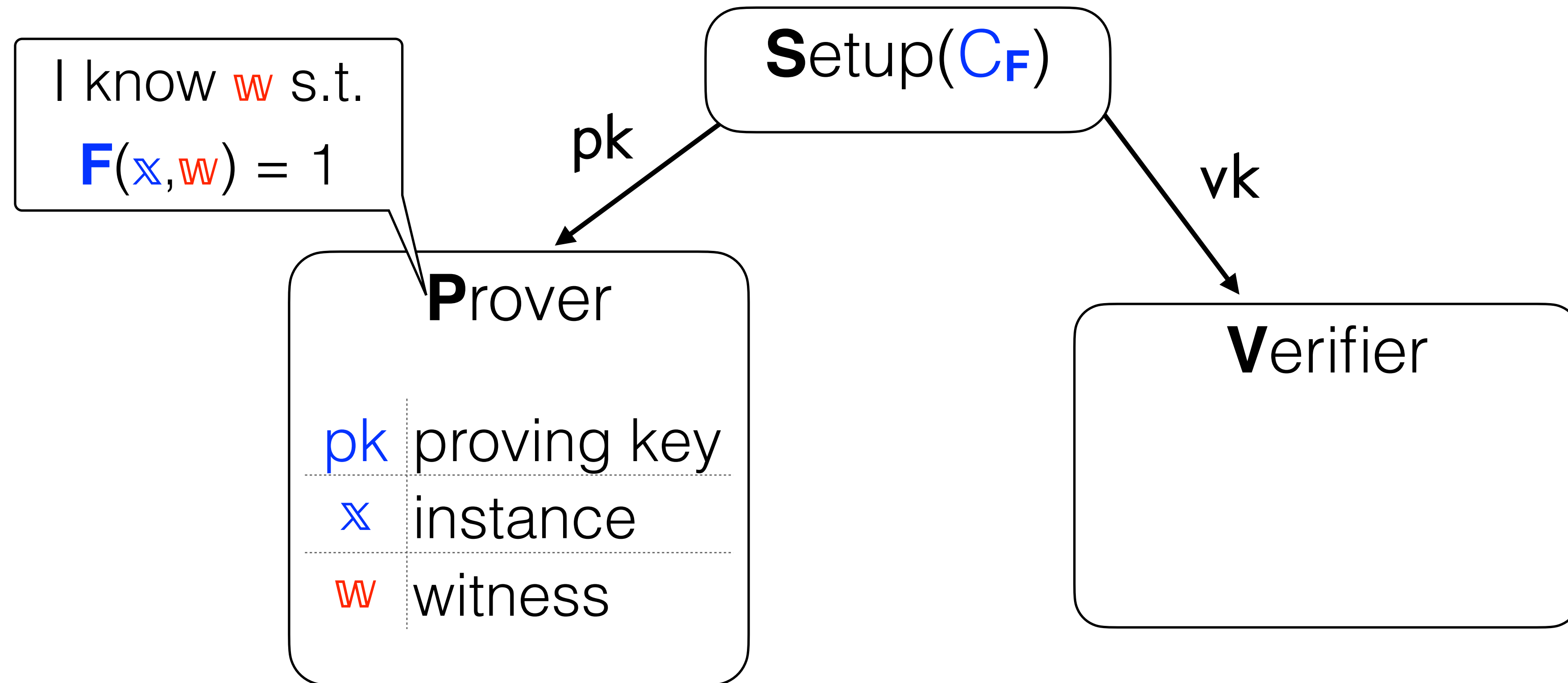
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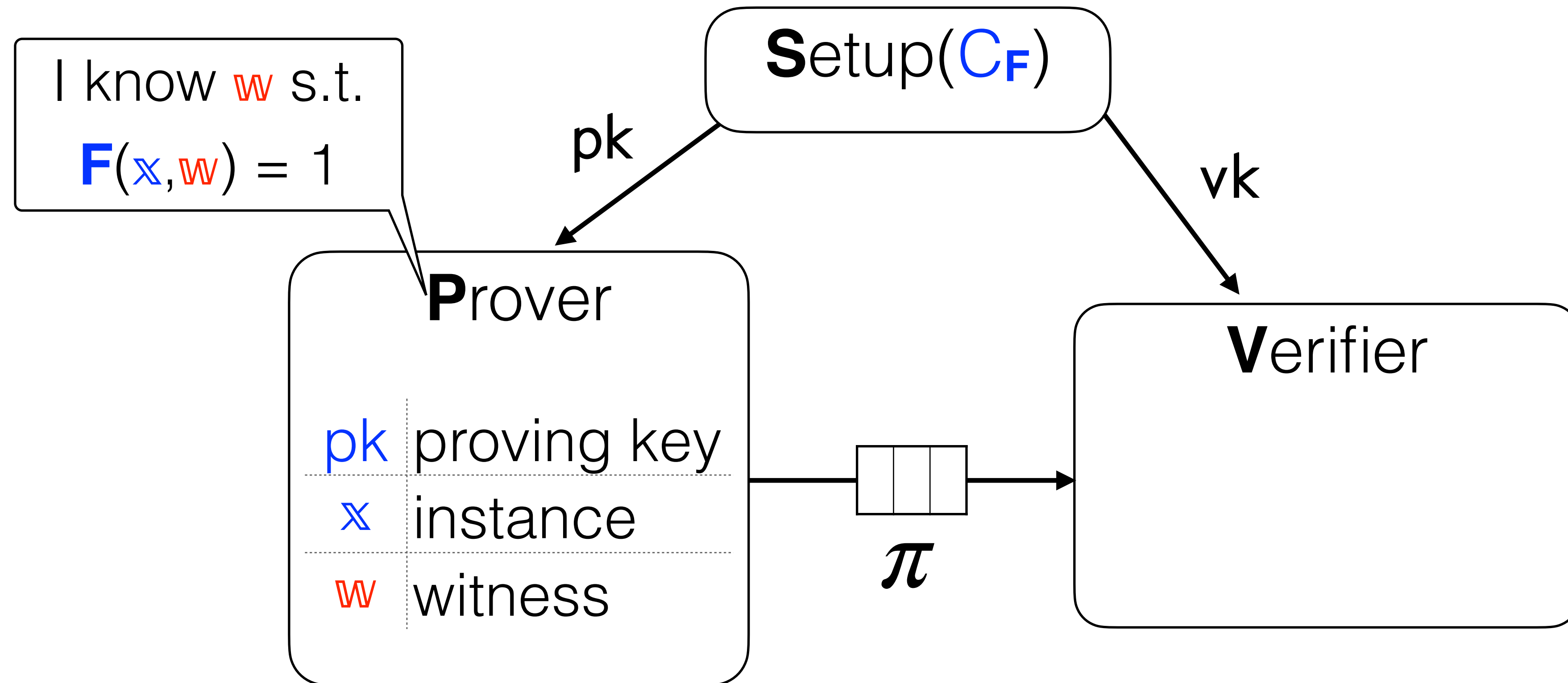
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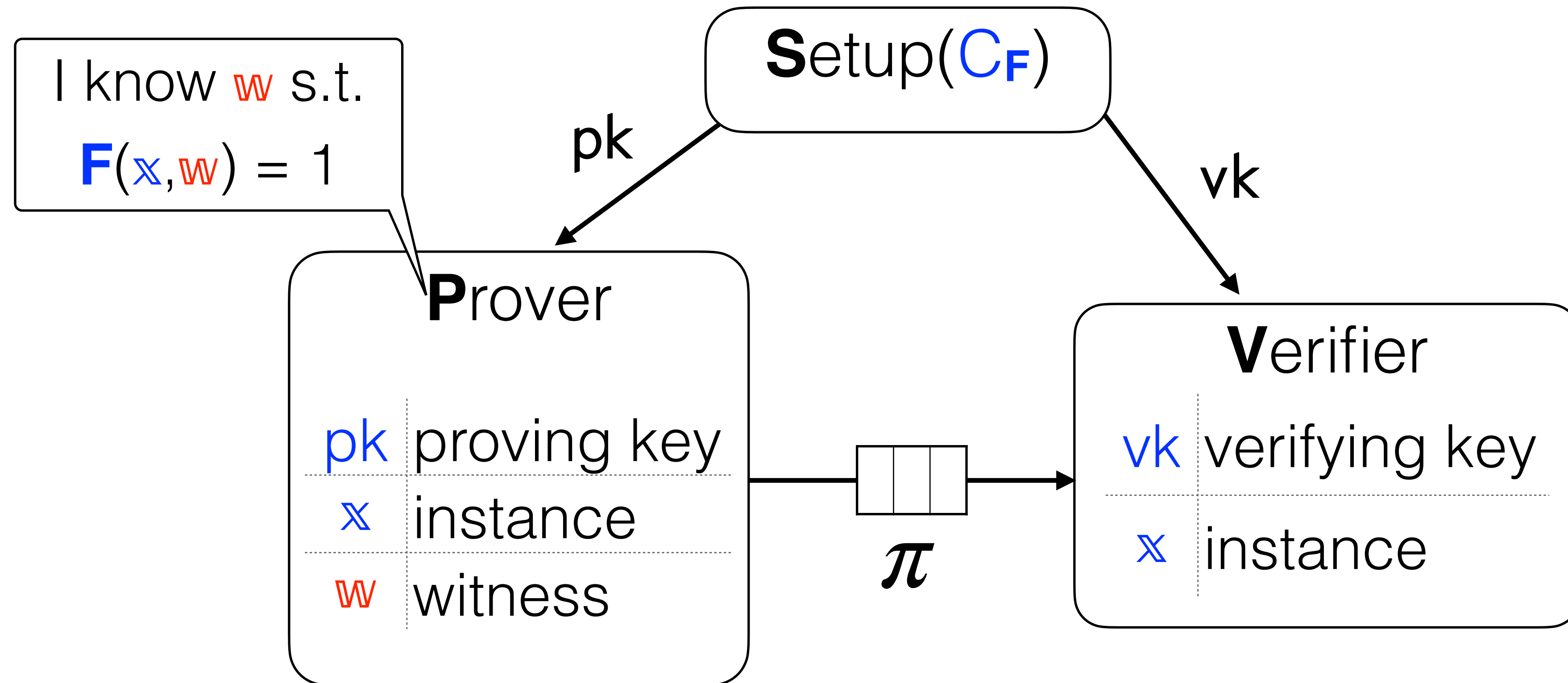
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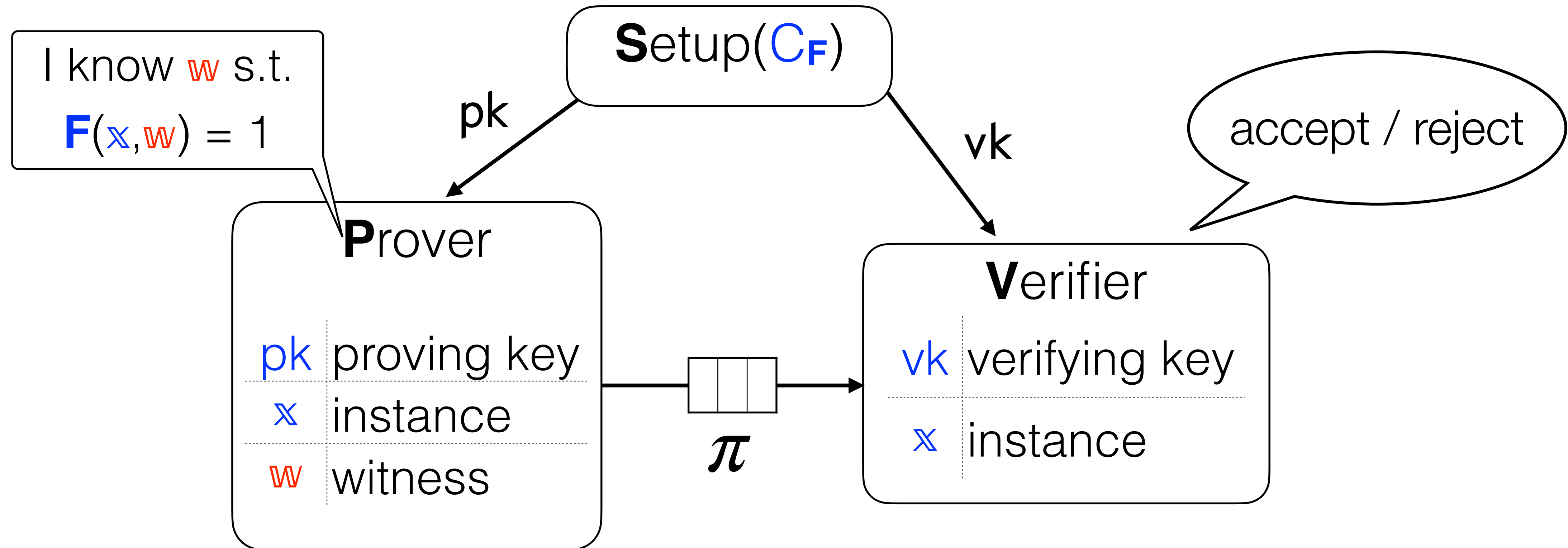
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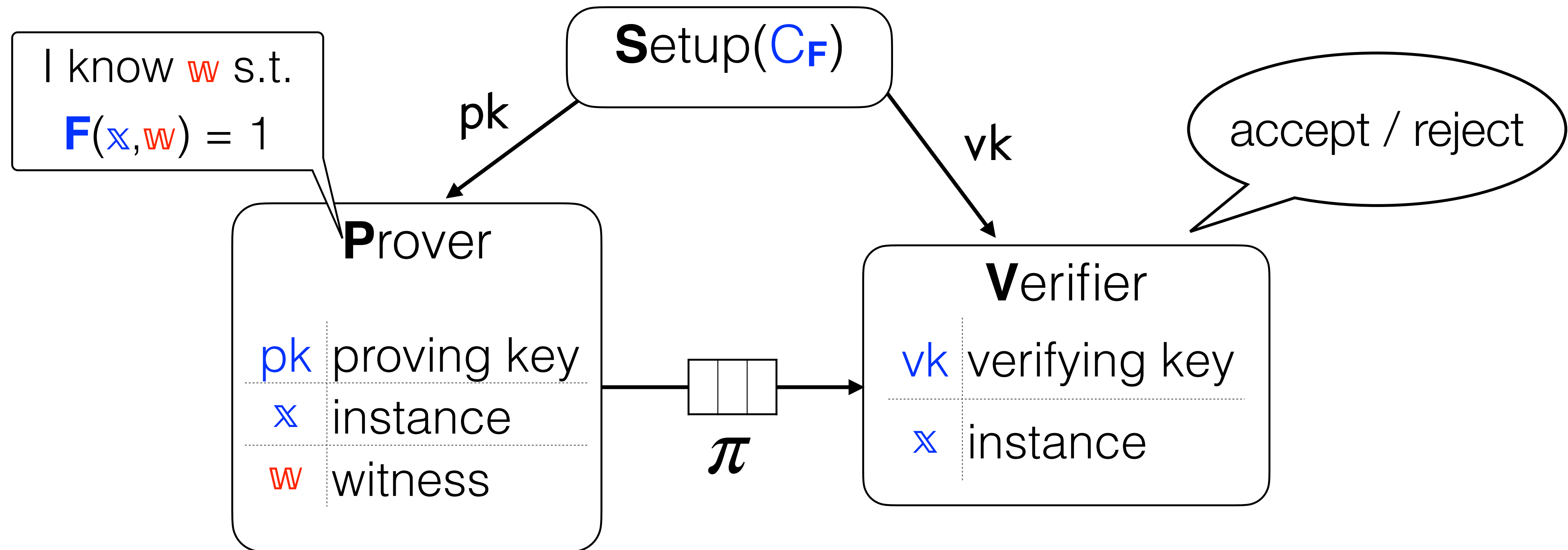
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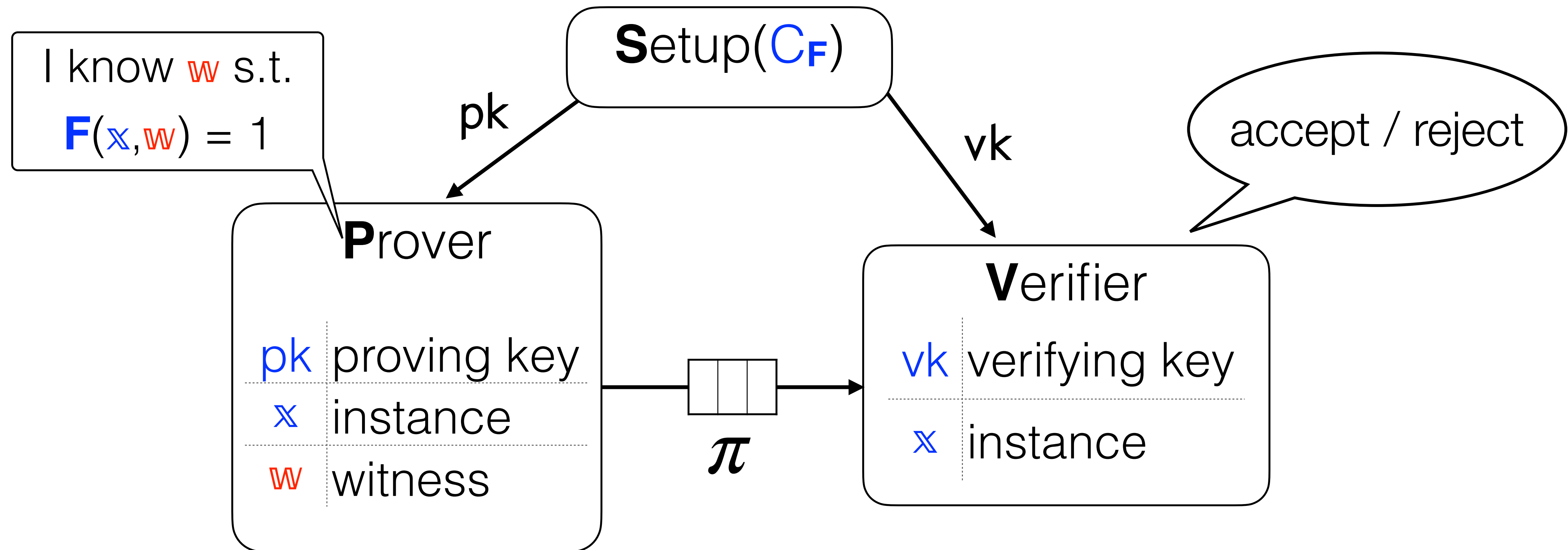


Succinct Non-Interactive Argument of Knowledge (SNARK)



Completeness: If **P** knows valid w , then **V** accepts the proof π

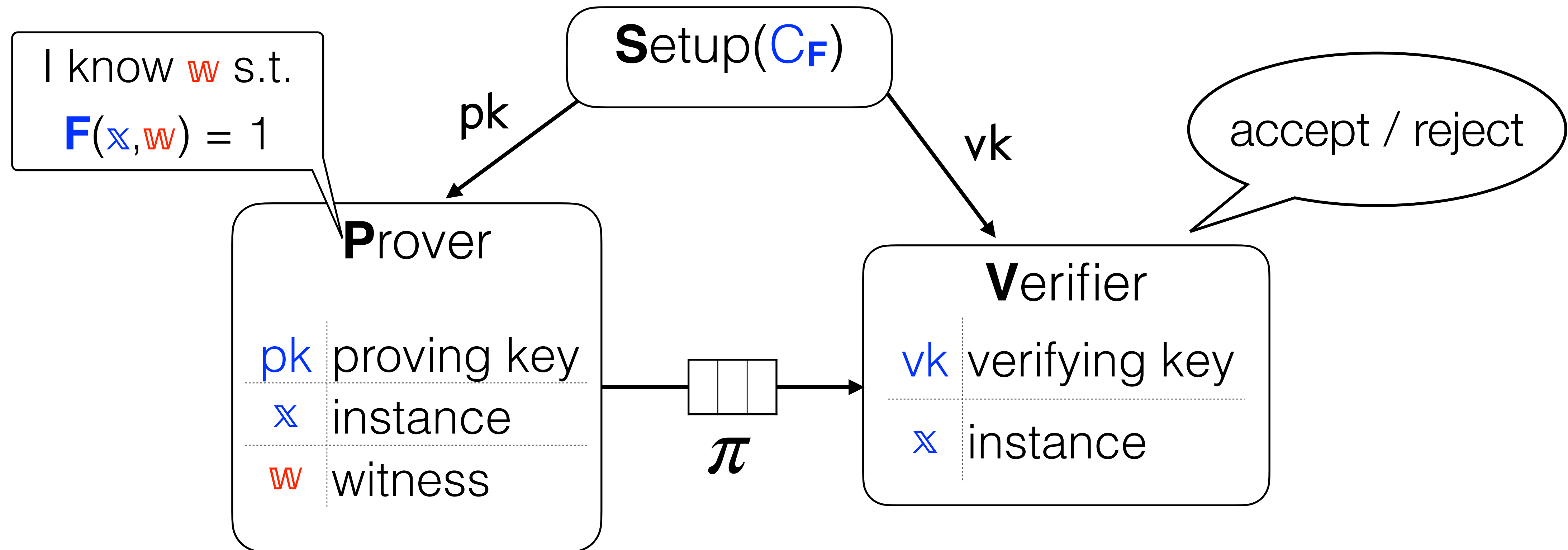
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Succinctness: Size of proof π and verifier running time are much smaller than running time of **F**

Q1: How small can the proof π be?

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For blockchains, smaller is better!

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Groth16 lower bound
Pairing-based SNARKs in
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group elements:
 $|\pi| \geq 1\mathbb{G}_1 + 1\mathbb{G}_2$

144 bytes

$|\pi|$
(on BLS12-381)

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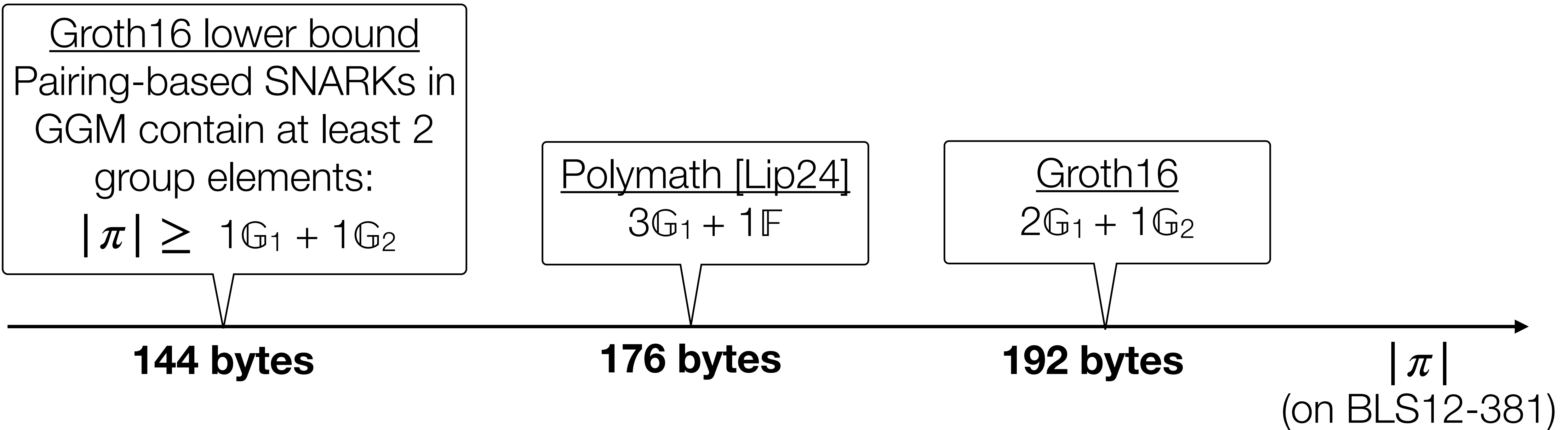
Groth16
 $2\mathbb{G}_1 + 1\mathbb{G}_2$

192 bytes

$|\pi|$
(on BLS12-381)

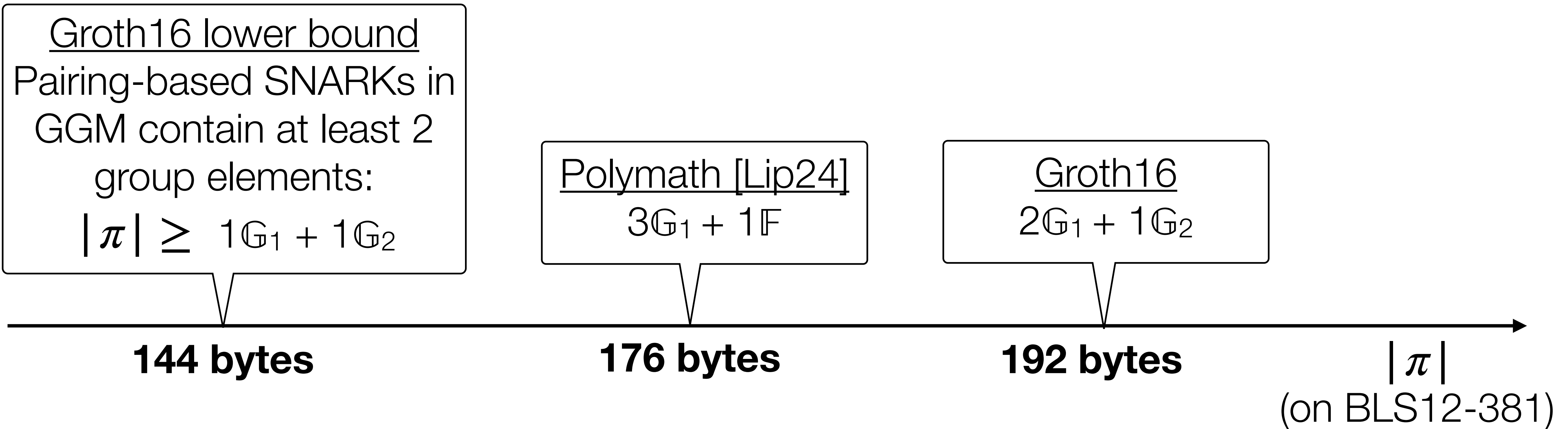
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Can we go lower than 176 bytes?

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- [illegible]

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⋮

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- Proposed recently for TurboPlonk [GW19], used widely [RISC0, Plonky3, CBBZ23, STW23]

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Unfortunately, no existing SNARK supports both!

Can we fix this?

Our Contributions

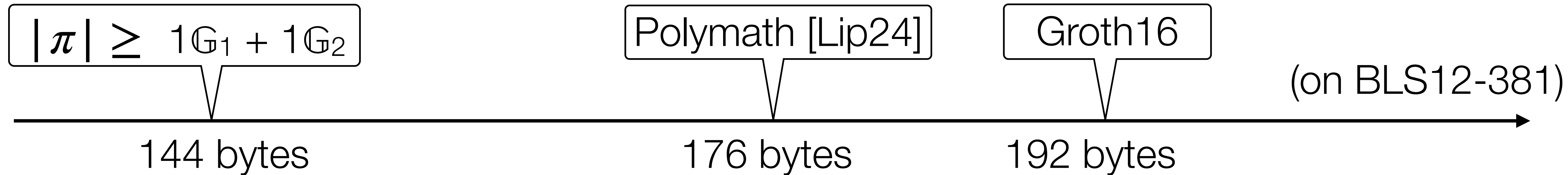
Garuda and Pari

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Pari: The smallest known SNARK with proof size $|\pi| = 2\mathbb{G}_1 + 2\mathbb{F}$

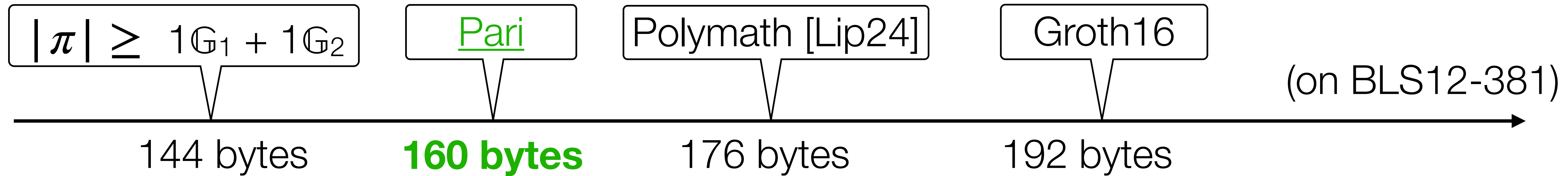
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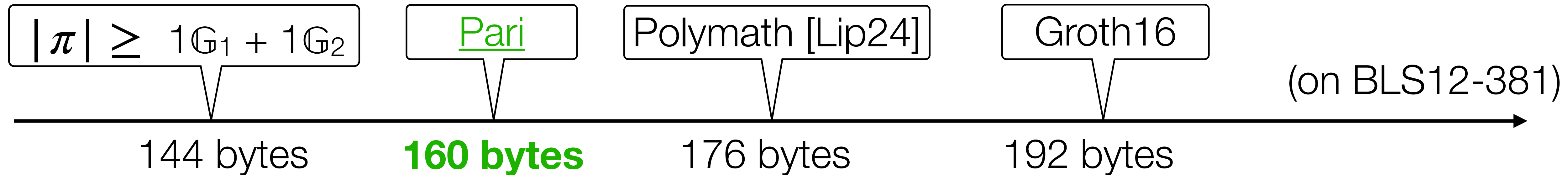
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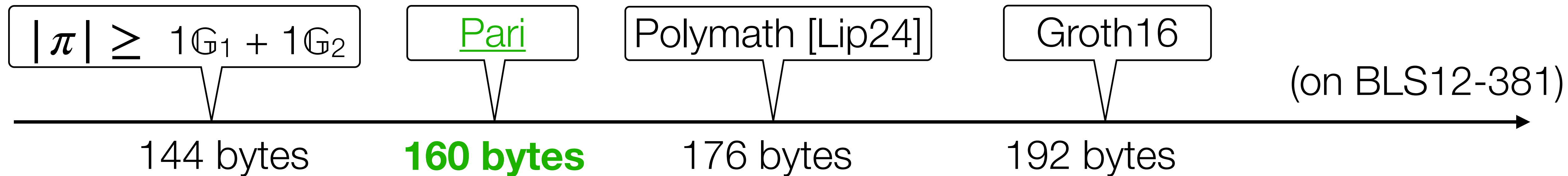
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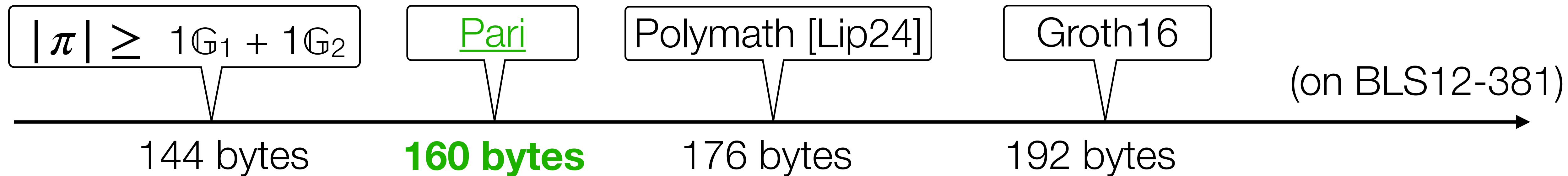


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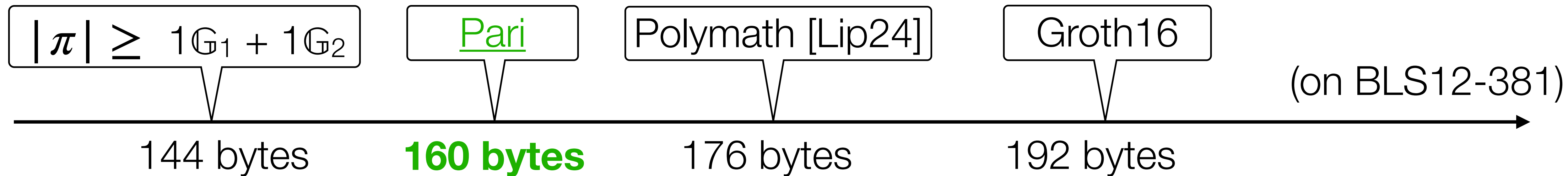
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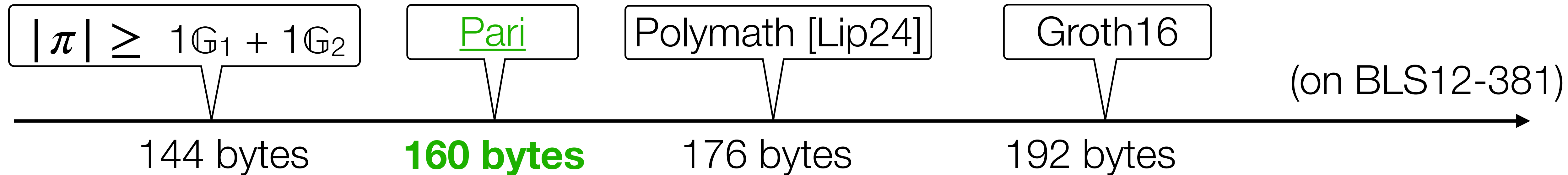
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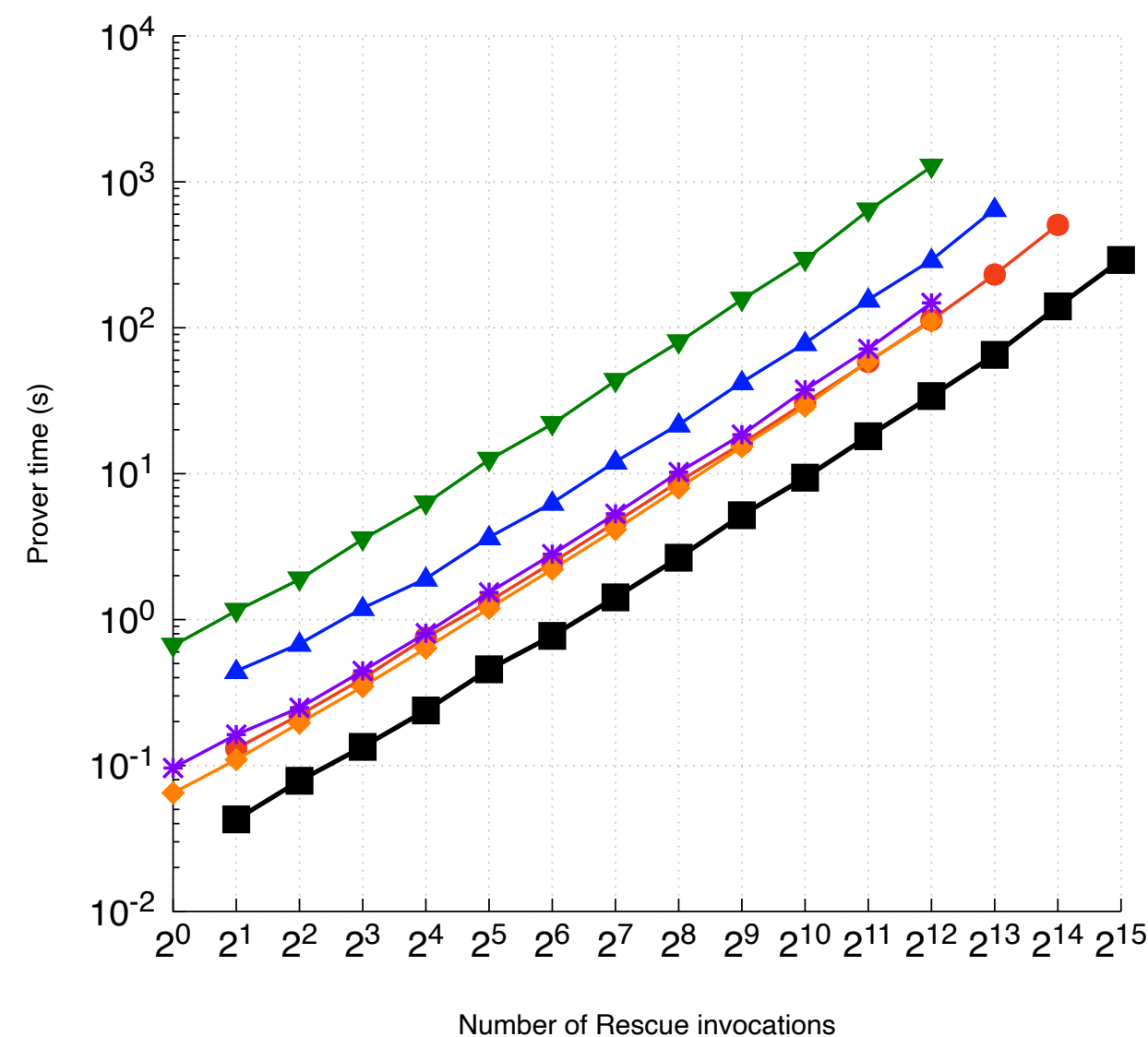
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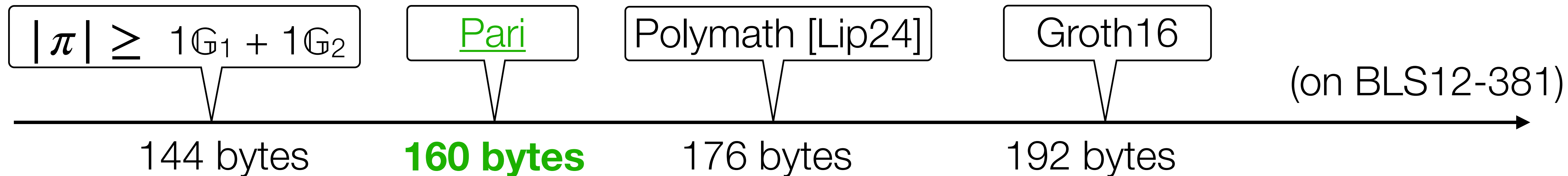


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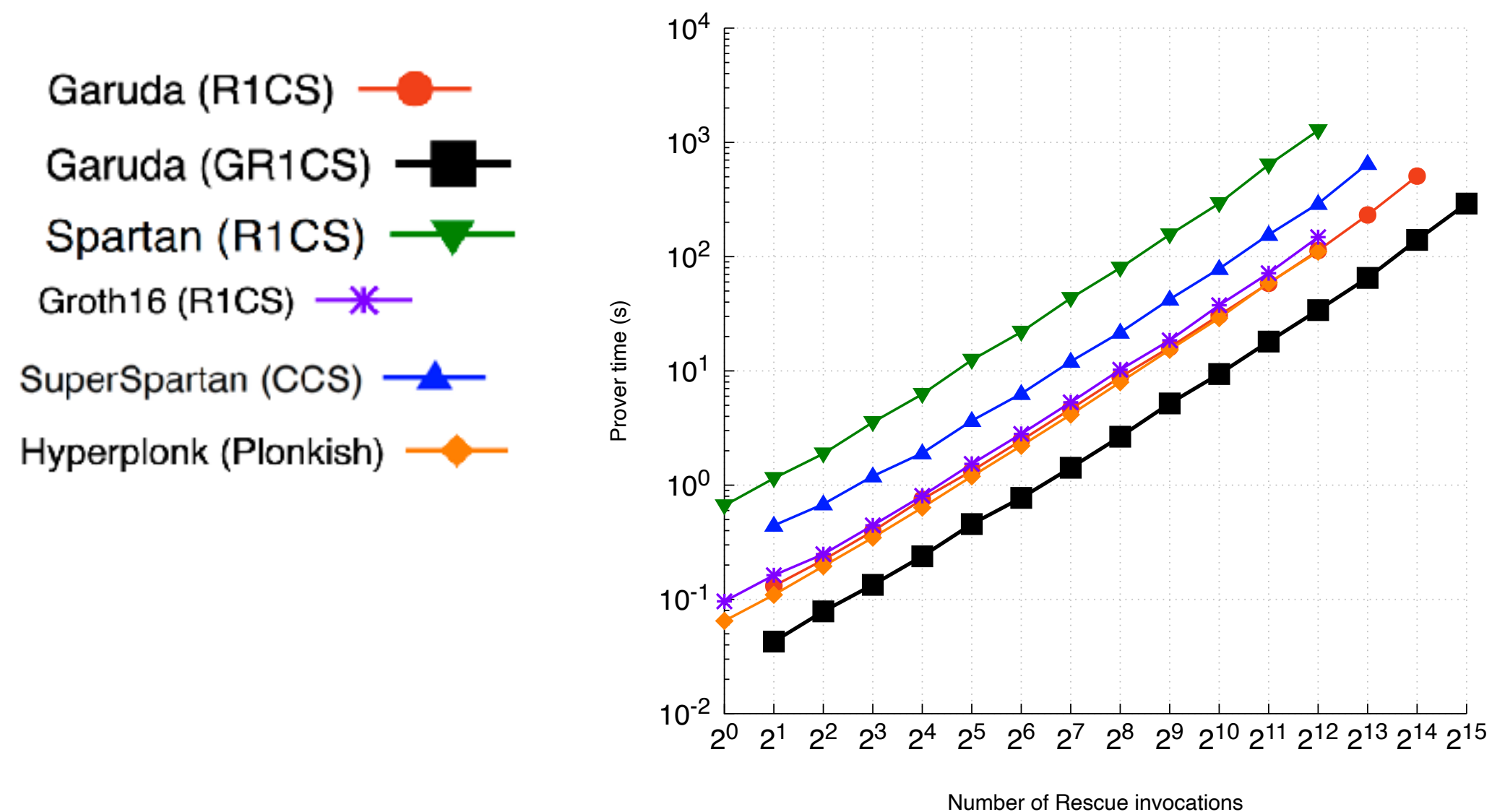
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New Methodology

We adapt existing SNARK methodologies [CHMMVW20, BFS20] to construct our SNARKs

Fewer responsibilities
Only needs to be sound

PIOP

EPC Scheme

Our Compiler

Preprocessing
SNARK for
R1CS

More responsibilities
PC + Equiffluent property

Background

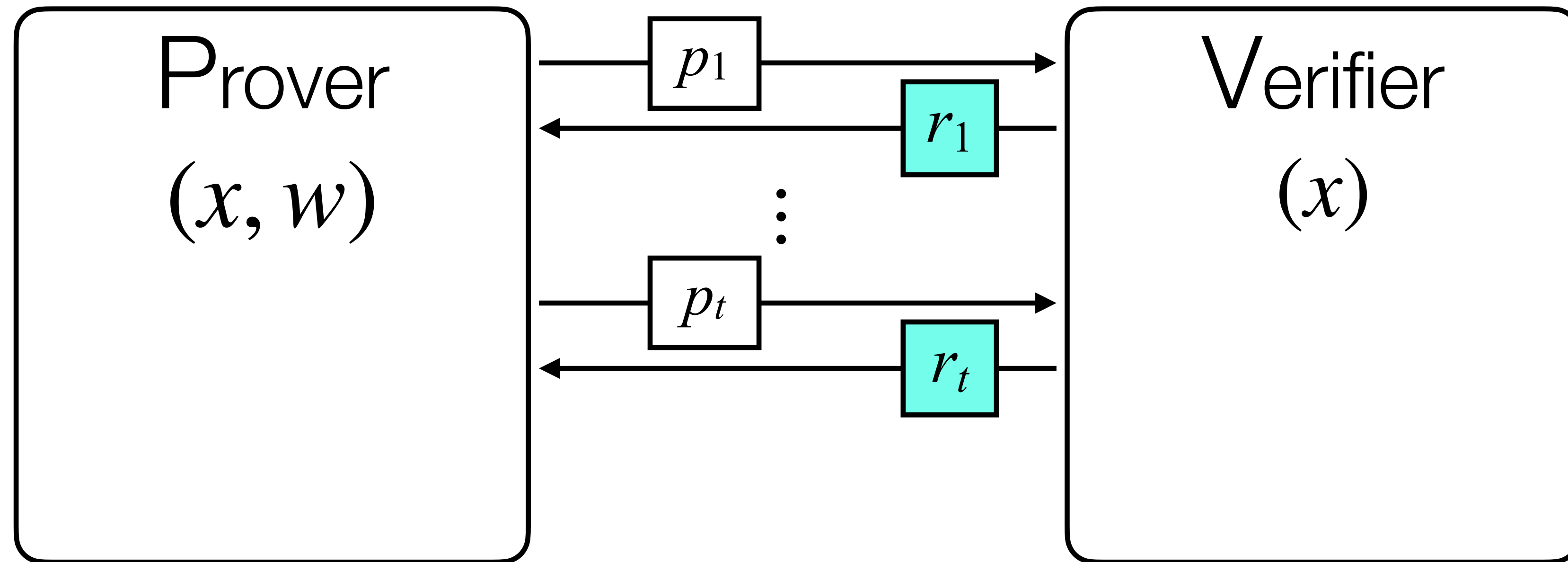
Background: Polynomial IOPs

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Prover
 (x, w)

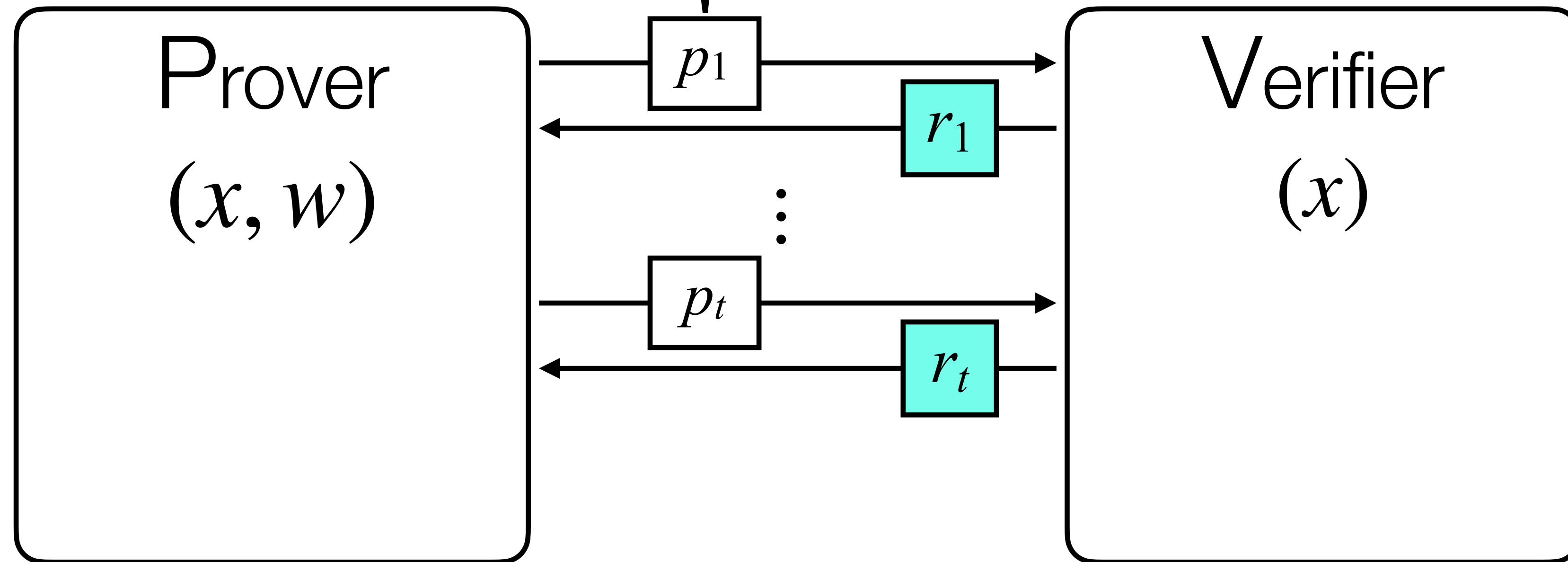
Verifier
 (x)

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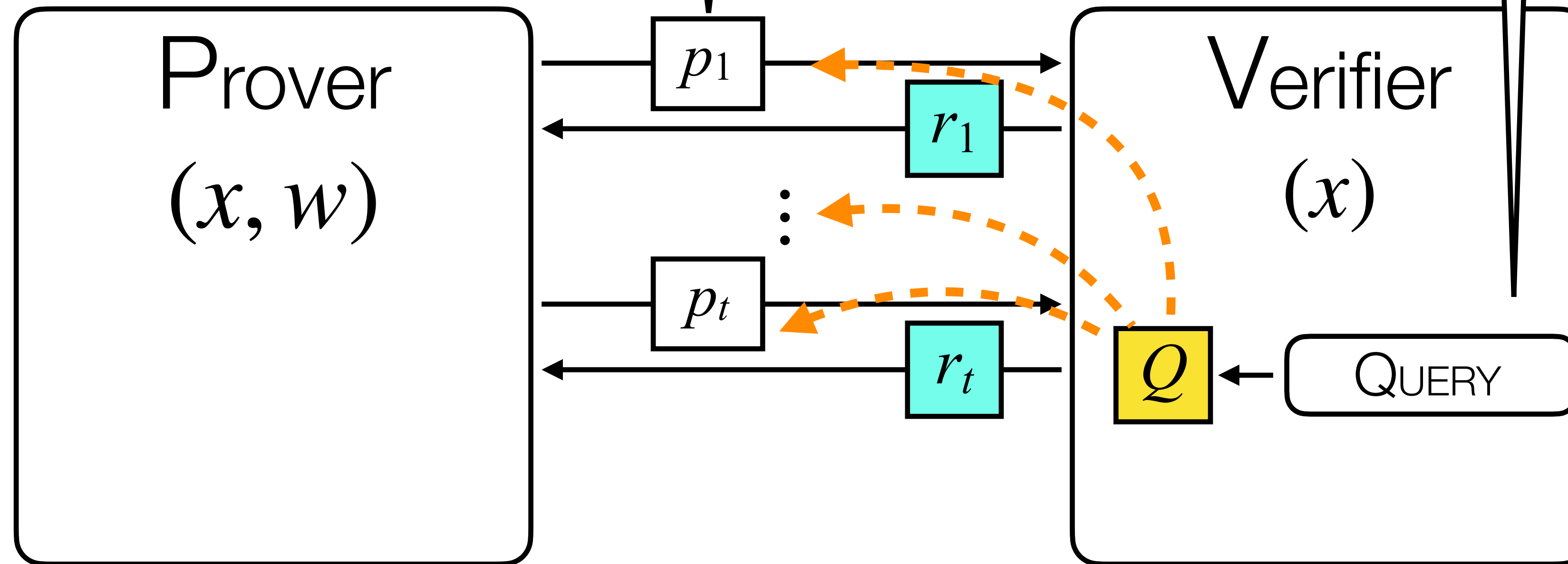
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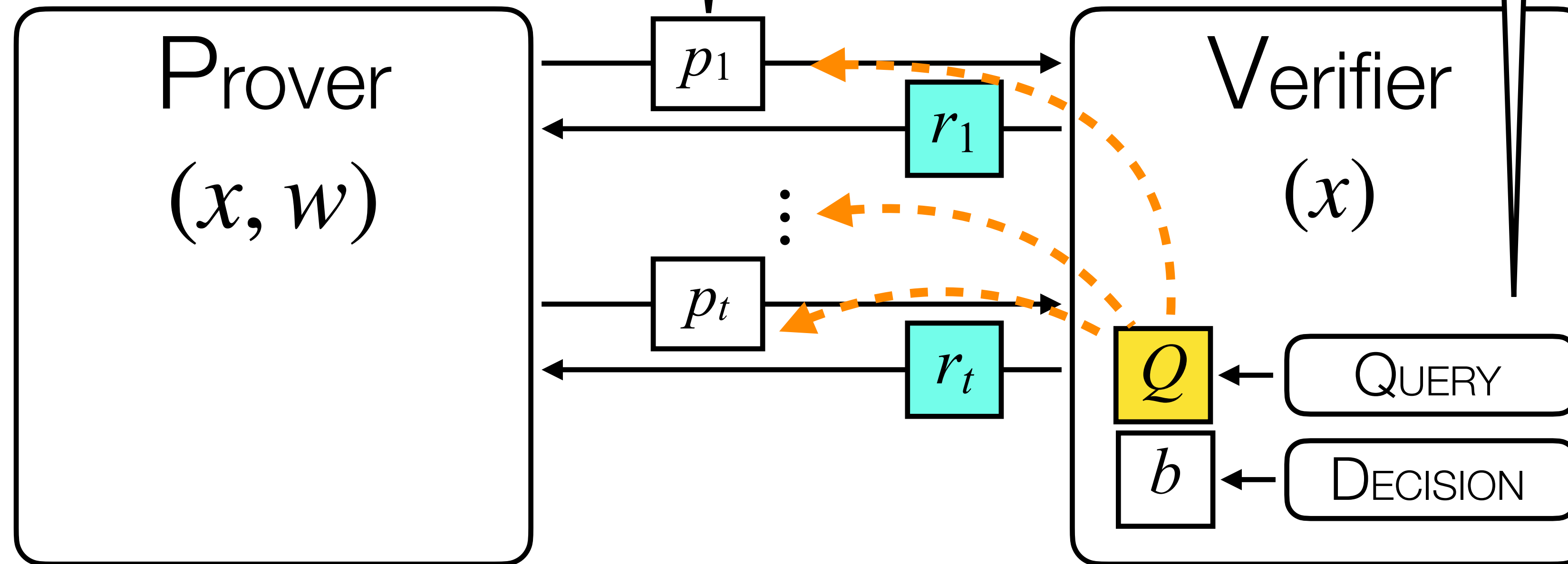
Verifier queries are
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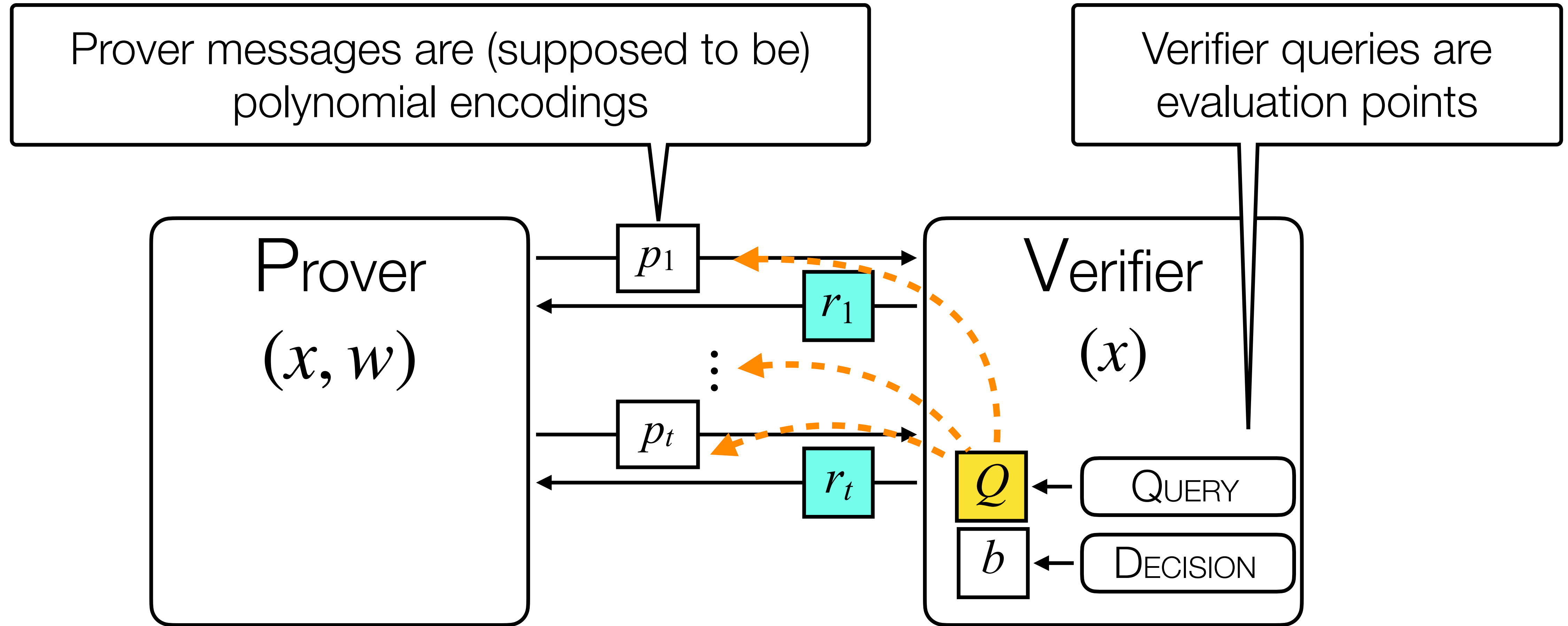
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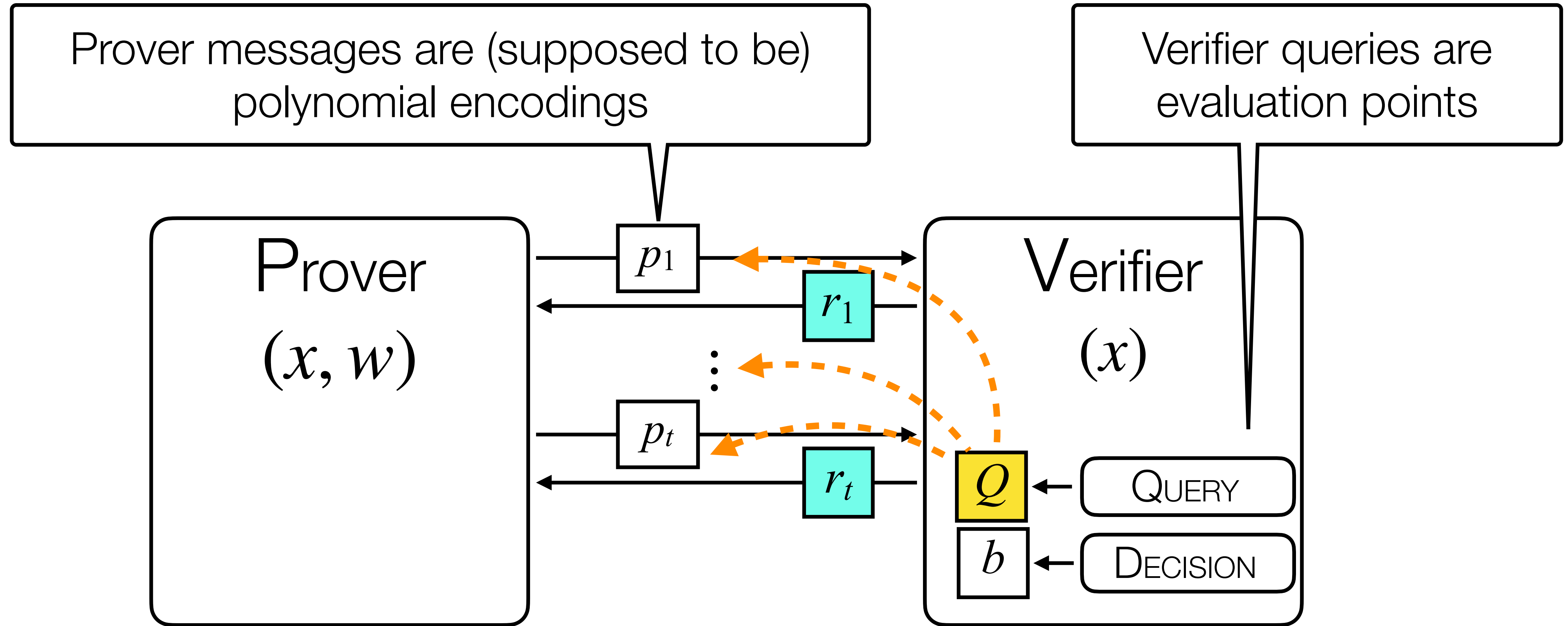


Background: Polynomial IOPs



- **Completeness:** If $F(x, w) = 1$, then \mathcal{V} accepts

Background: Polynomial IOPs



- **Completeness:** If $F(x, w) = 1$, then \mathcal{V} accepts
- **Soundness:** If $F(x, w) \neq 1$, then \mathcal{V} rejects

Background: PC schemes

Commit to a polynomial, and later on prove its correct evaluation

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SENDER

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SENDER

RECEIVER

Background: PC schemes

Commit to a polynomial, and later on prove its correct evaluation



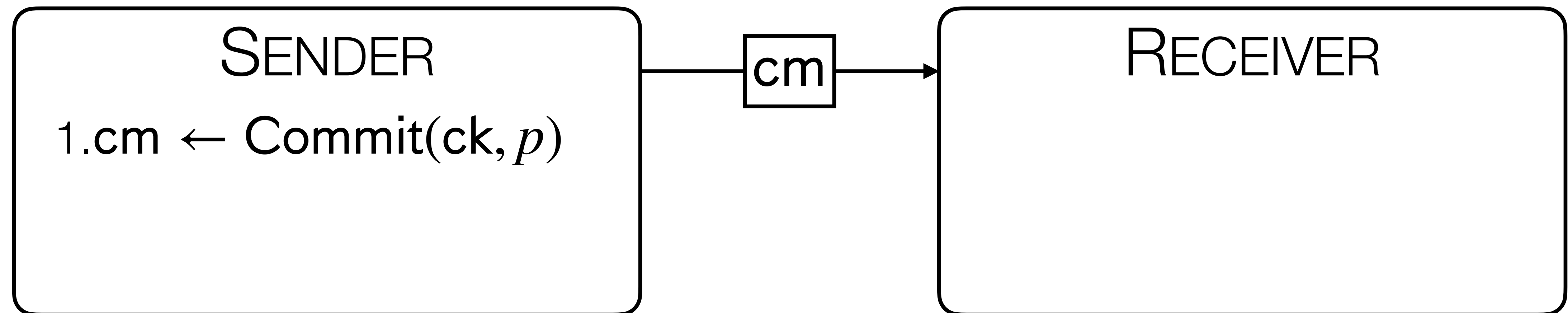
SENDER

$1.cm \leftarrow \text{Commit}(ck, p)$

RECEIVER

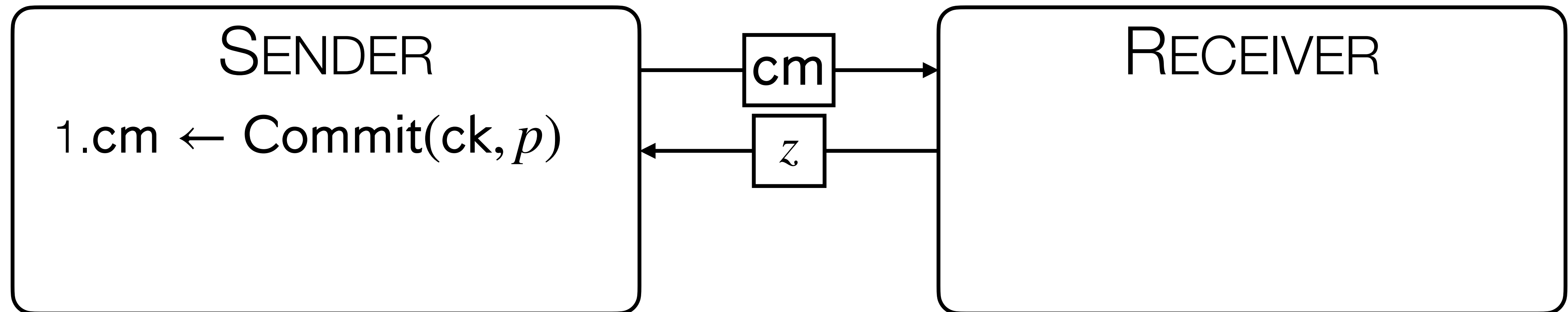
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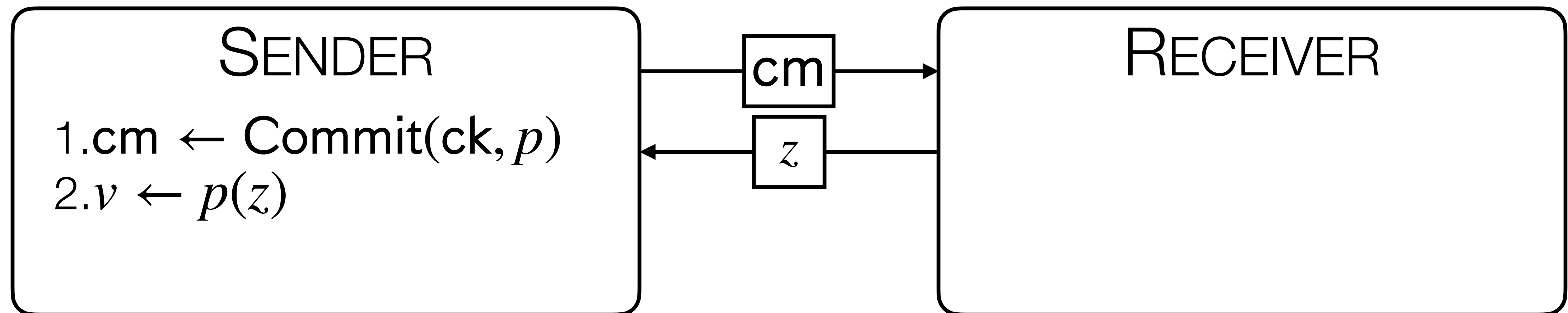
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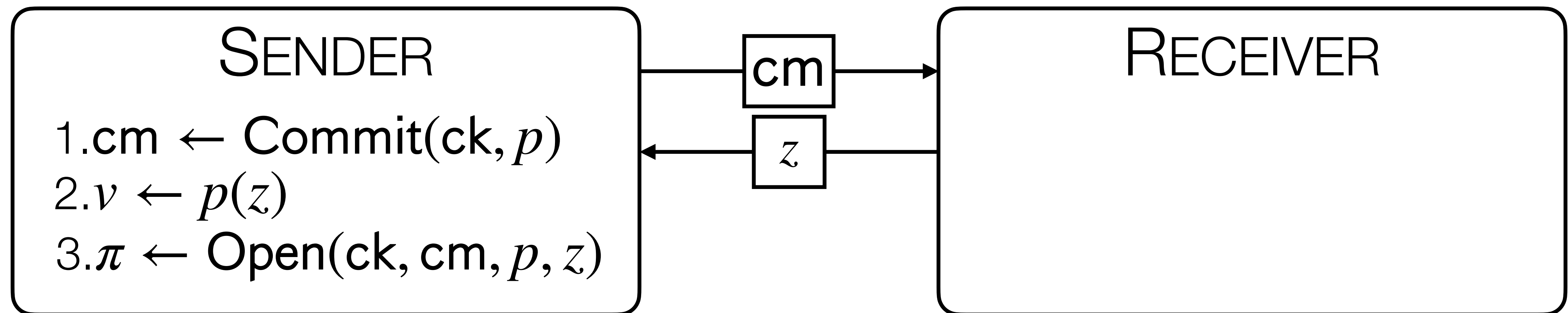
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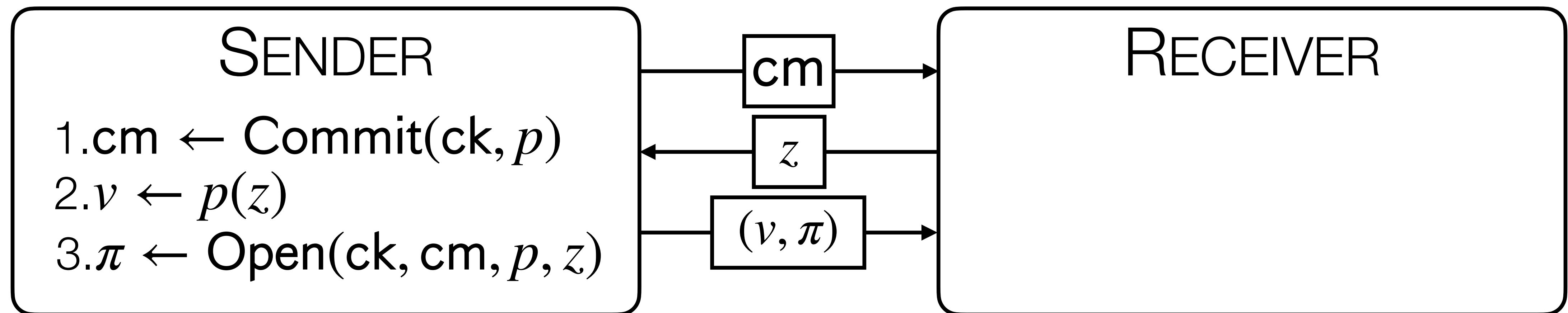
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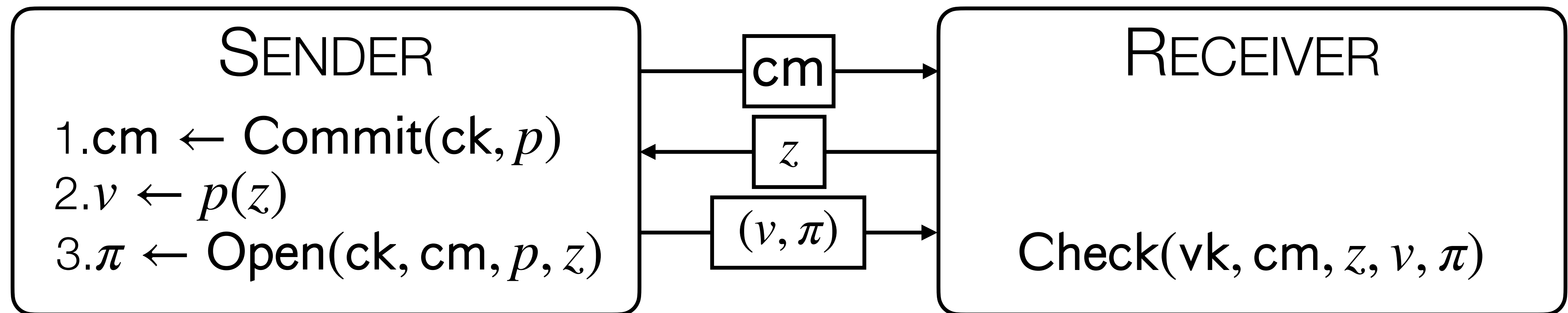
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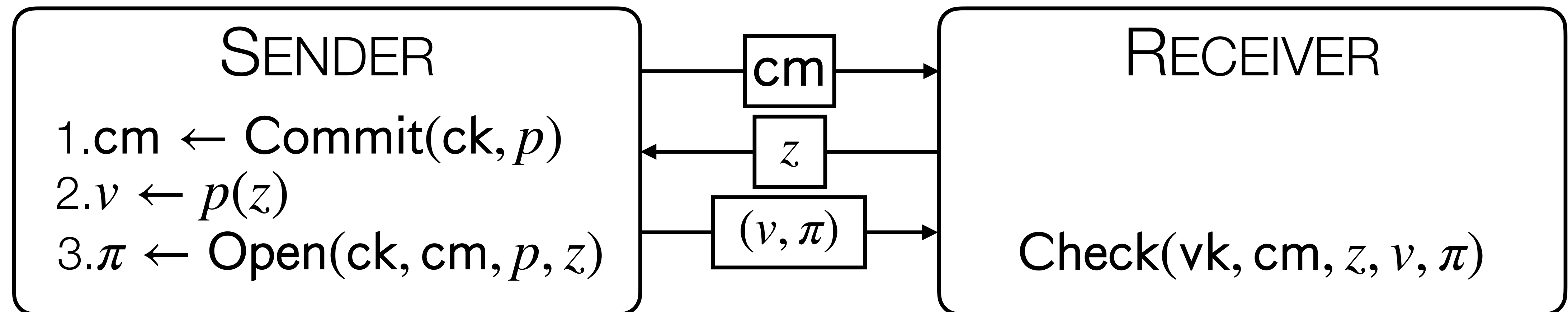
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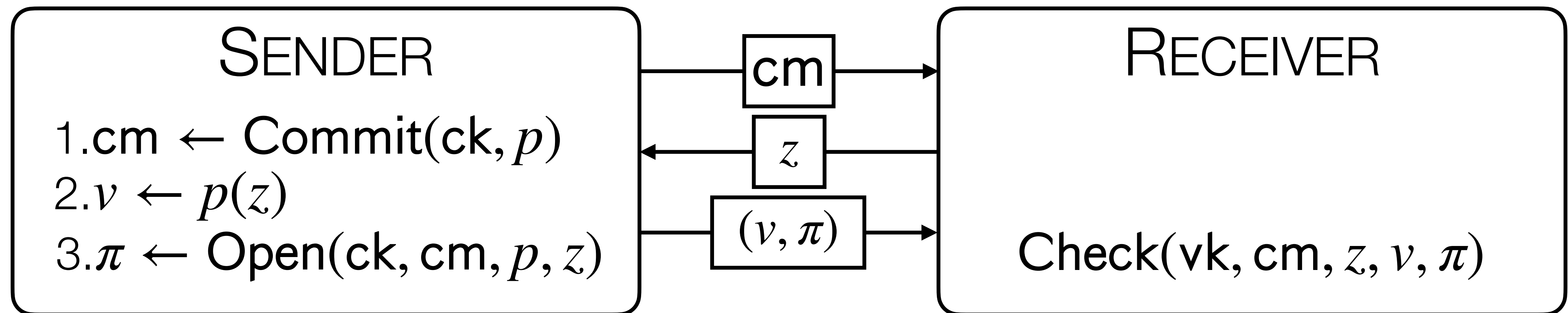
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- **Completeness:** Whenever $p(z) = v$, the Receiver accepts.

Background: PC schemes

Commit to a polynomial, and later on prove its correct evaluation



- **Completeness:** Whenever $p(z) = v$, the Receiver accepts.
- **Extractability:** Whenever the Receiver accepts, the Sender's commitment \mathbf{cm} “contains” a polynomial p satisfying $p(z) = v$.

PIOPs + PC Schemes \rightarrow SNARK

[CHMMVW20, BFS20]

PIOPs + PC Schemes \rightarrow SNARK

[CHMMVW20, BFS20]

SETUP($1^\lambda, \mathfrak{i}$)

max degree n



PIOP(\mathfrak{i})

(ck, vk)



PC.SETUP(n)

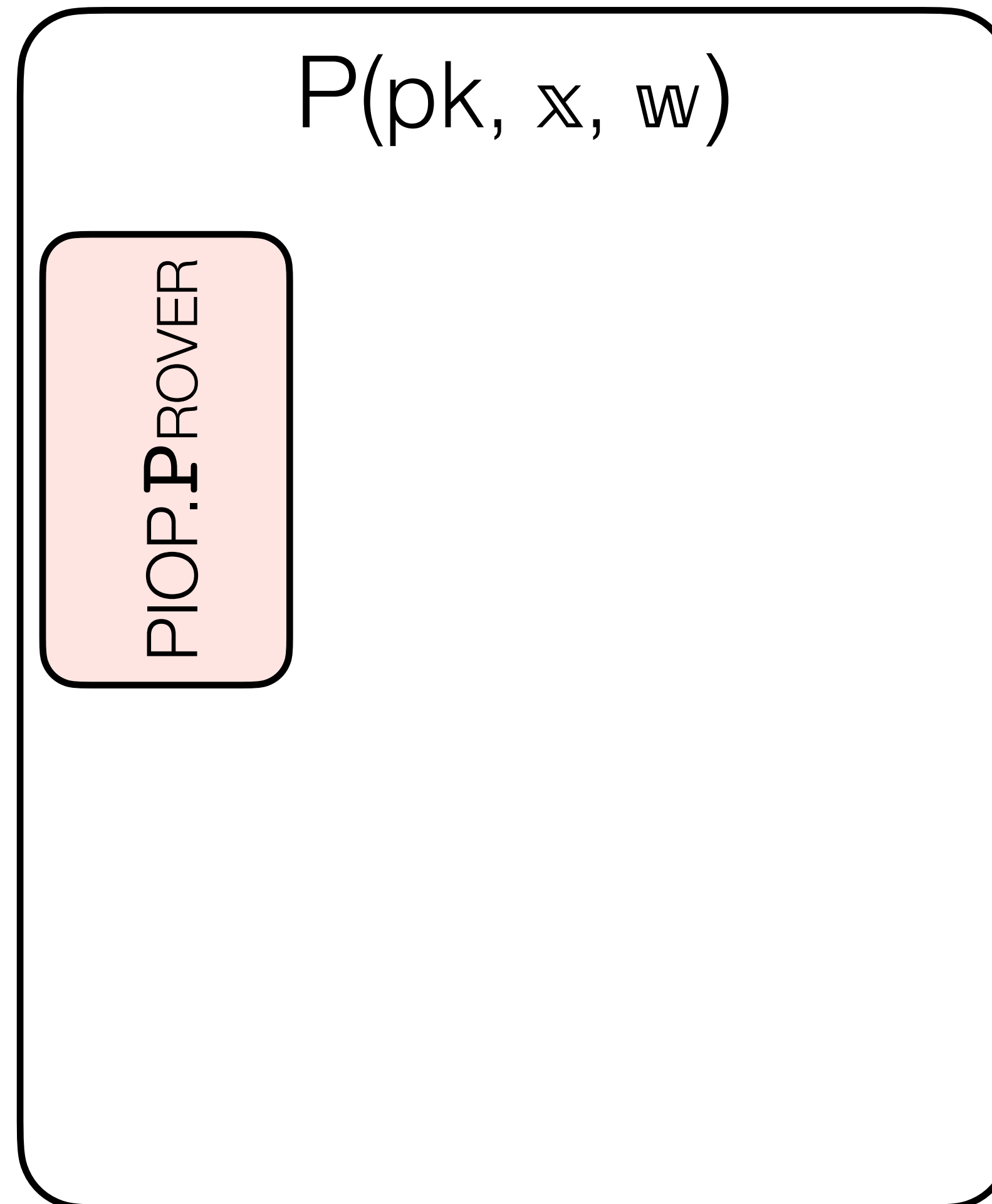
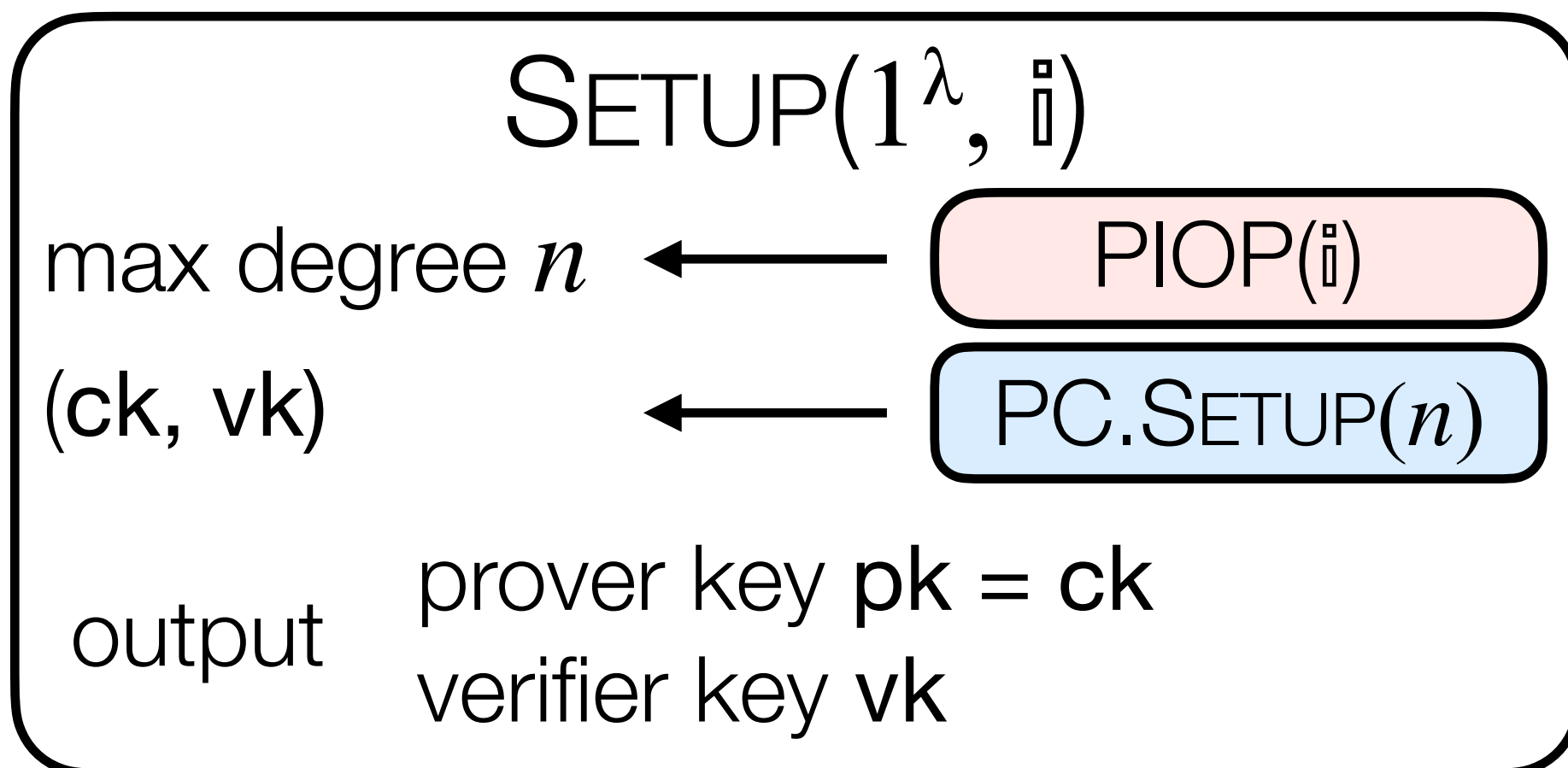
output

prover key $pk = ck$

verifier key vk

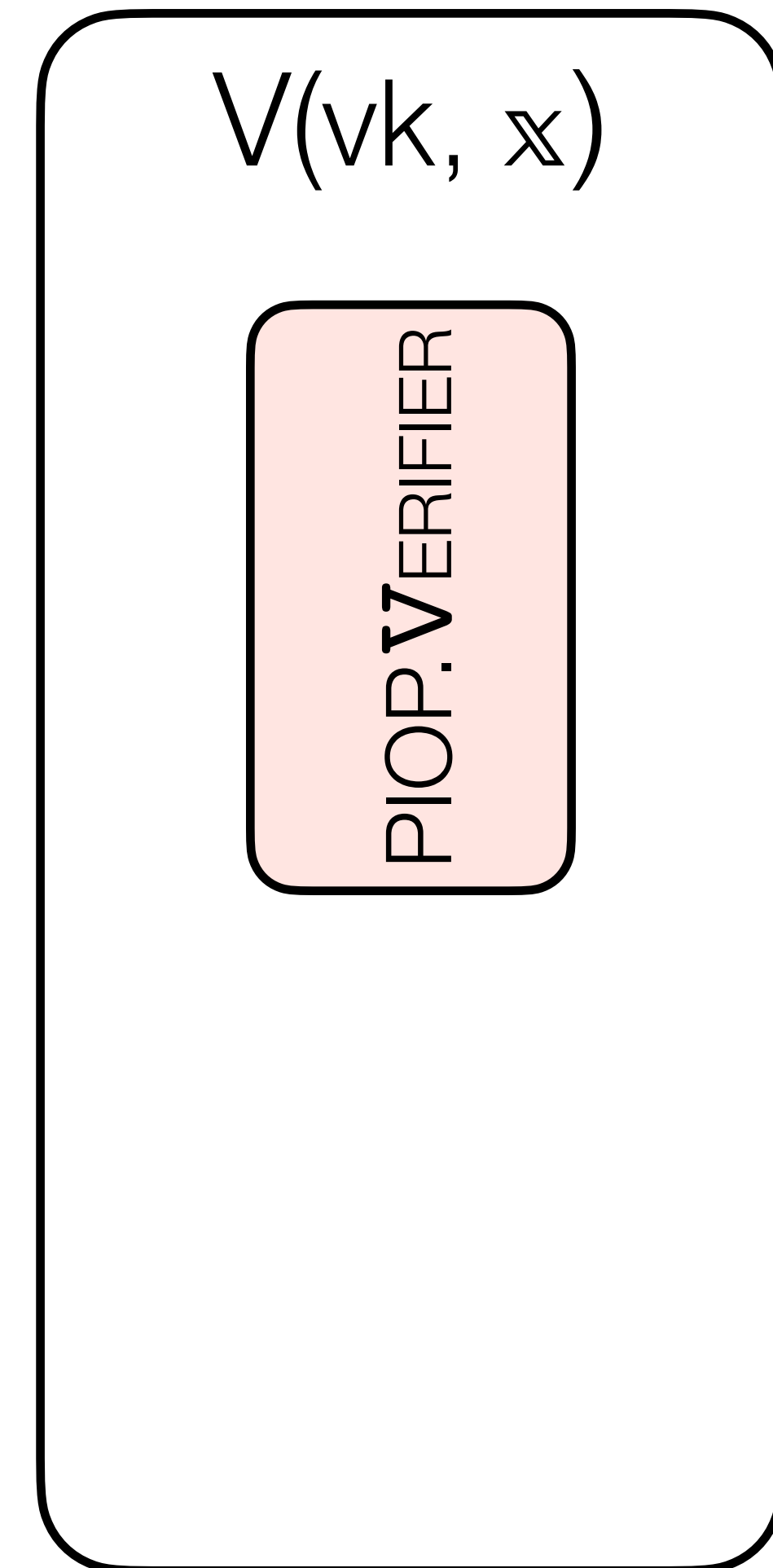
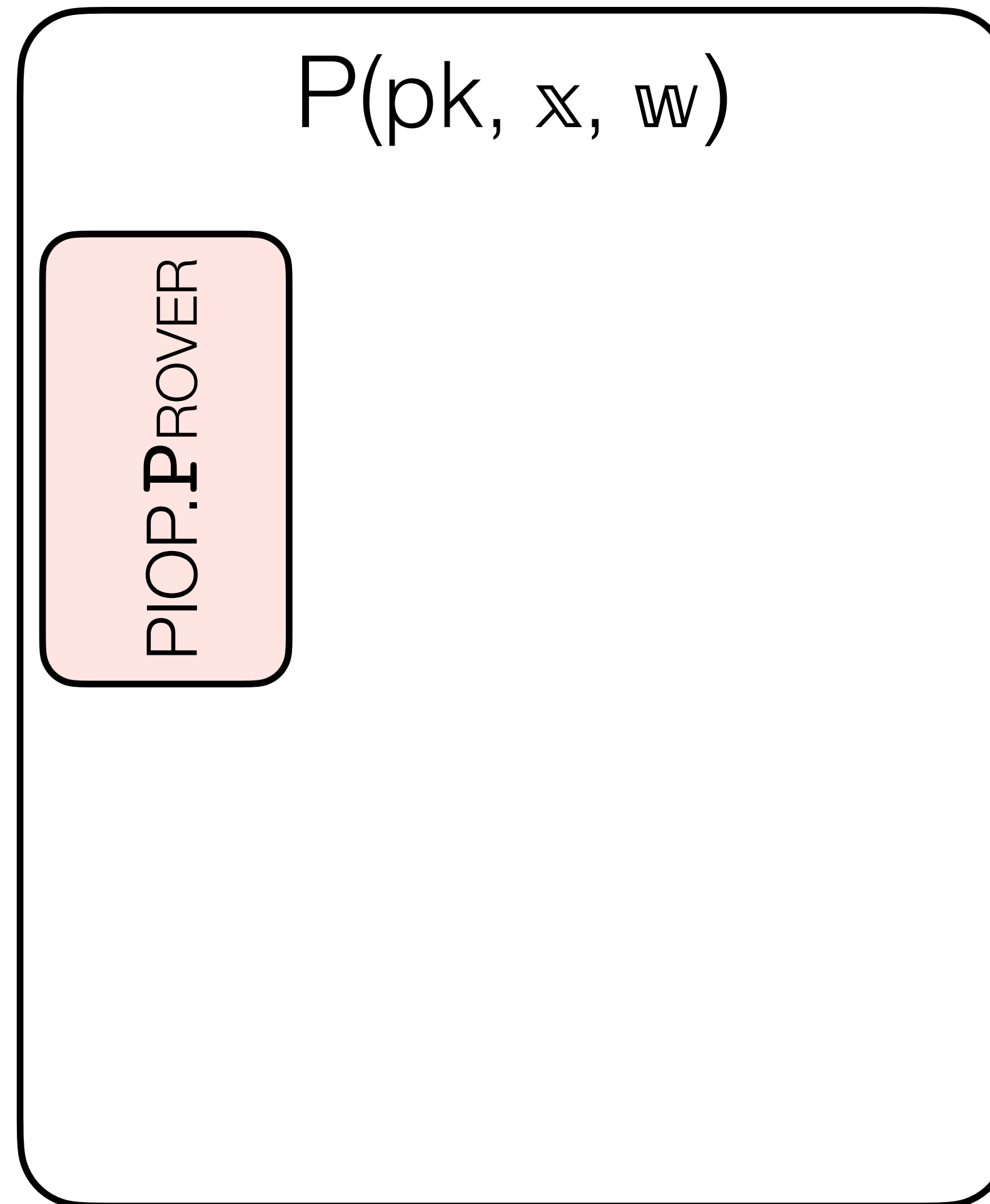
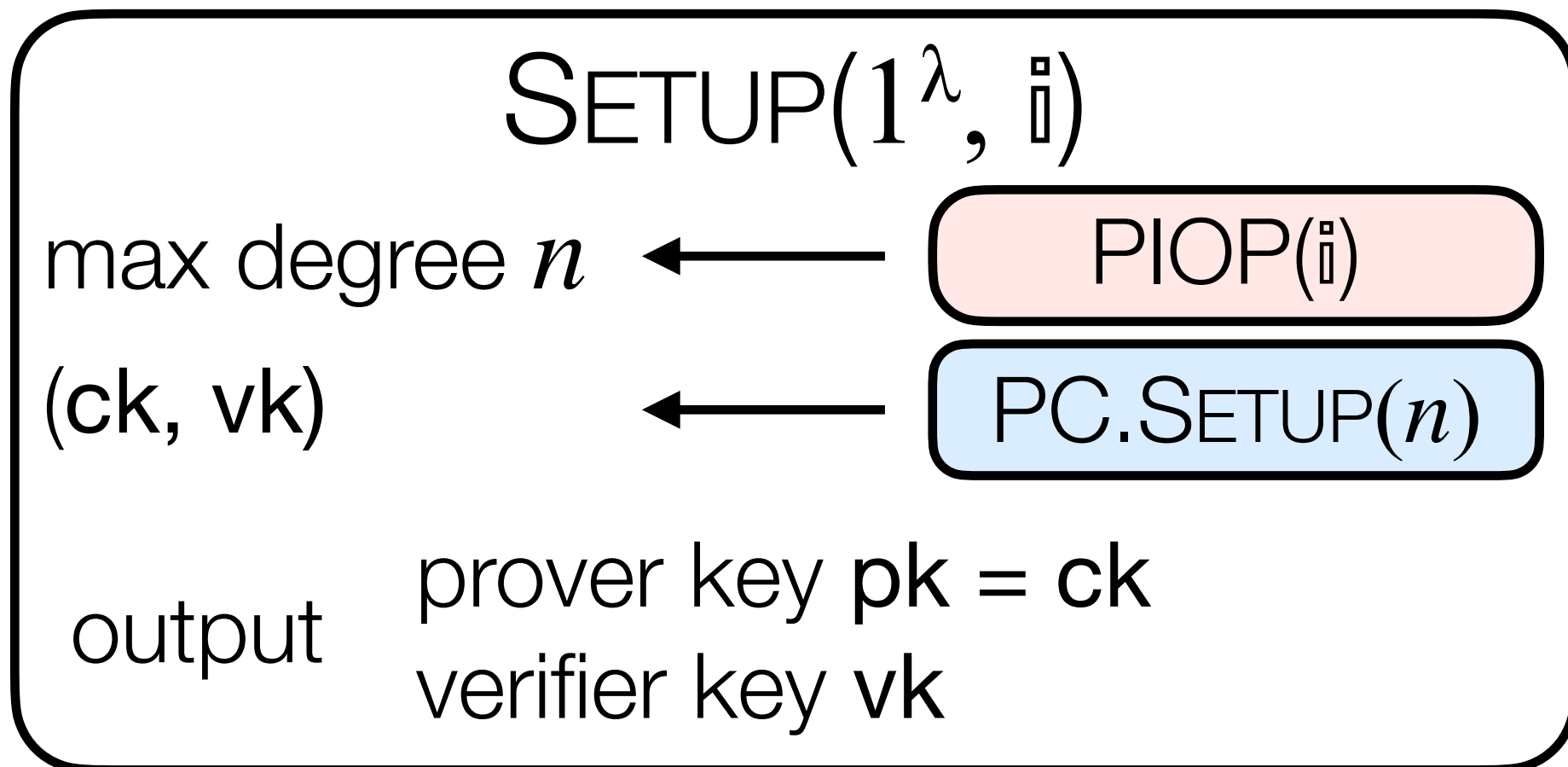
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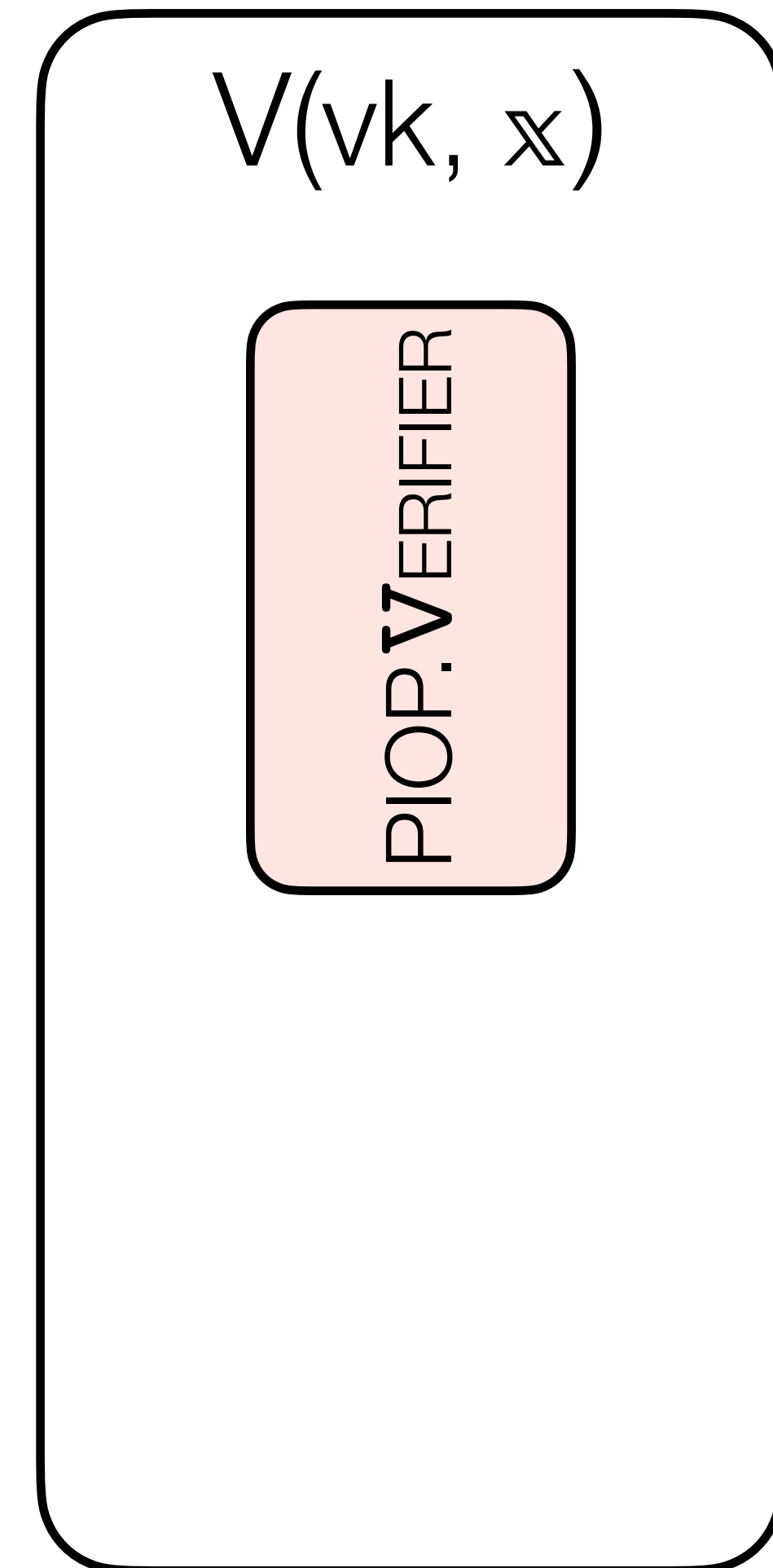
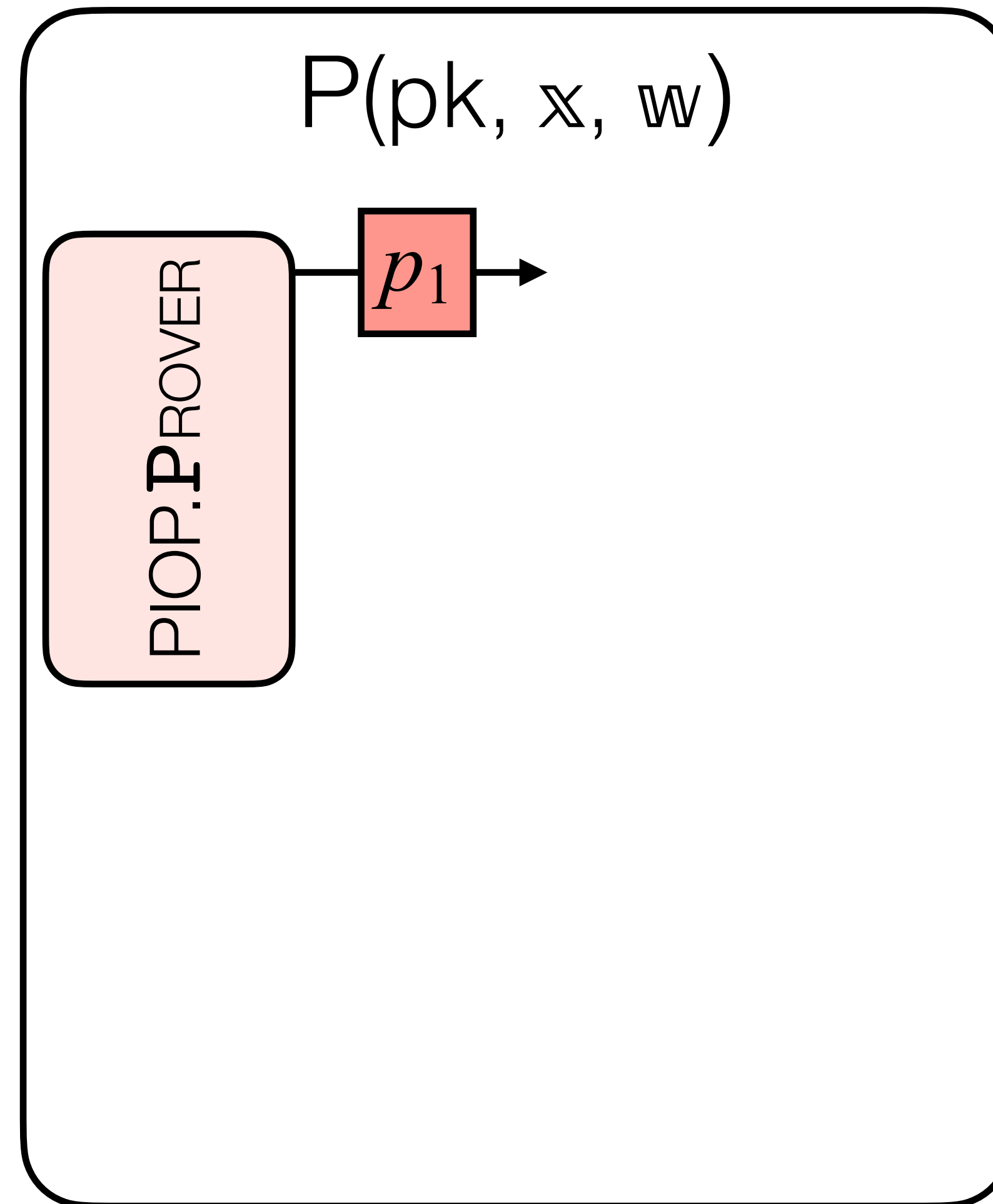
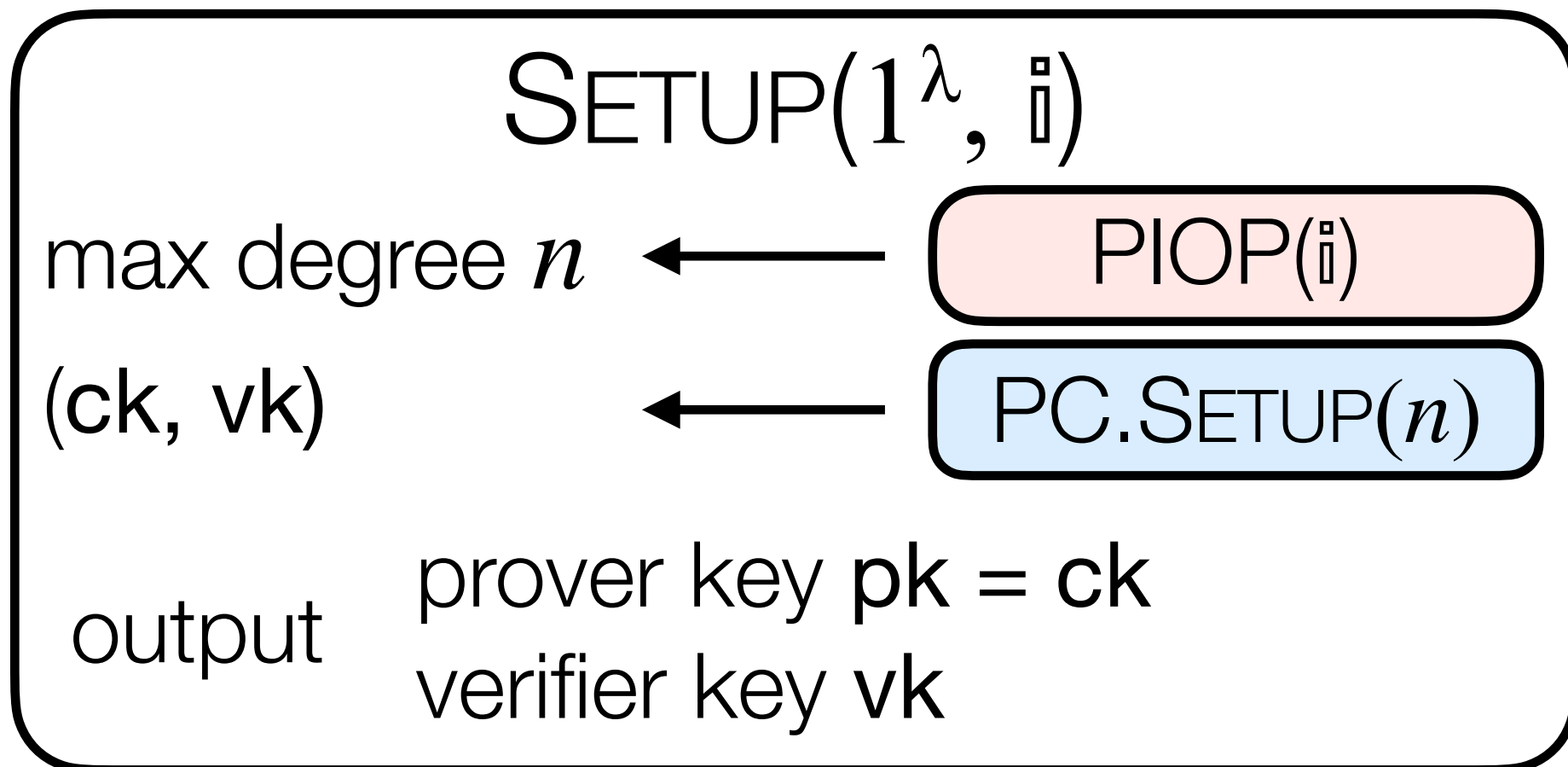
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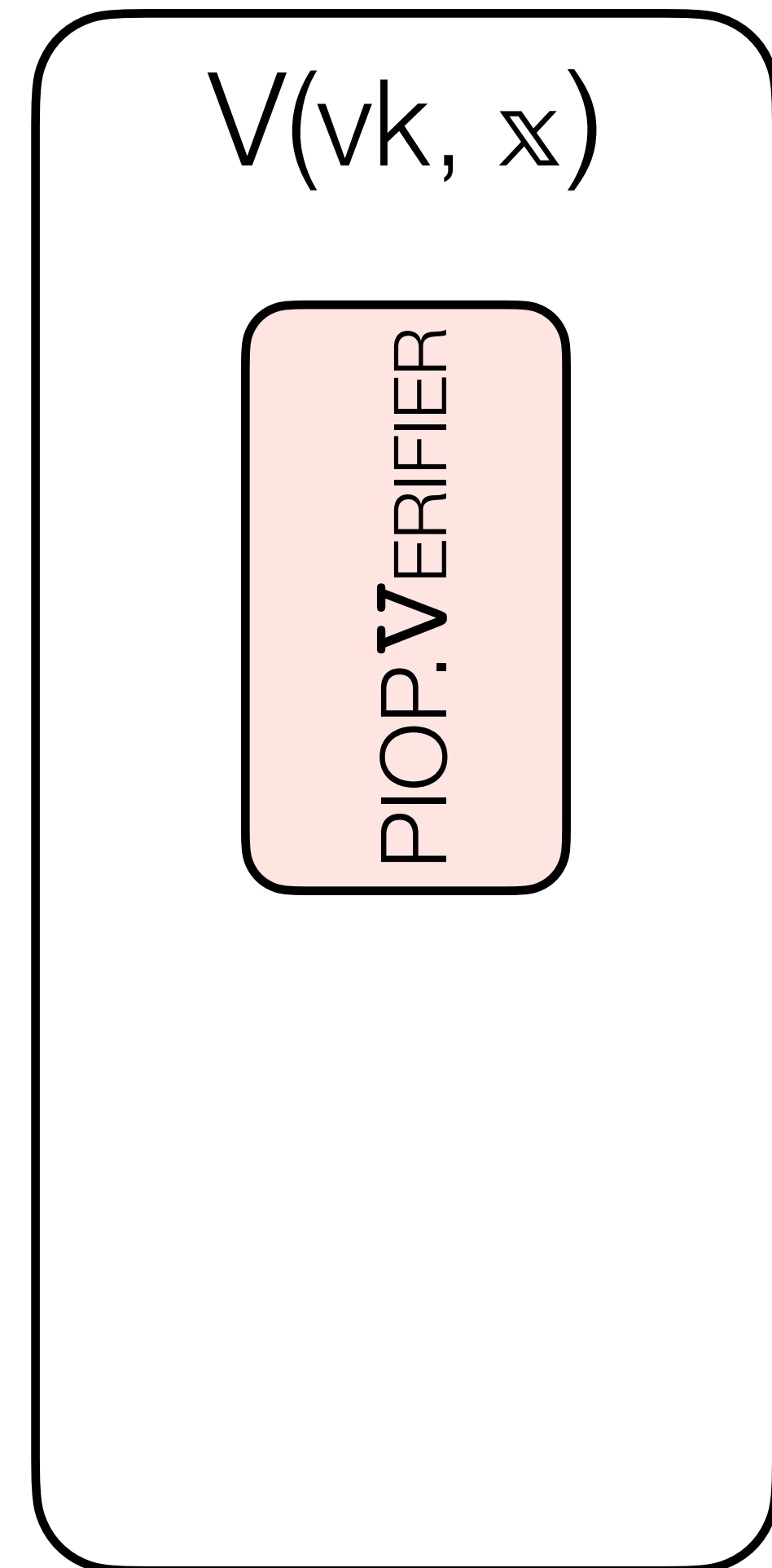
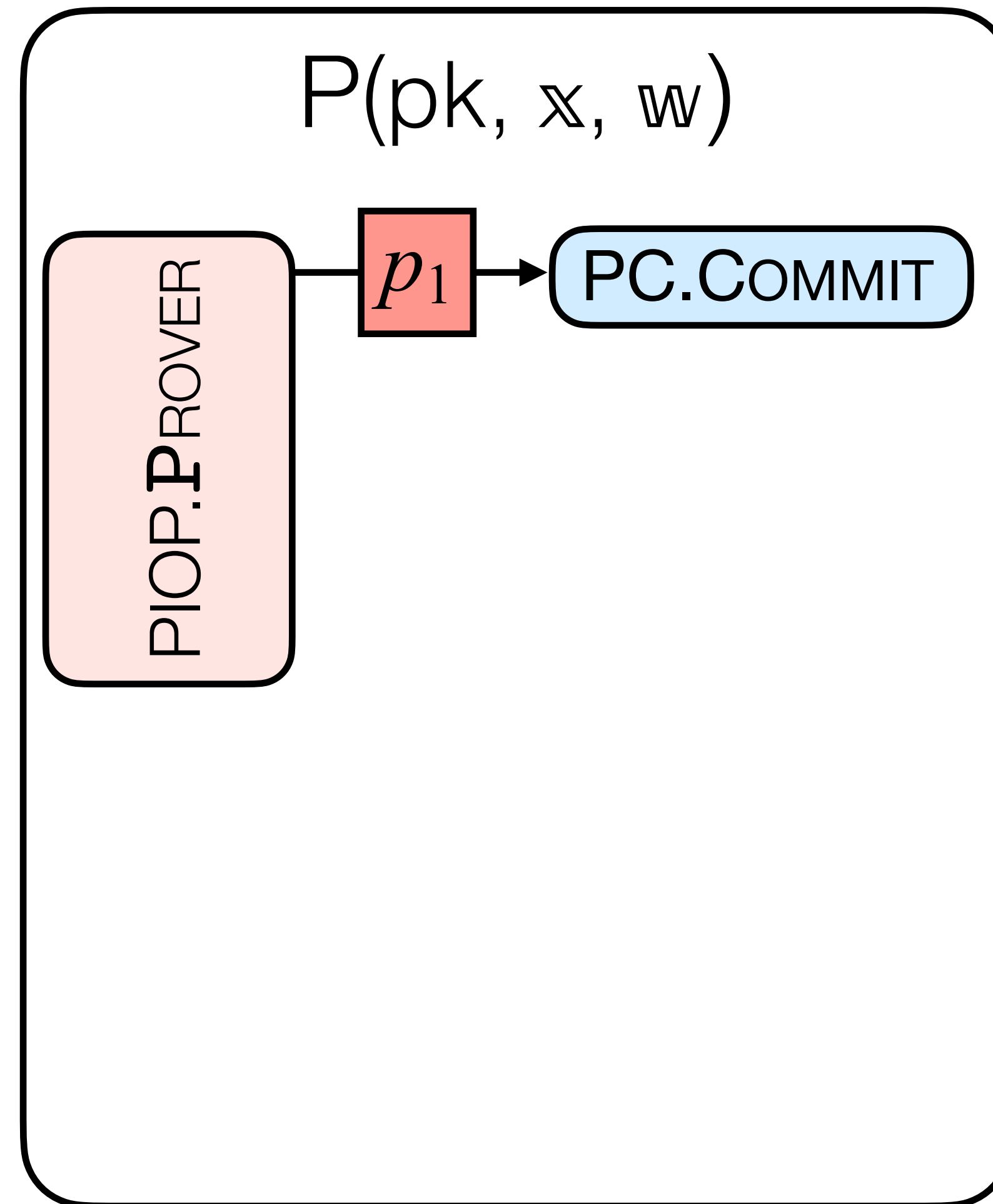
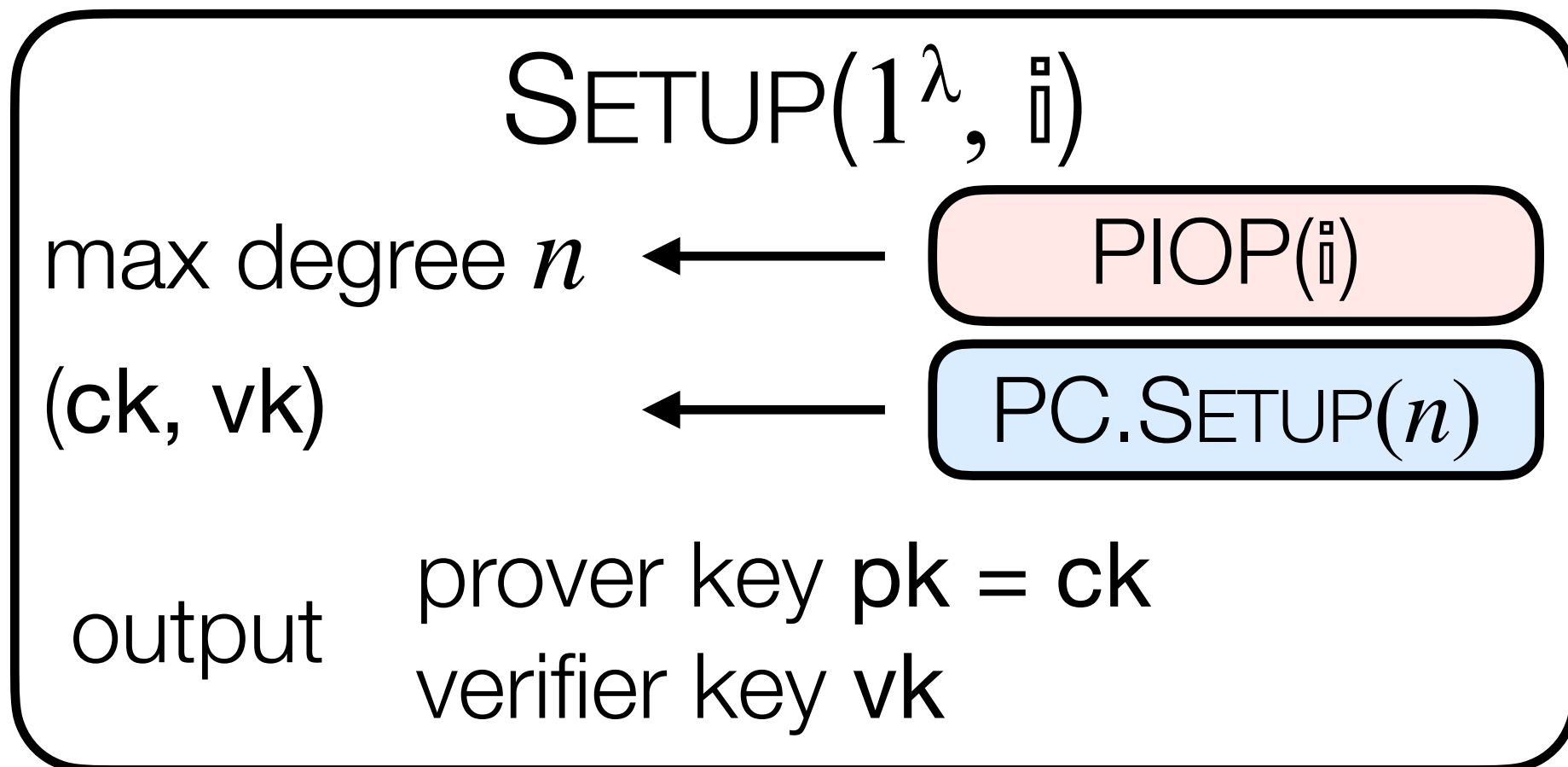
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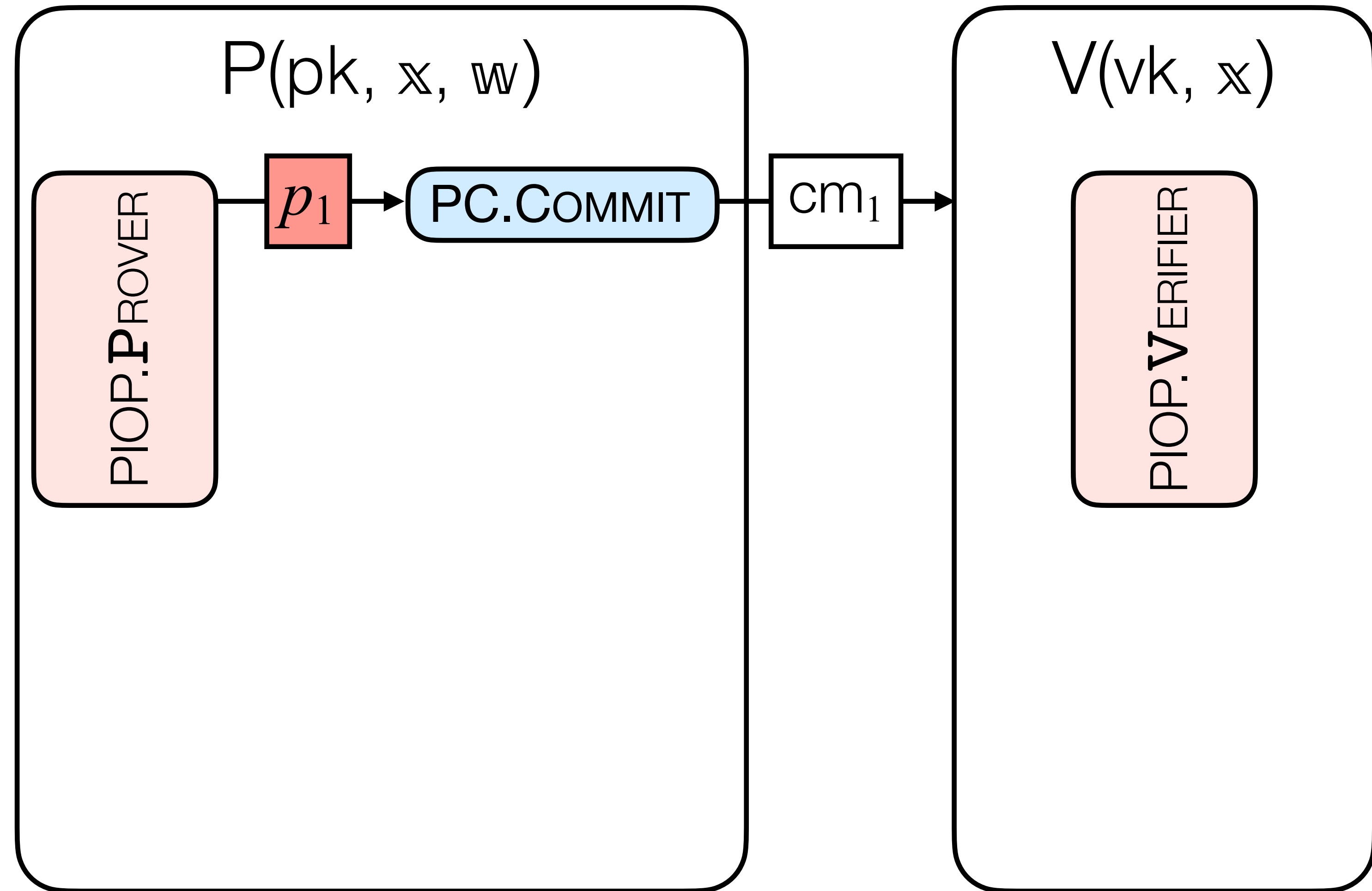
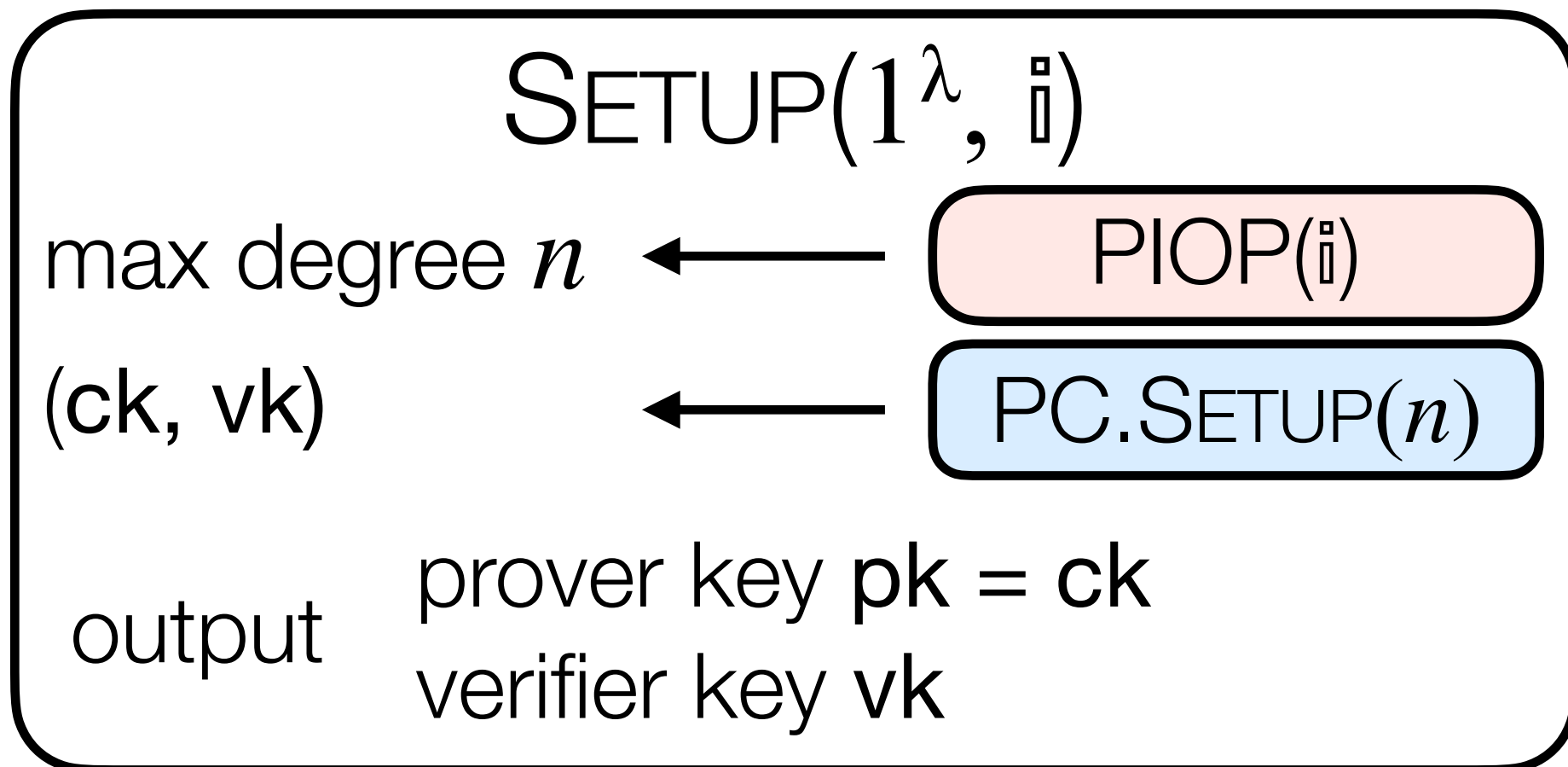
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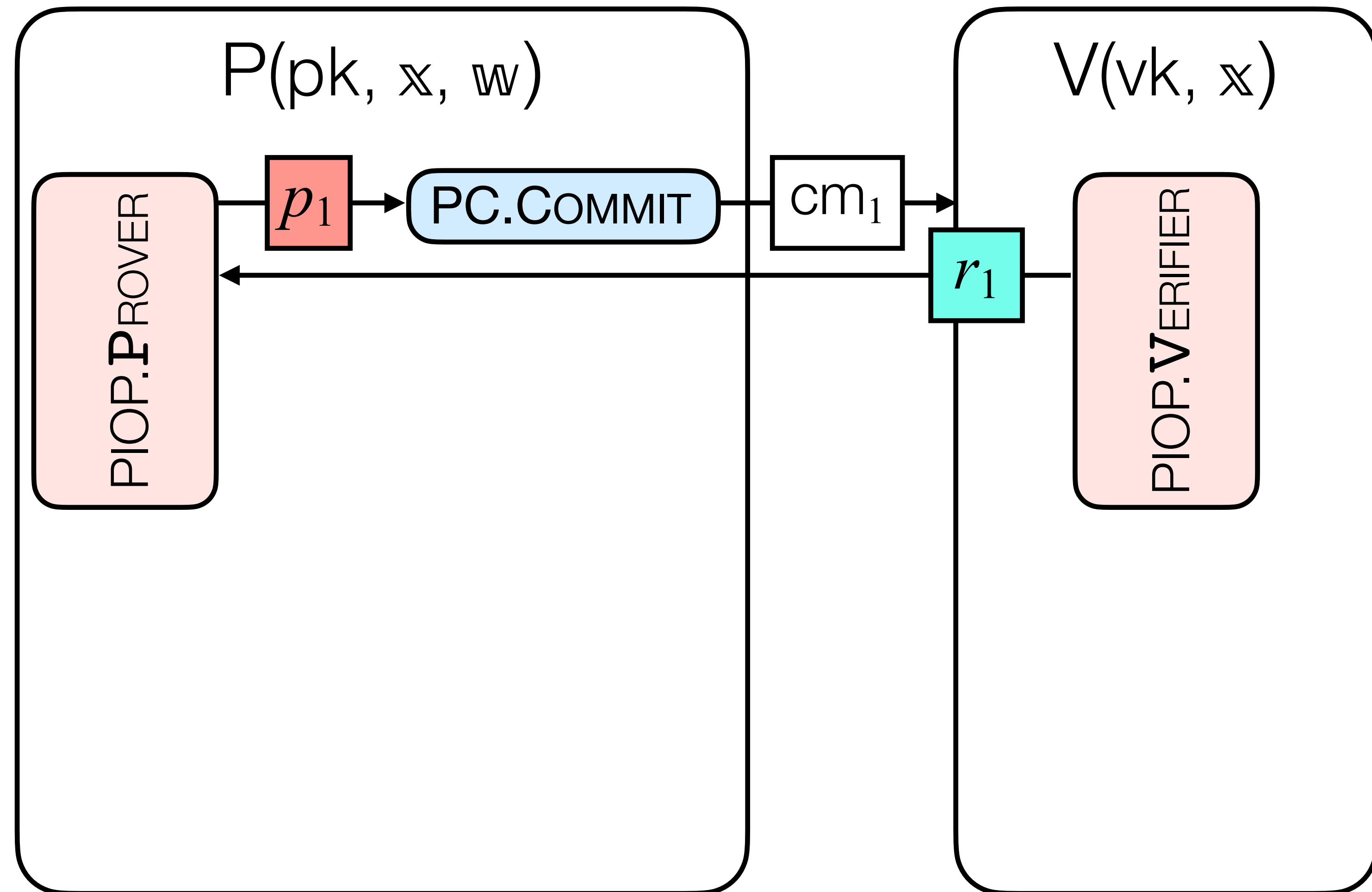
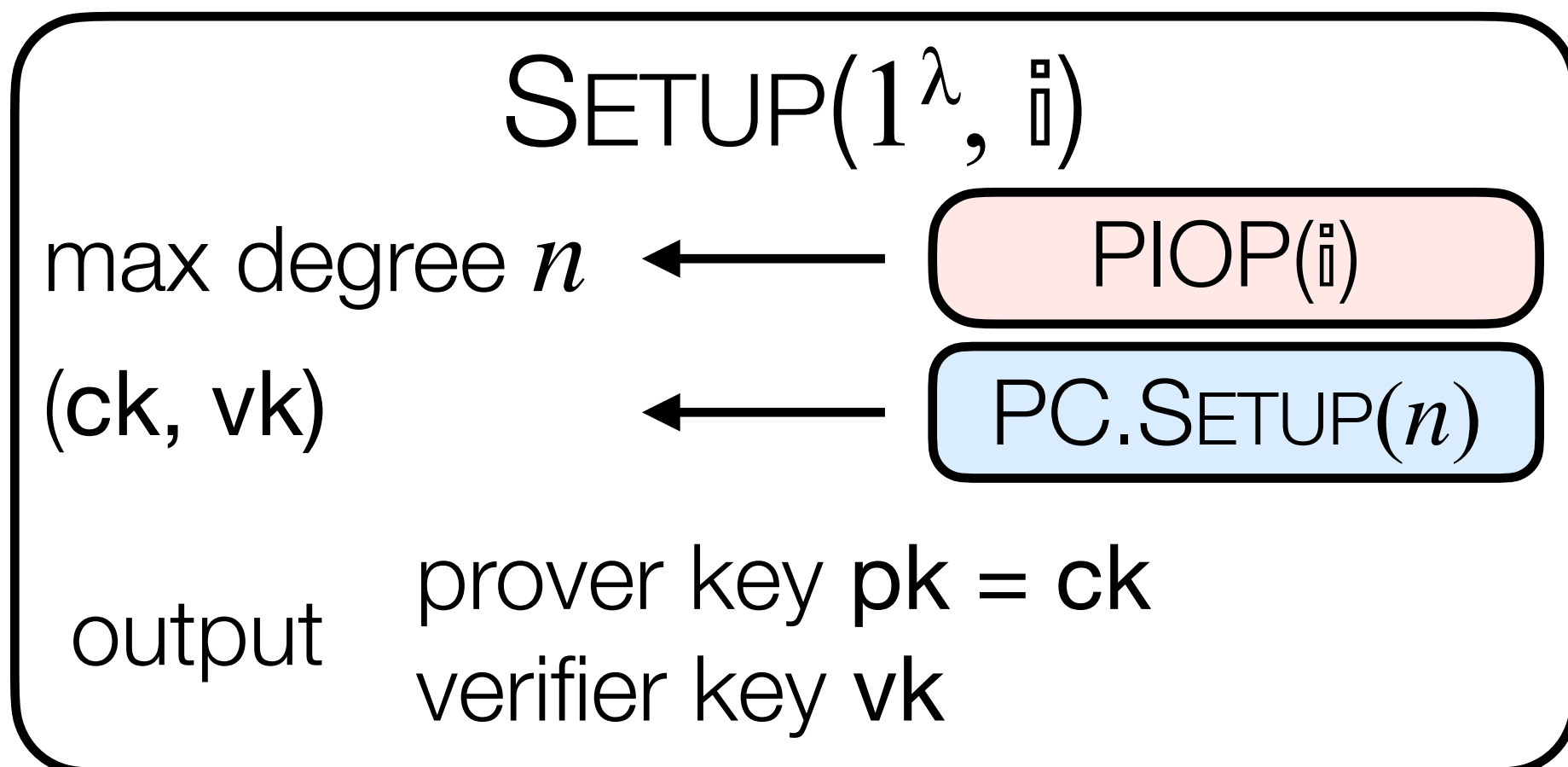
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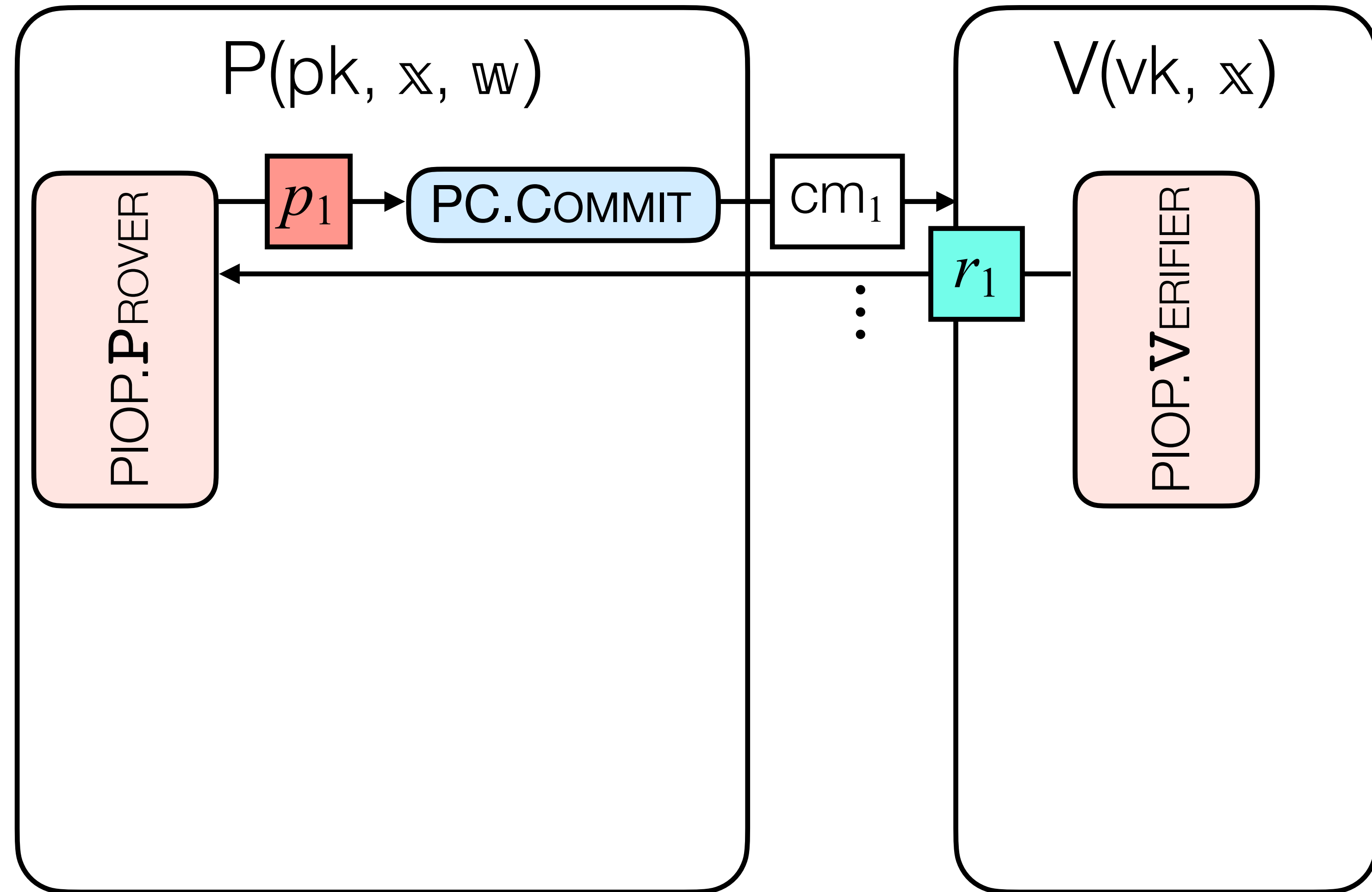
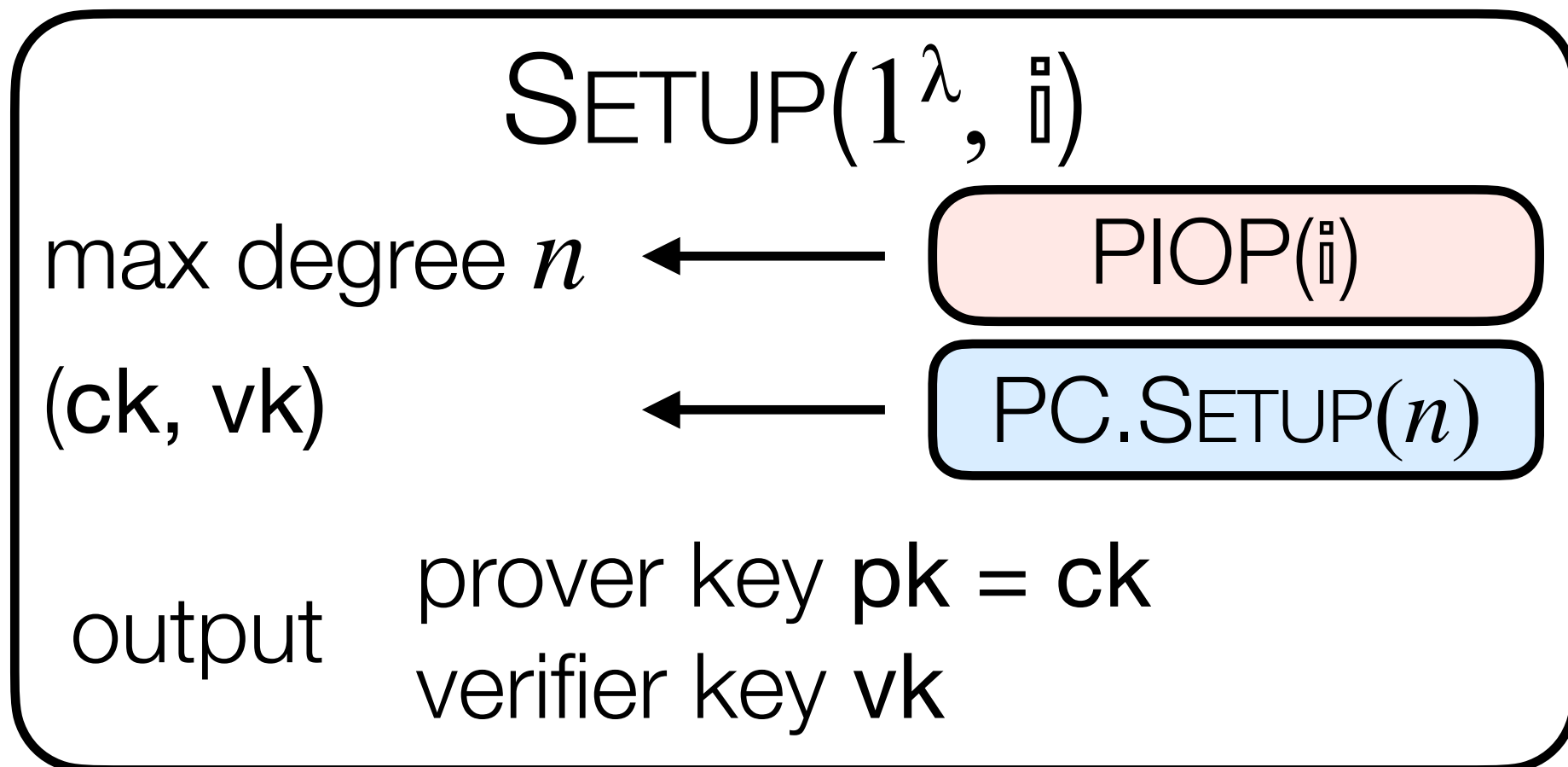
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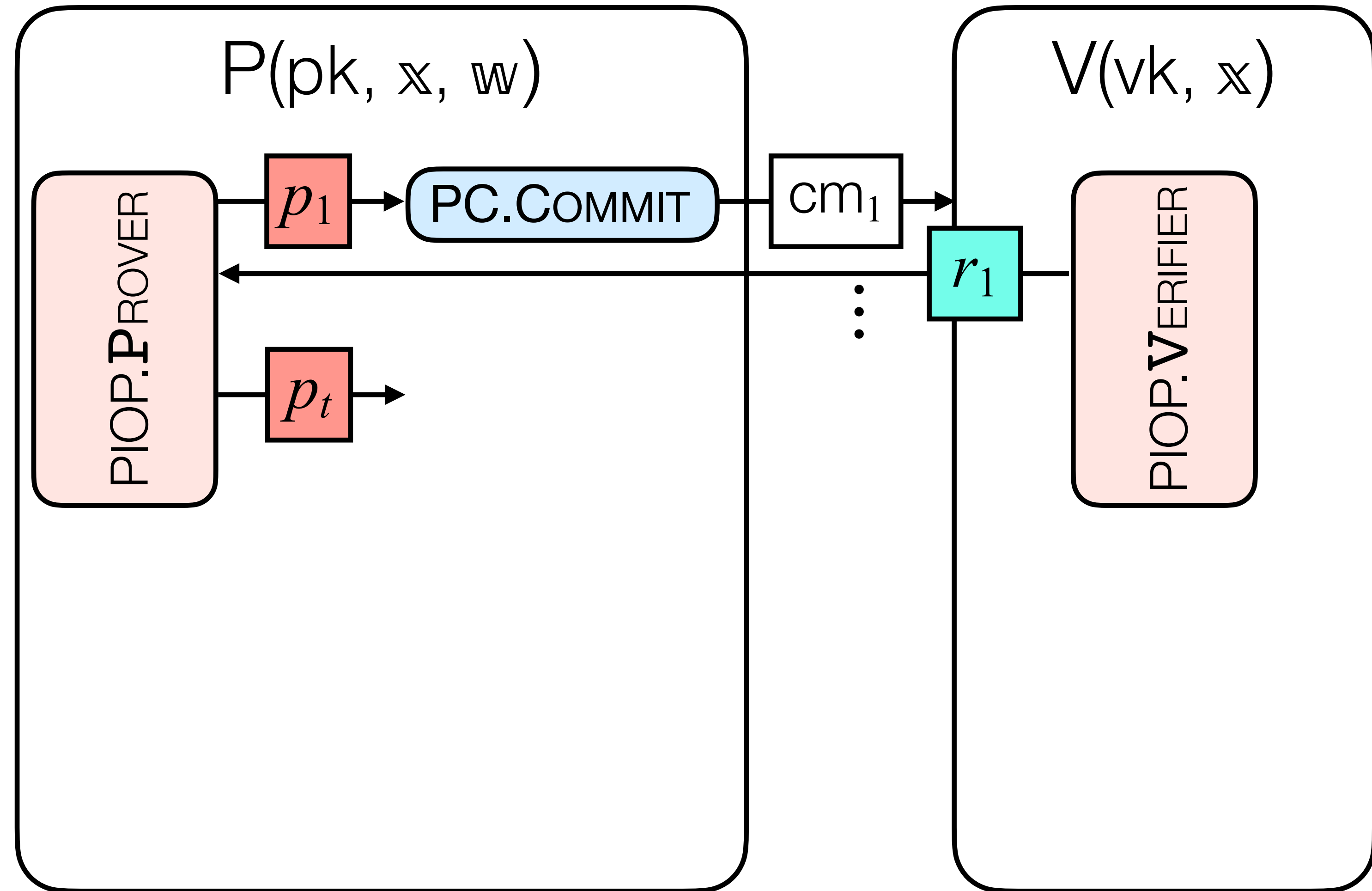
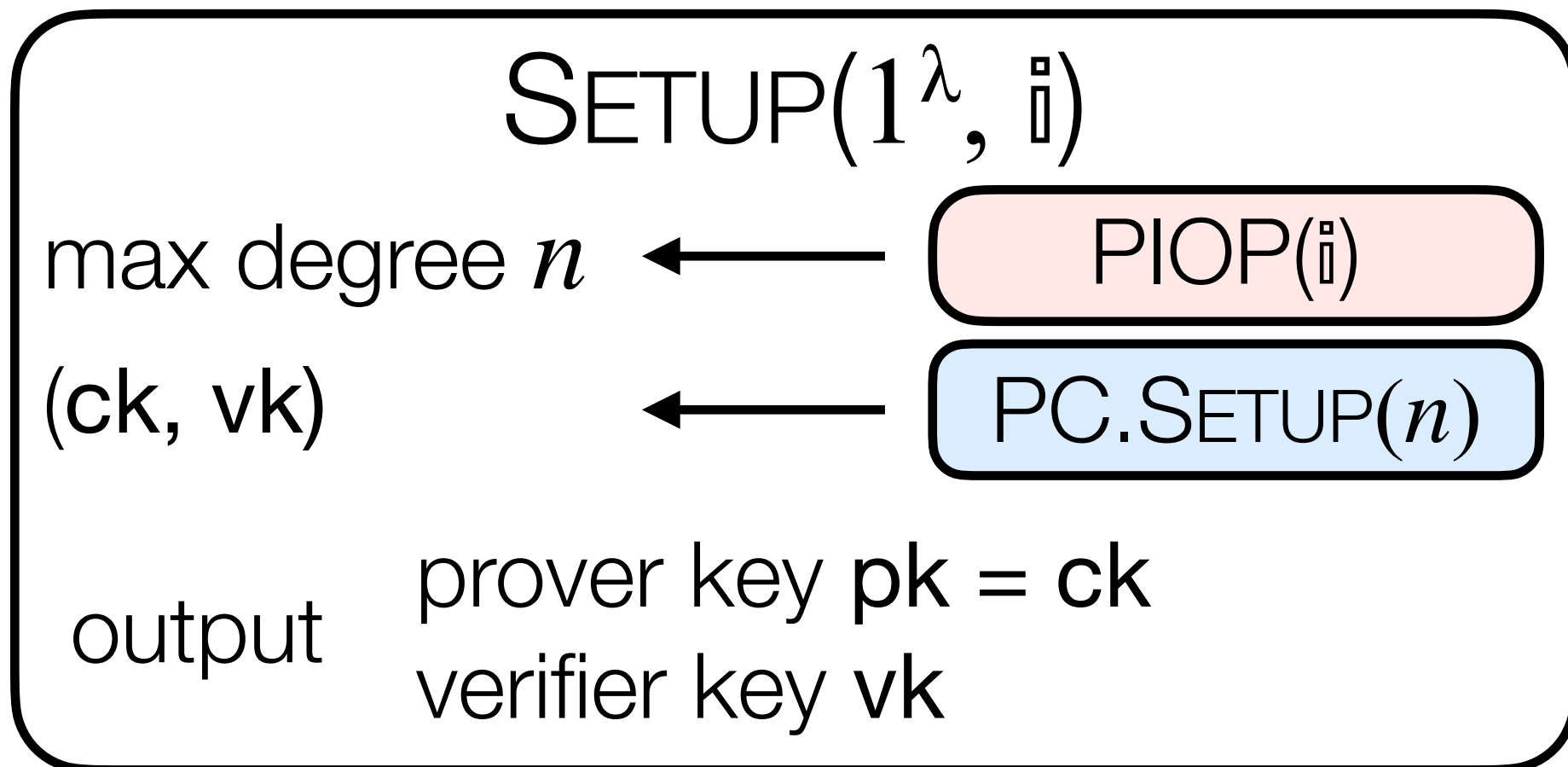
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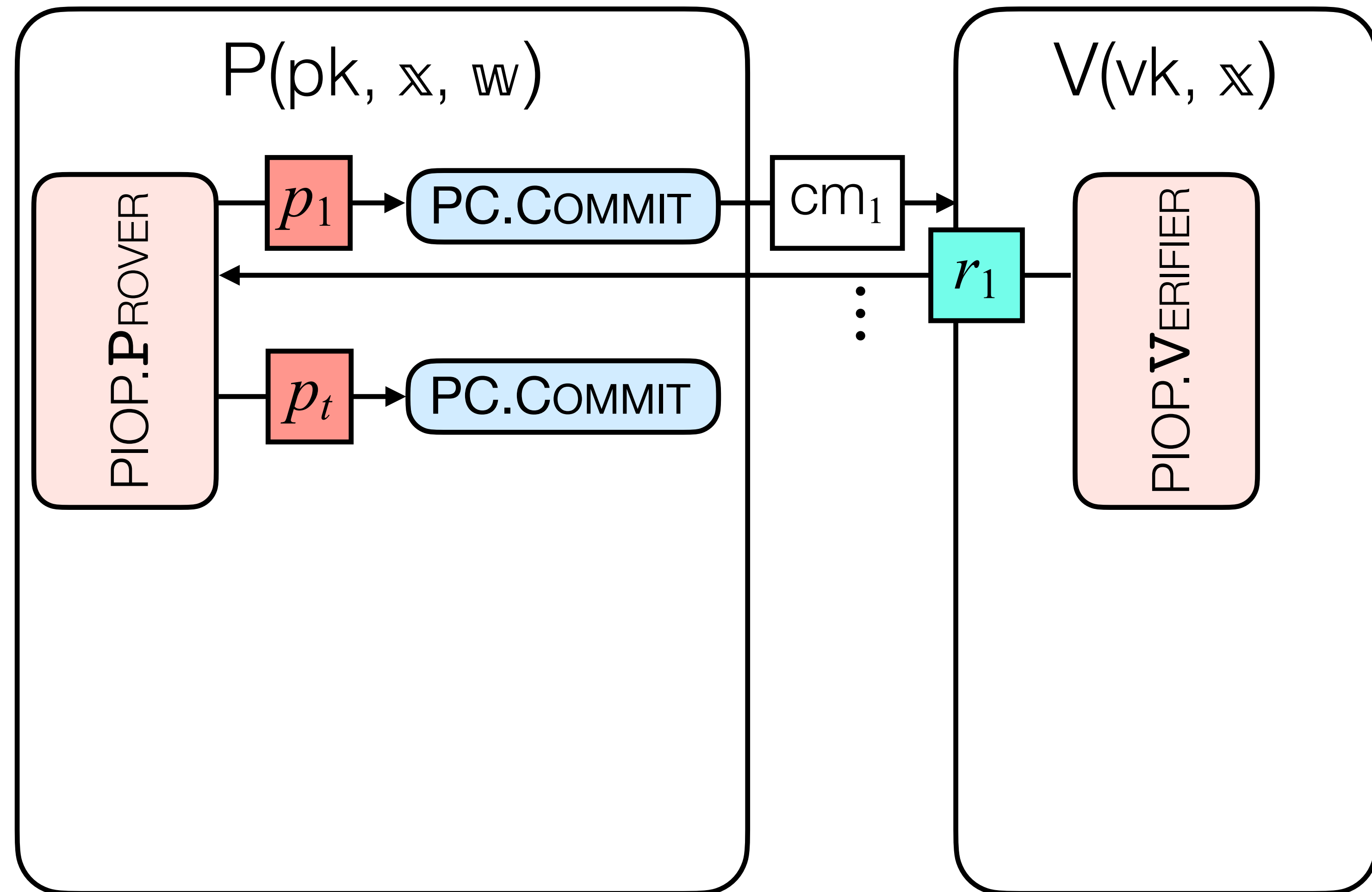
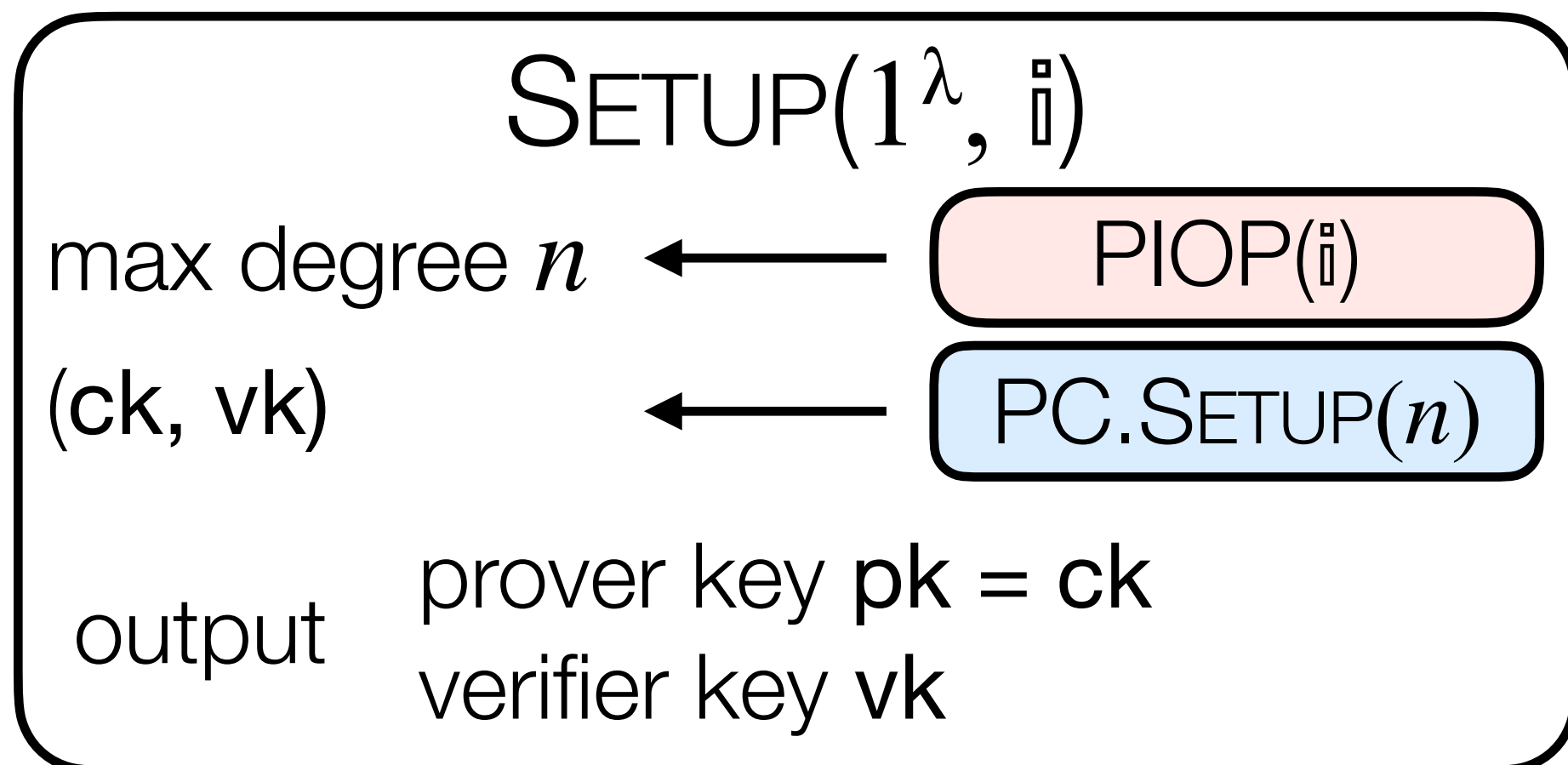
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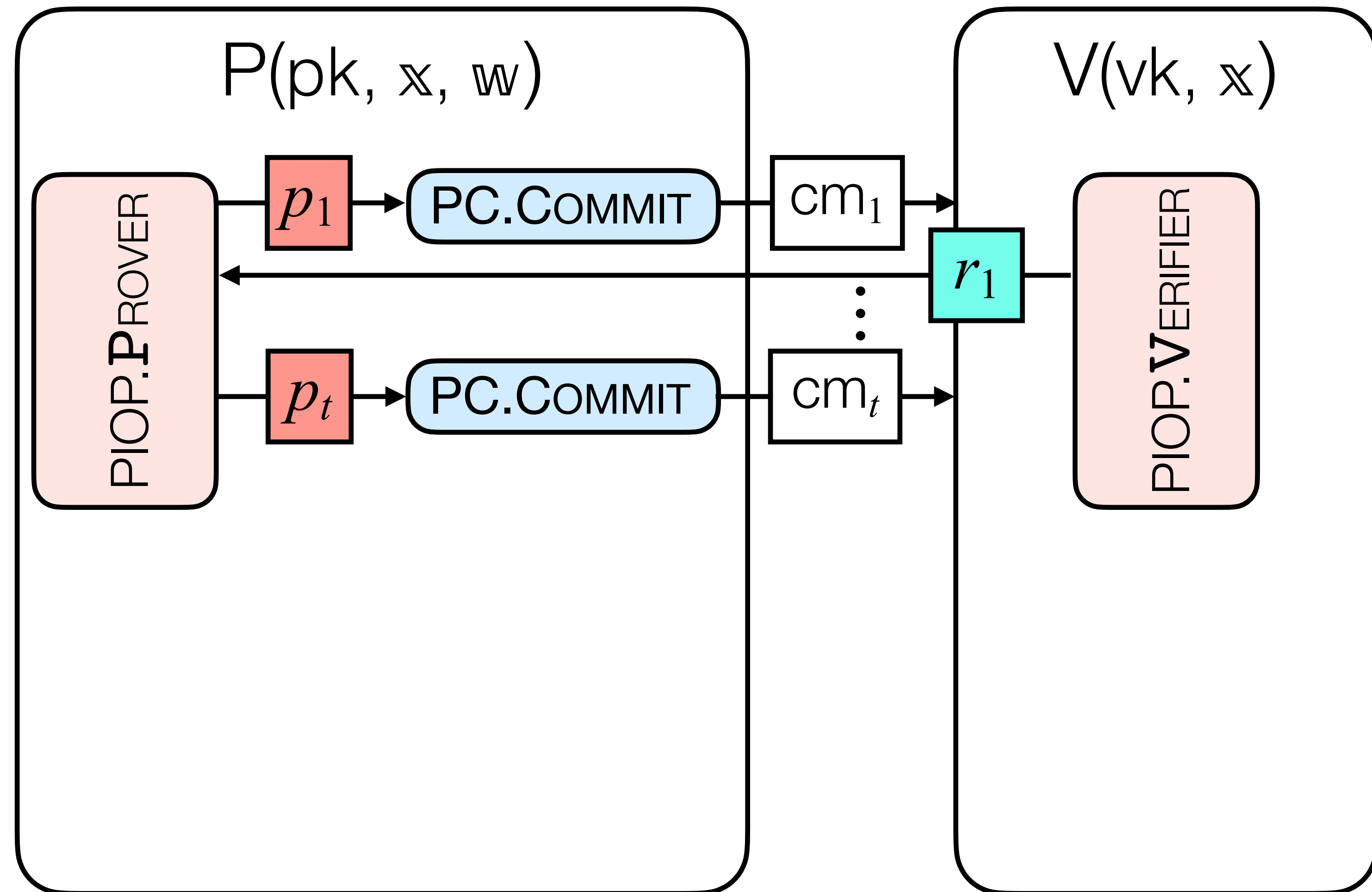
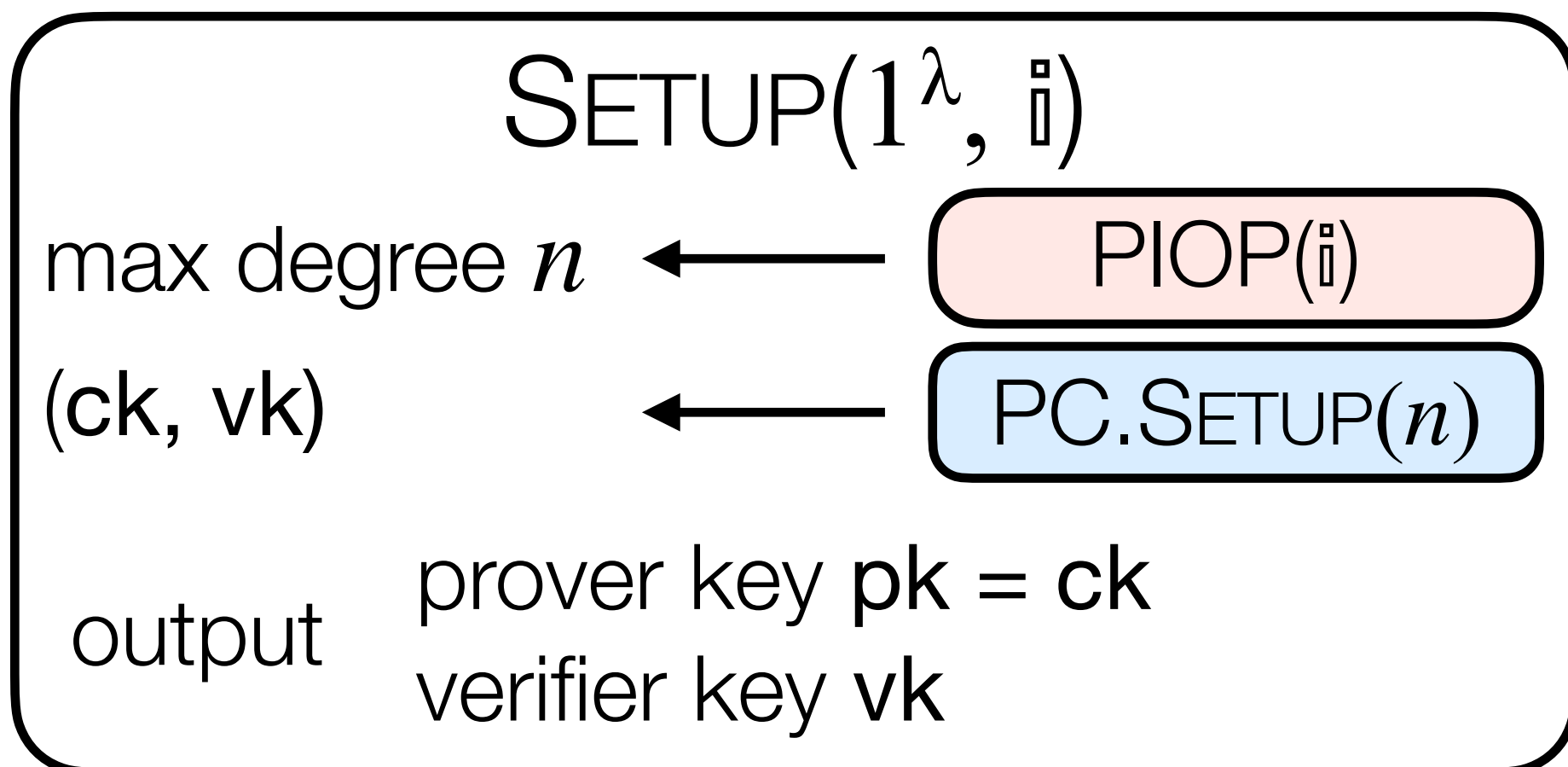
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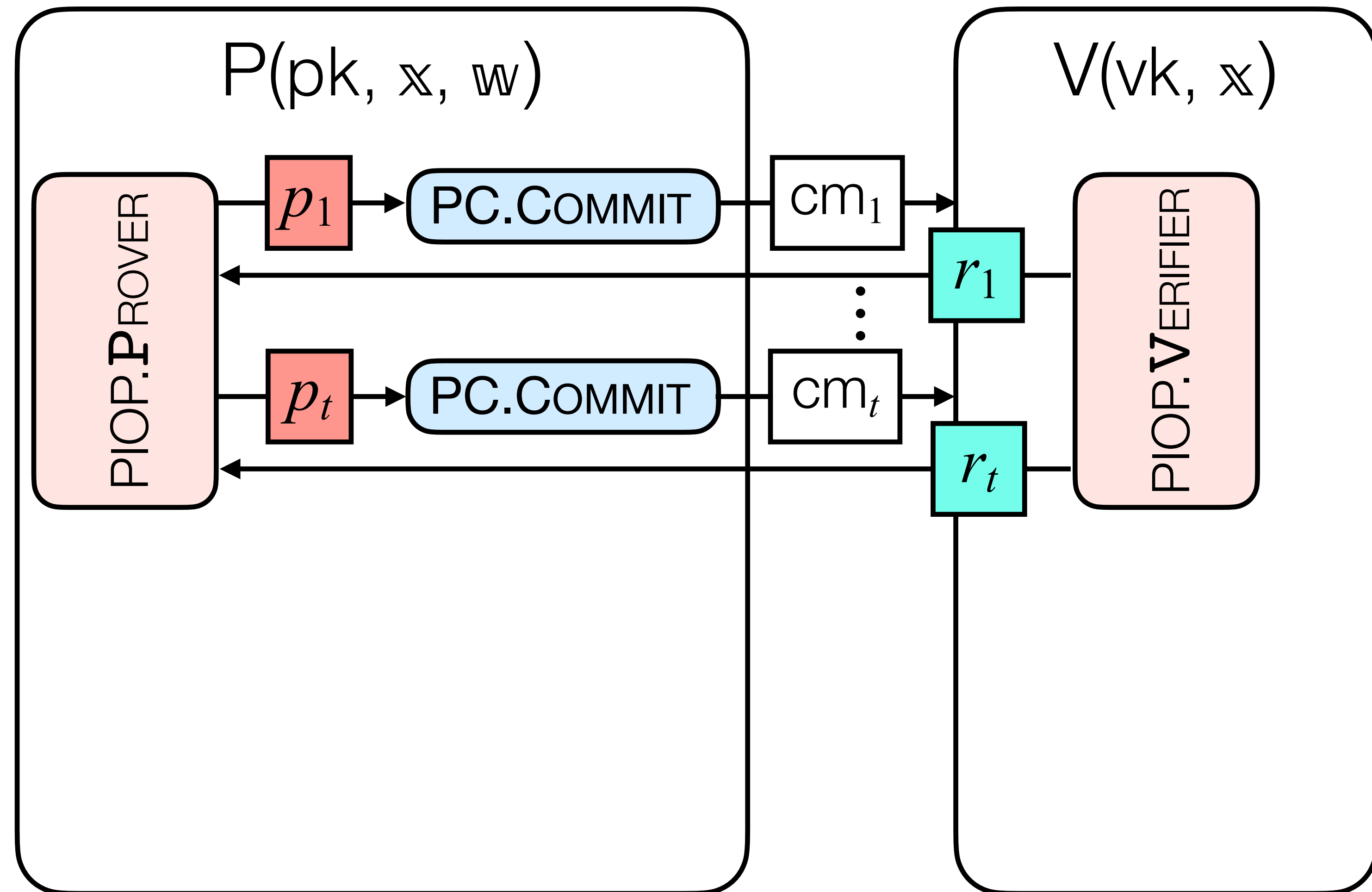
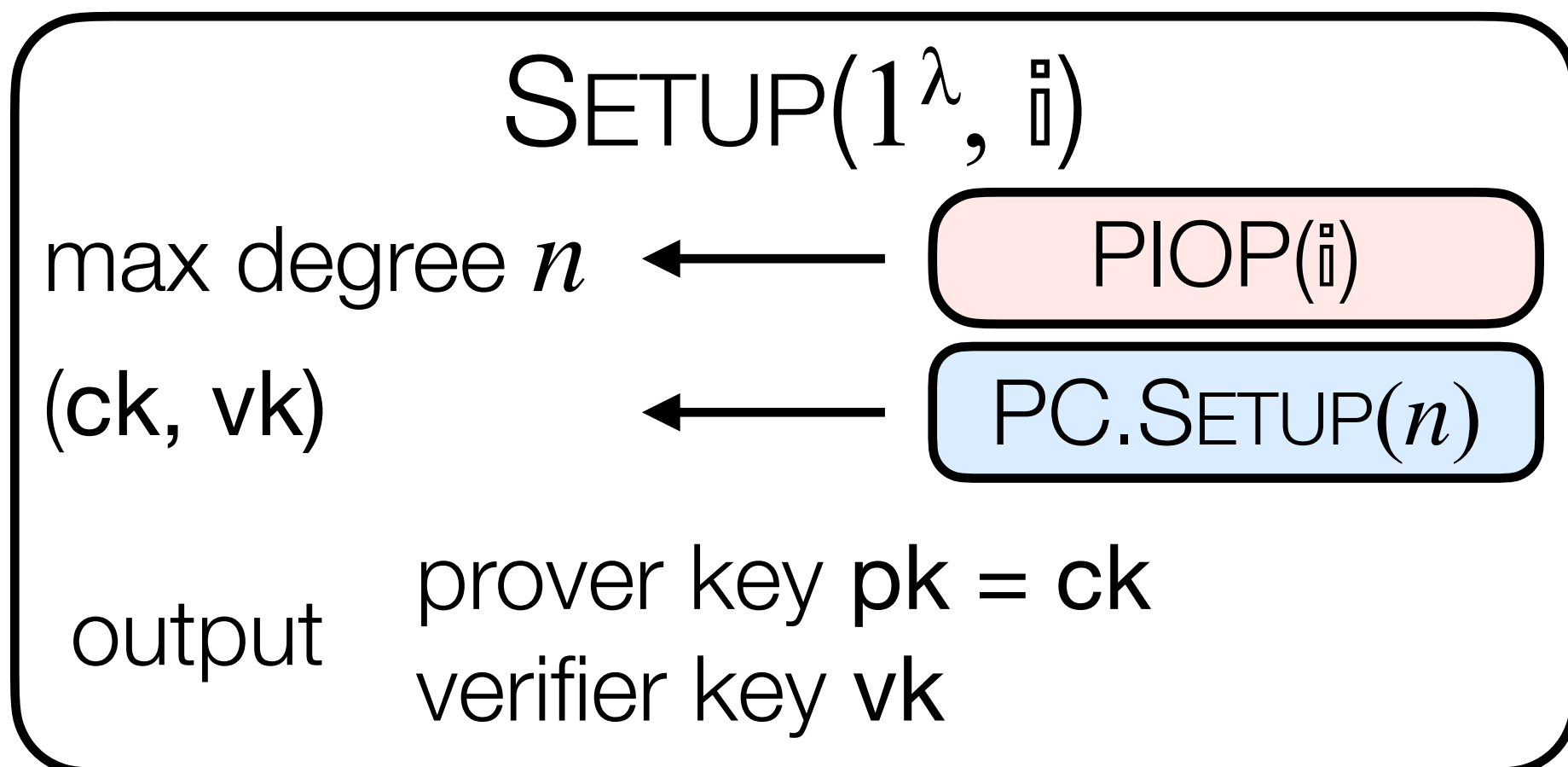
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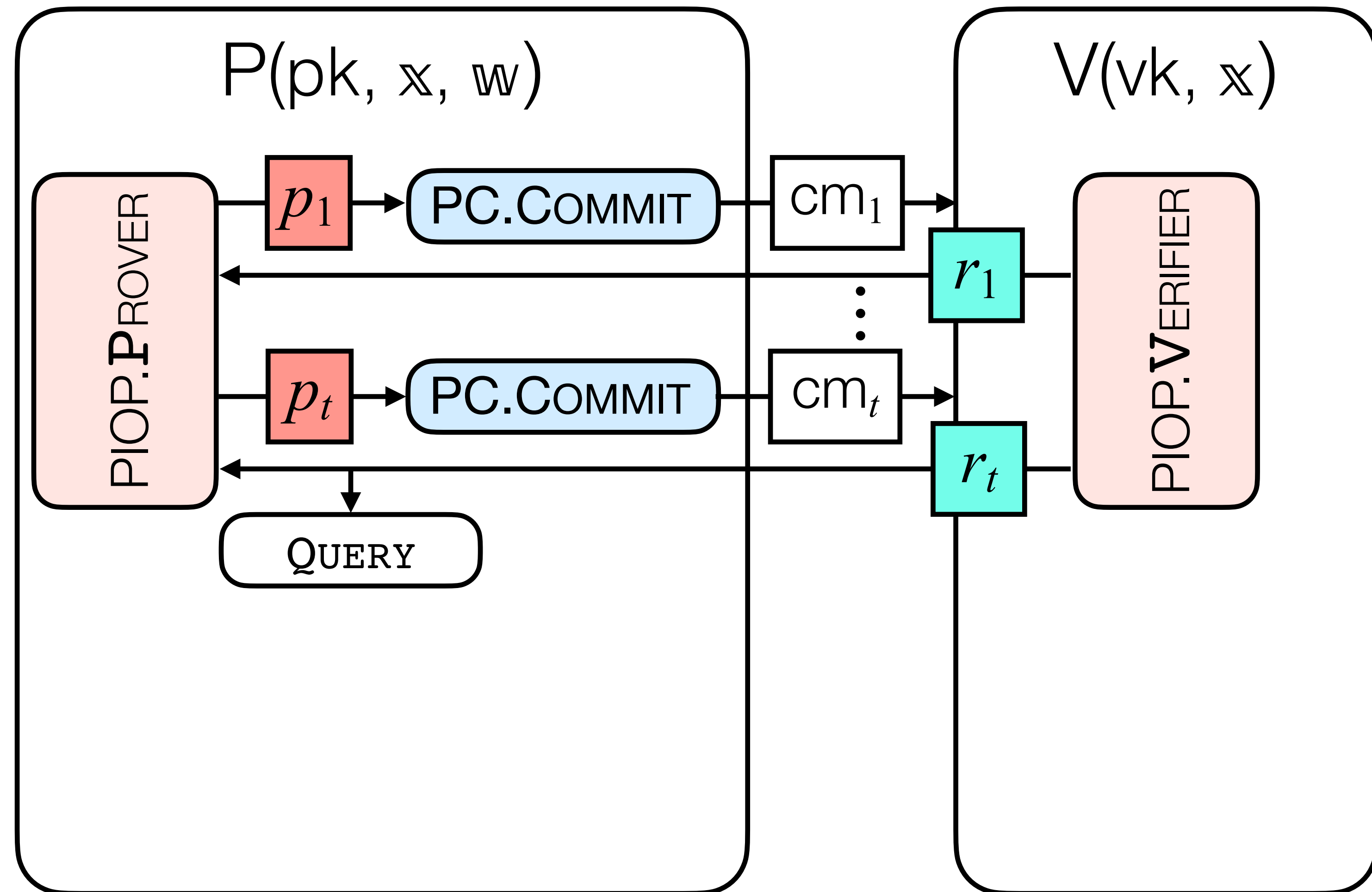
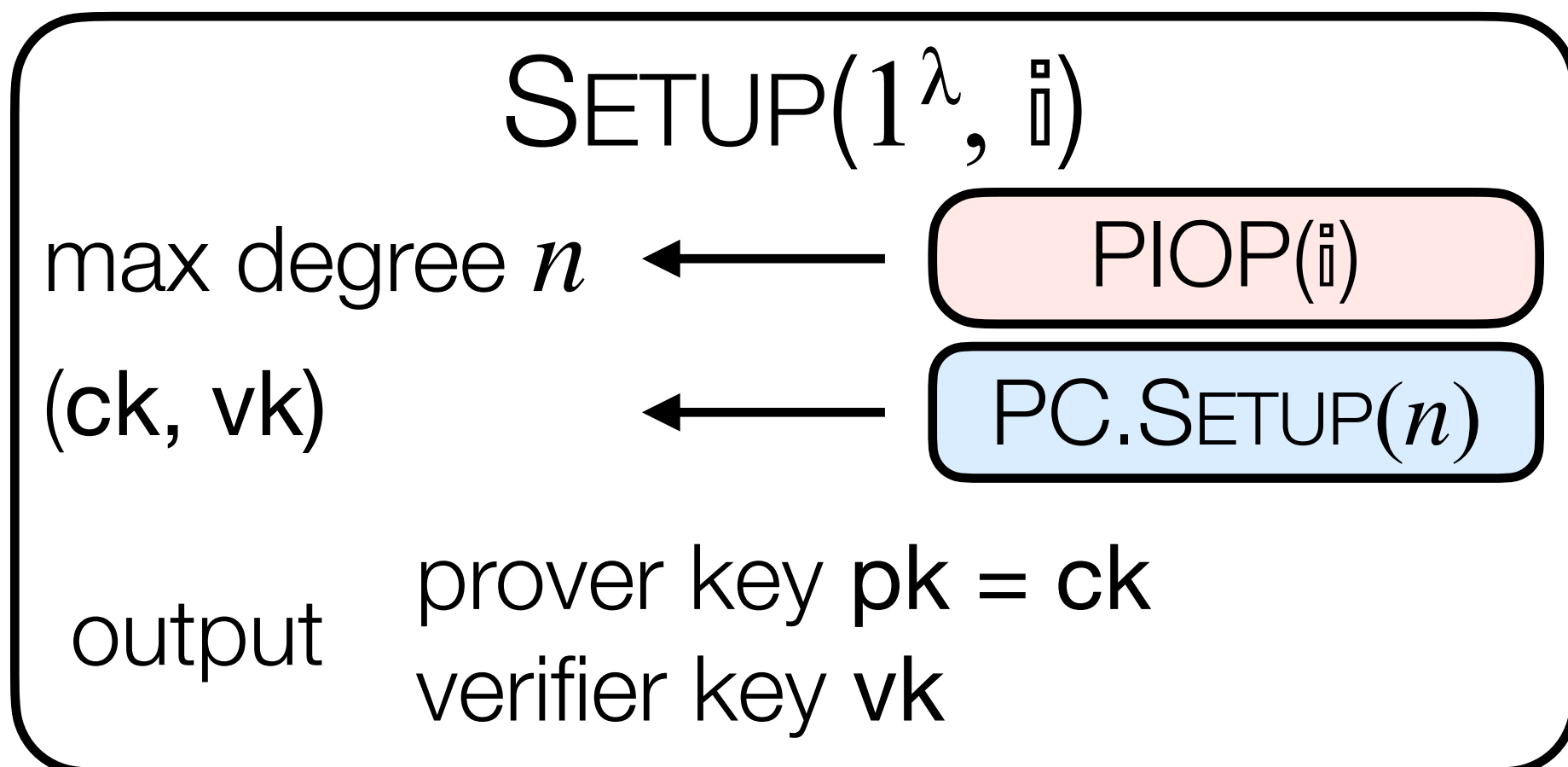
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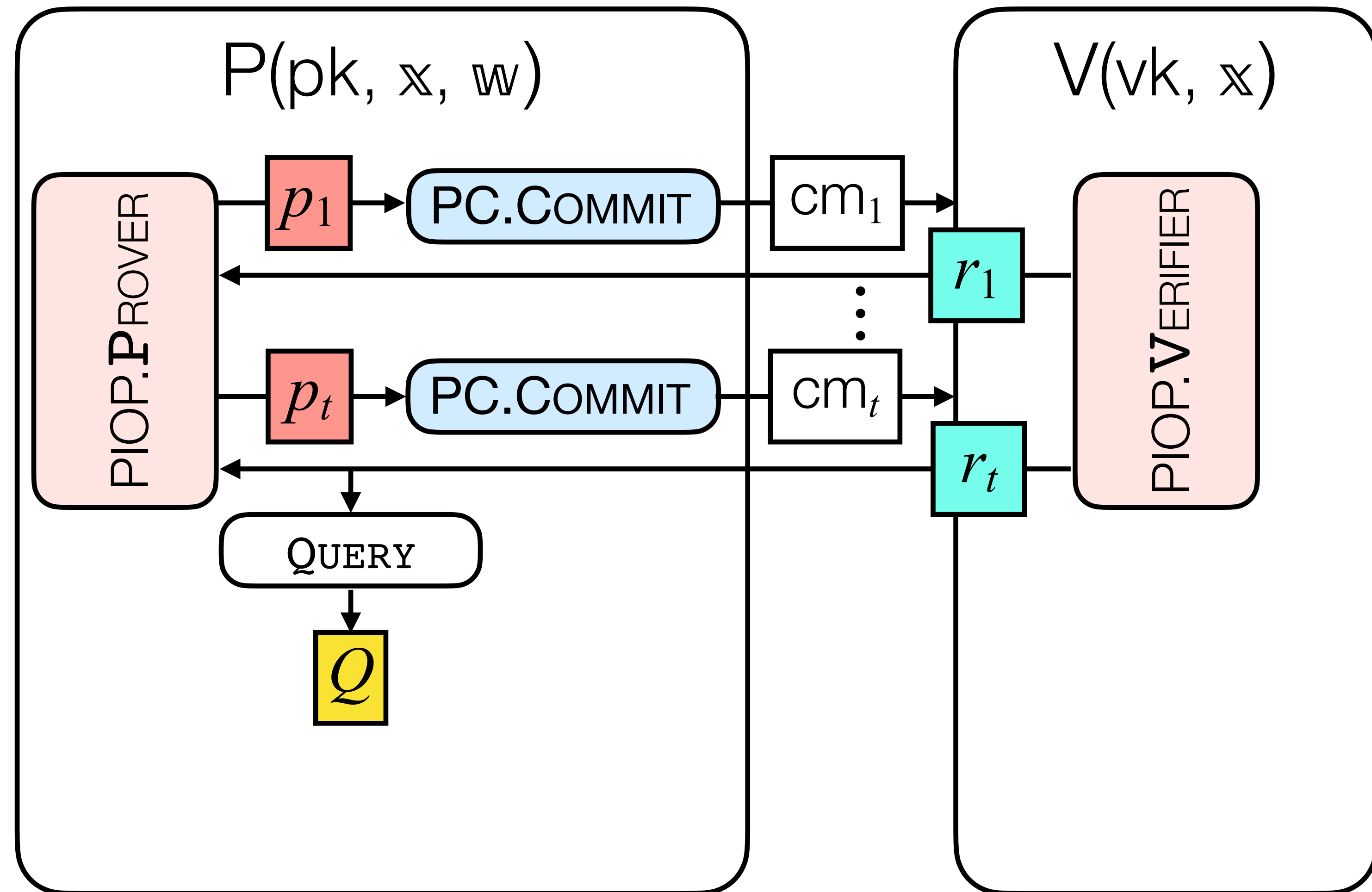
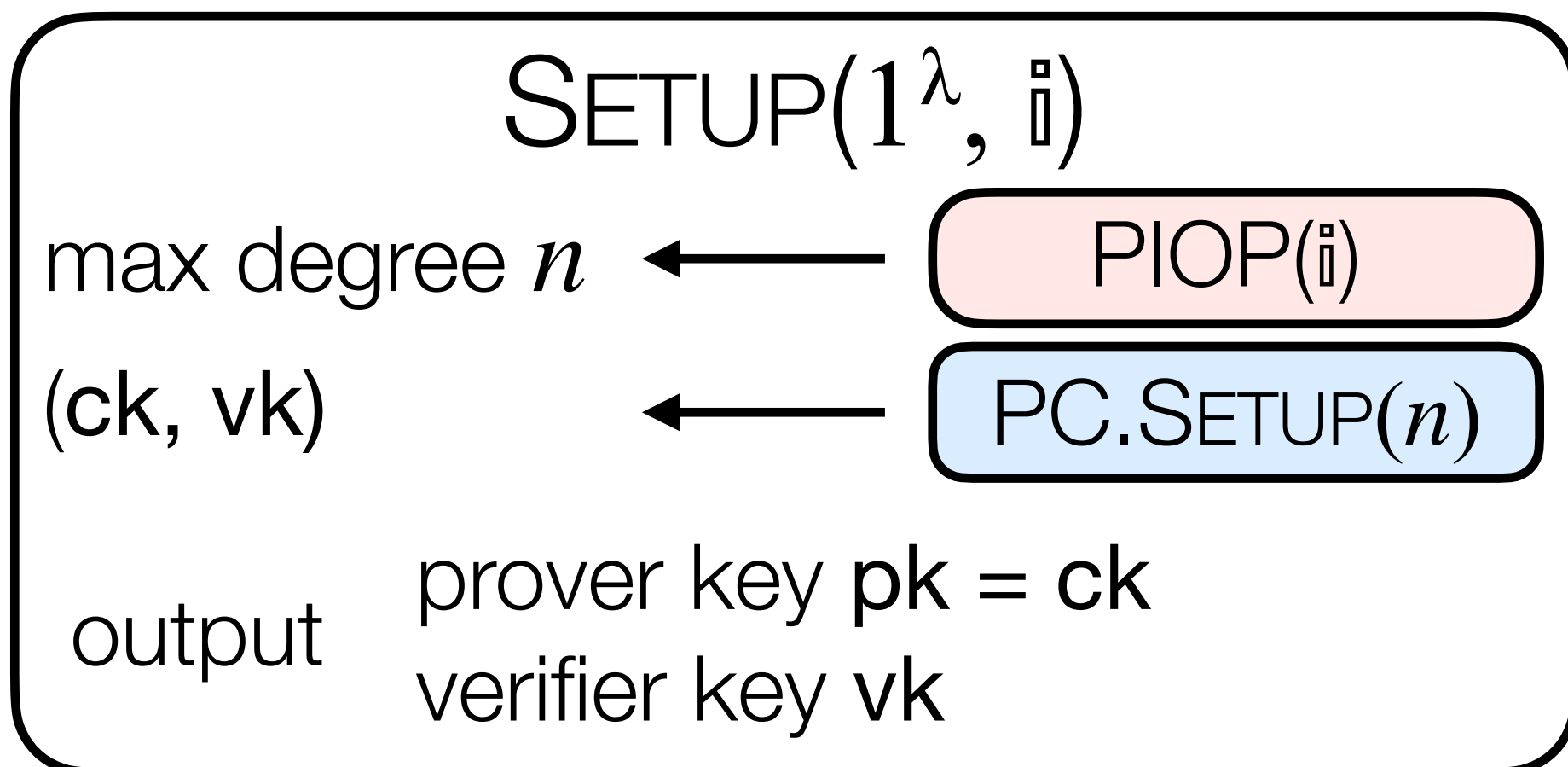
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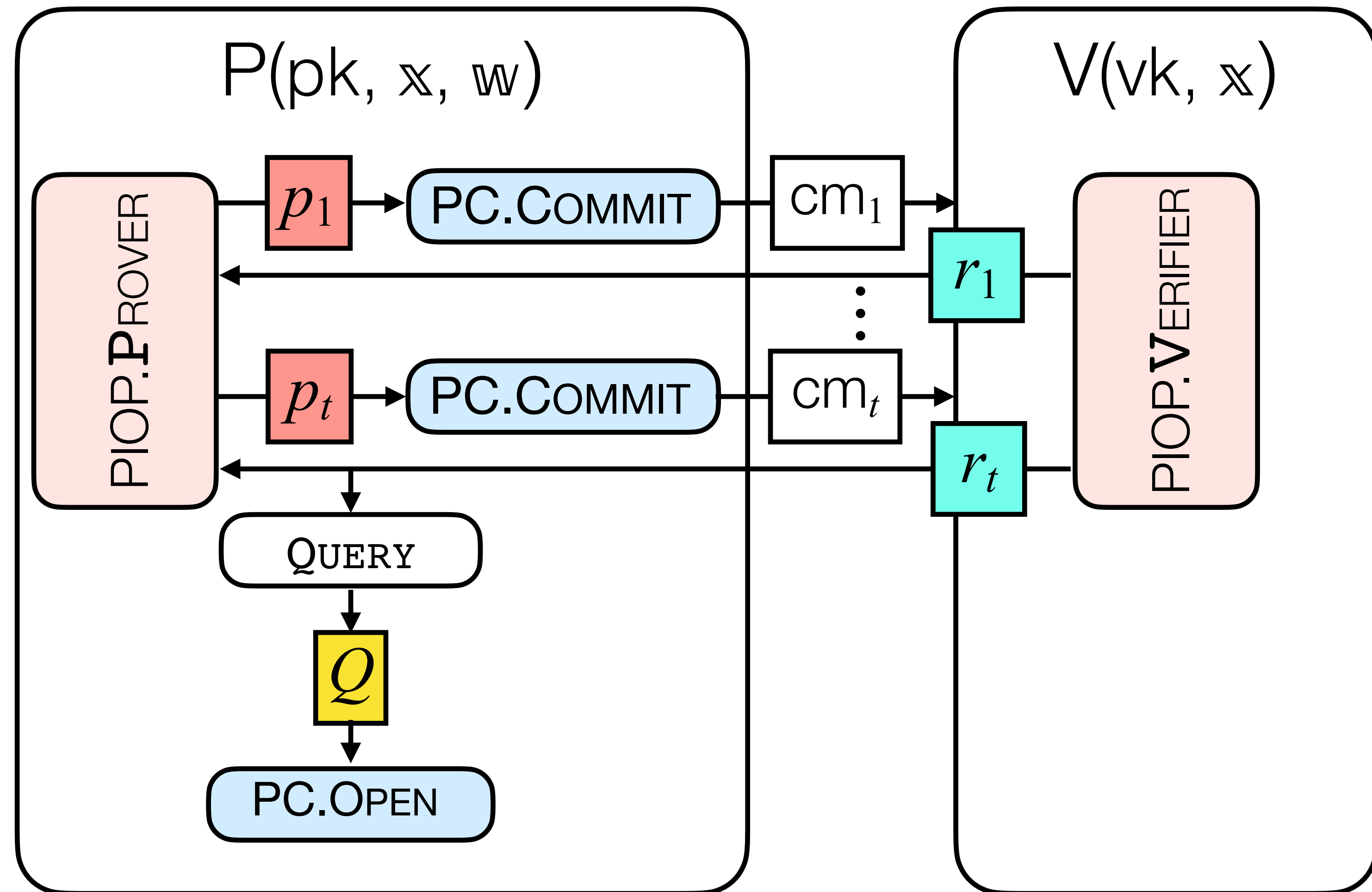
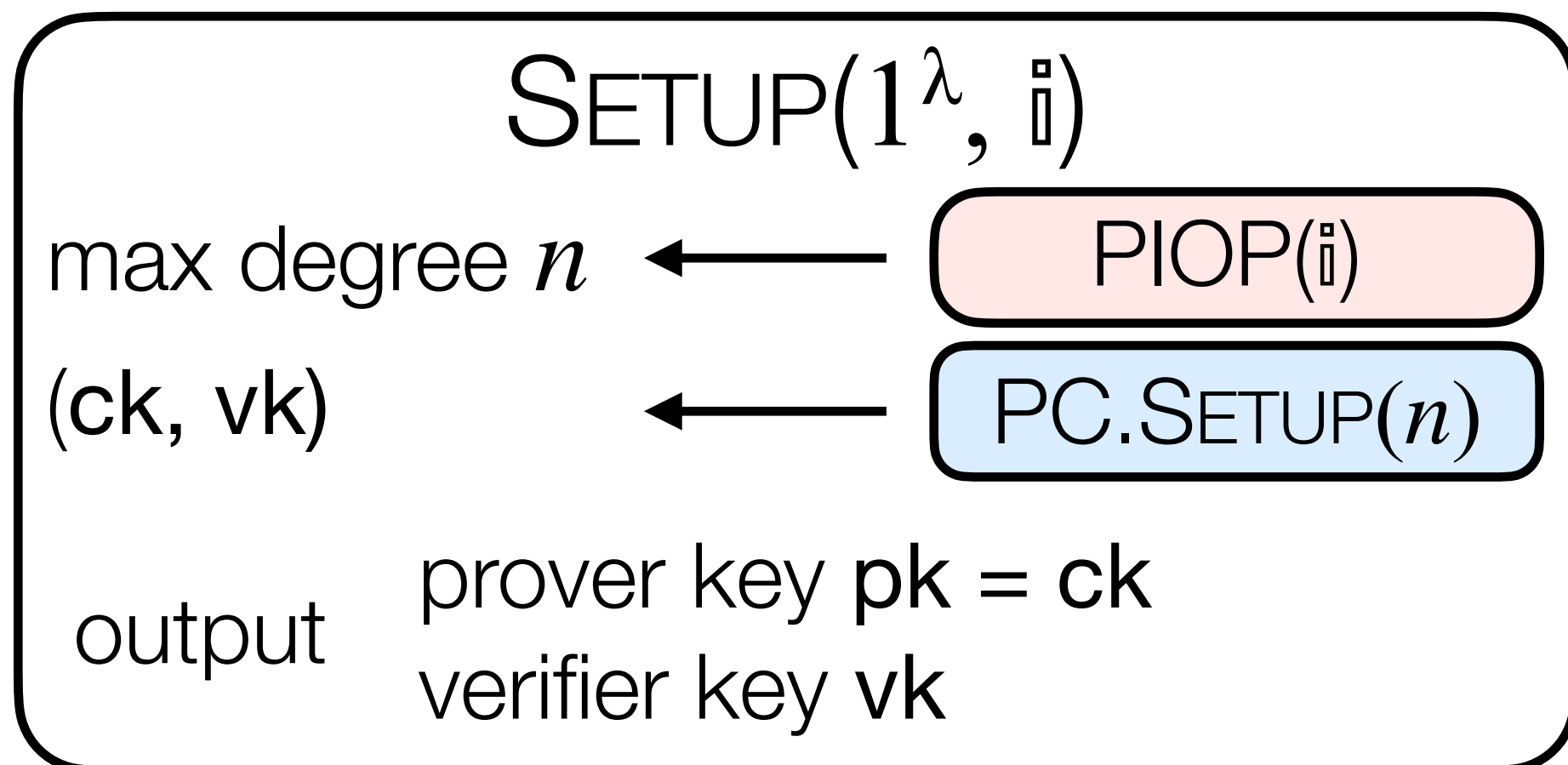
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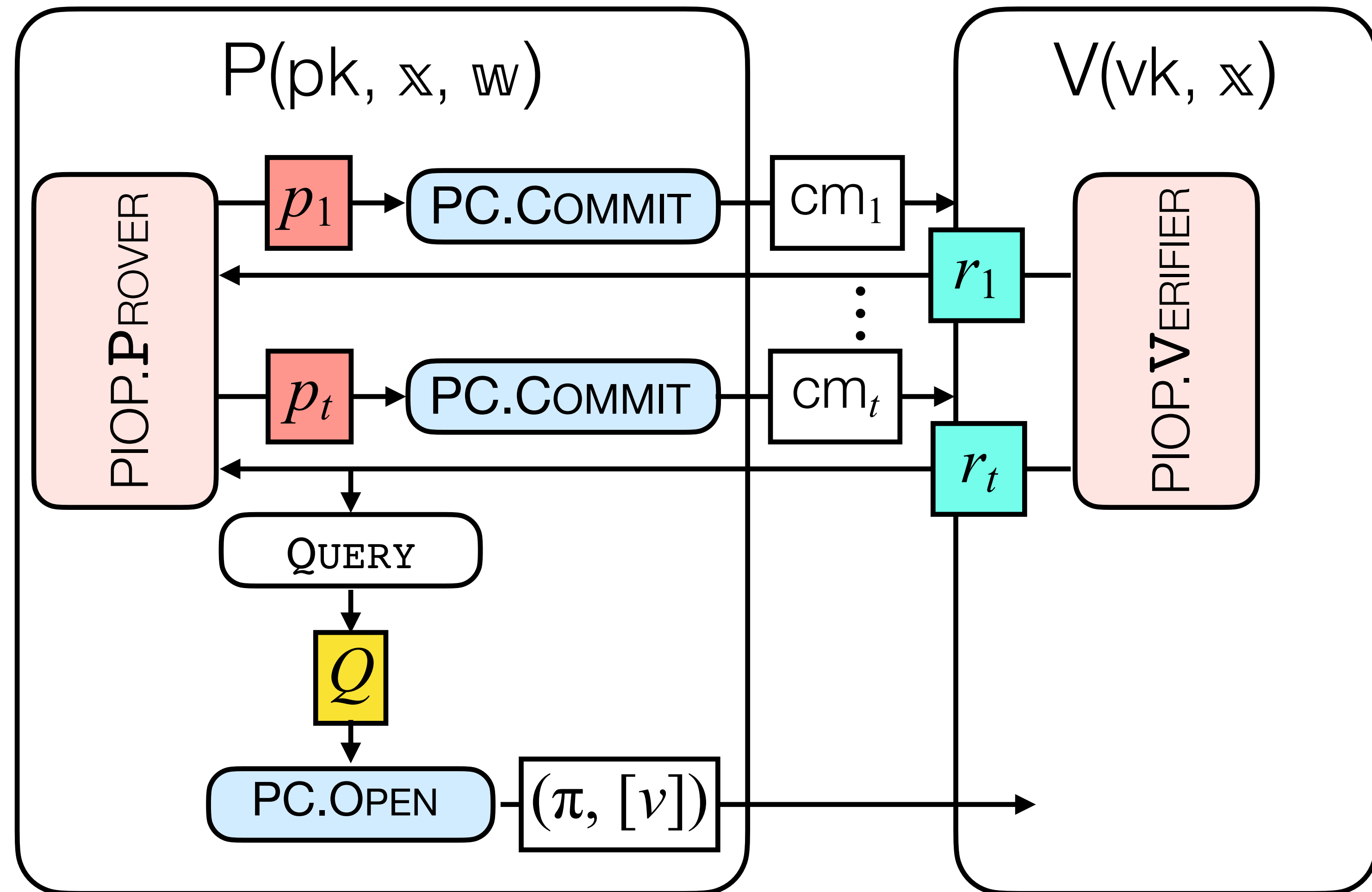
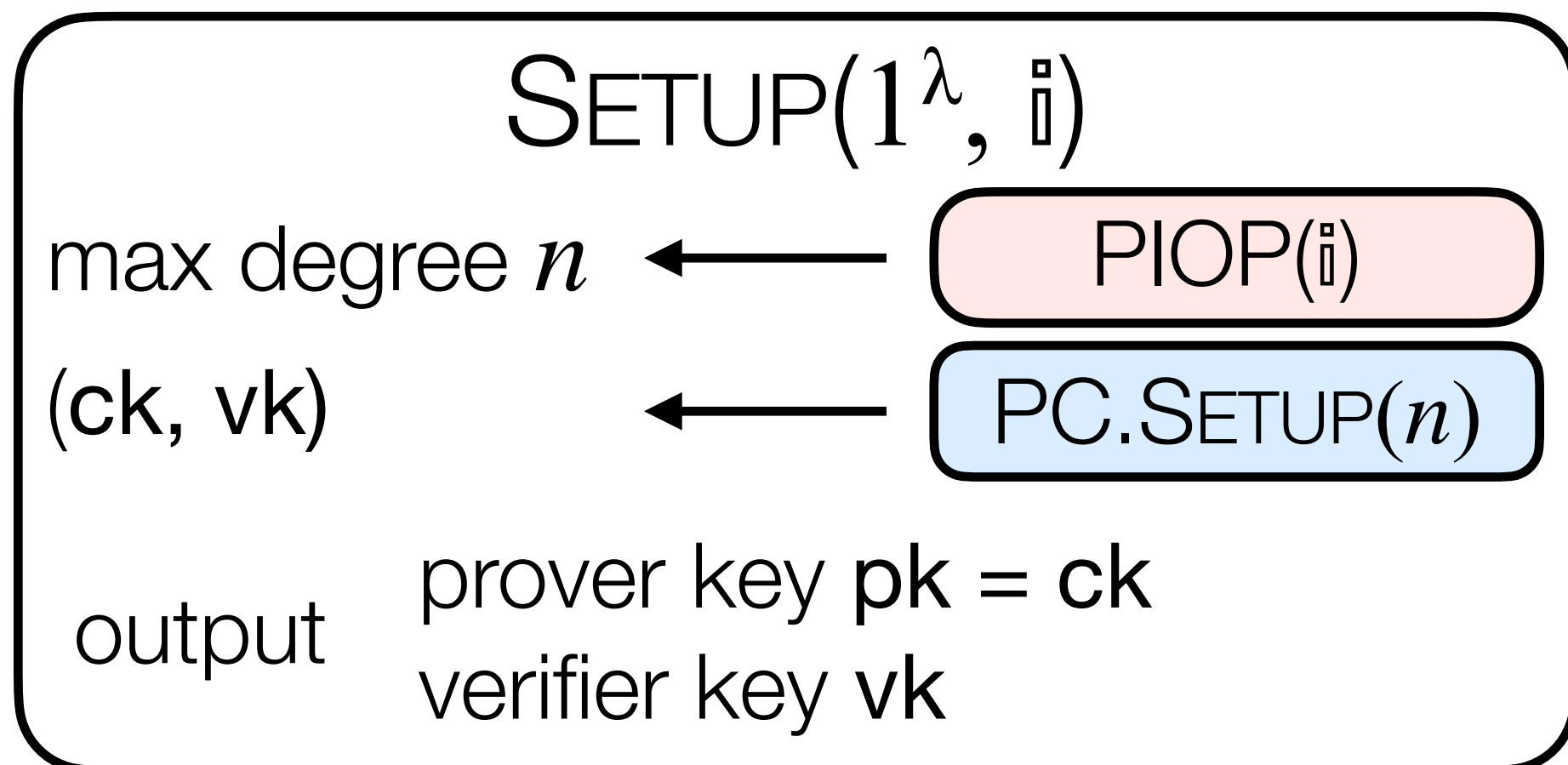
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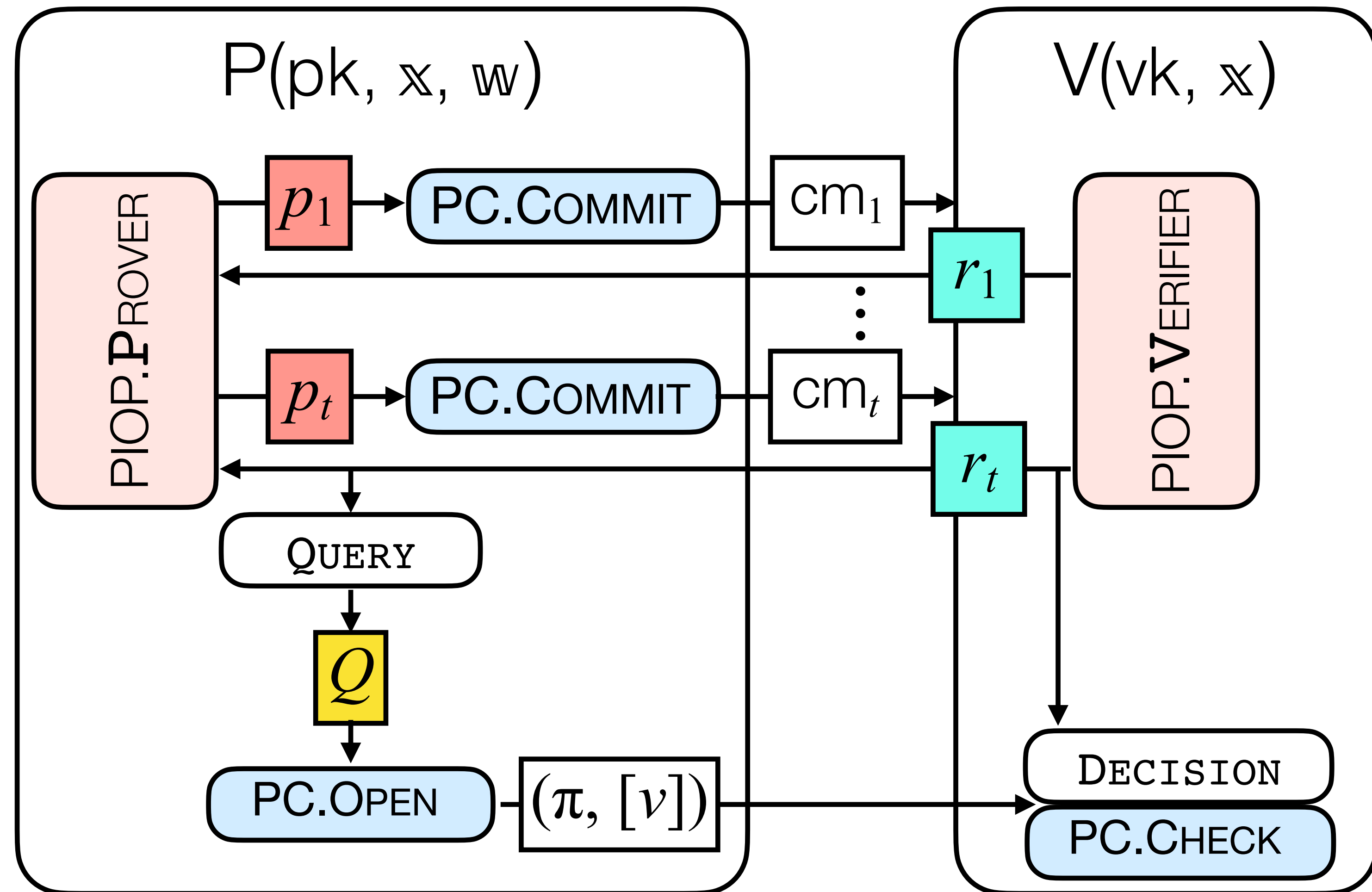
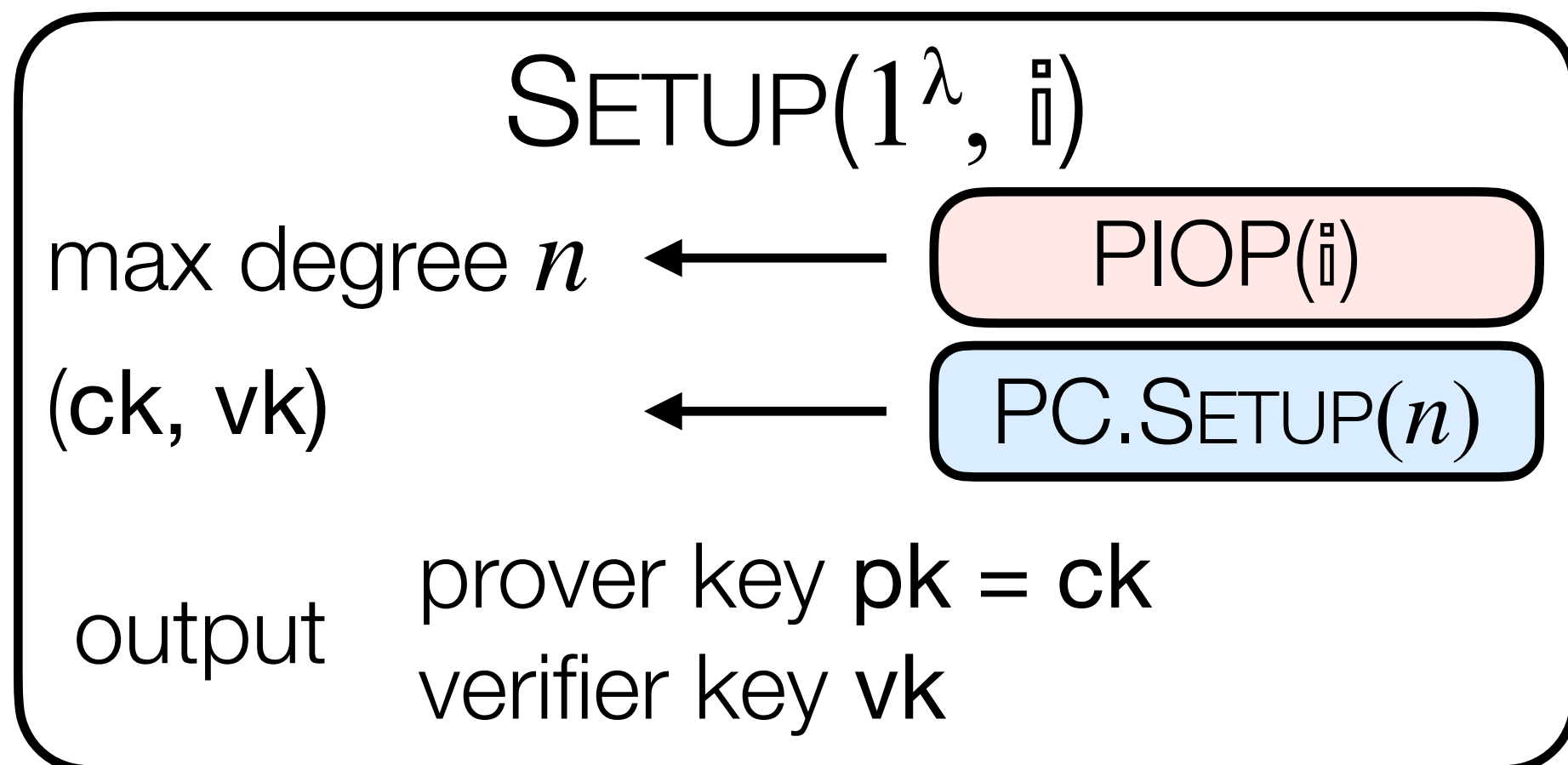
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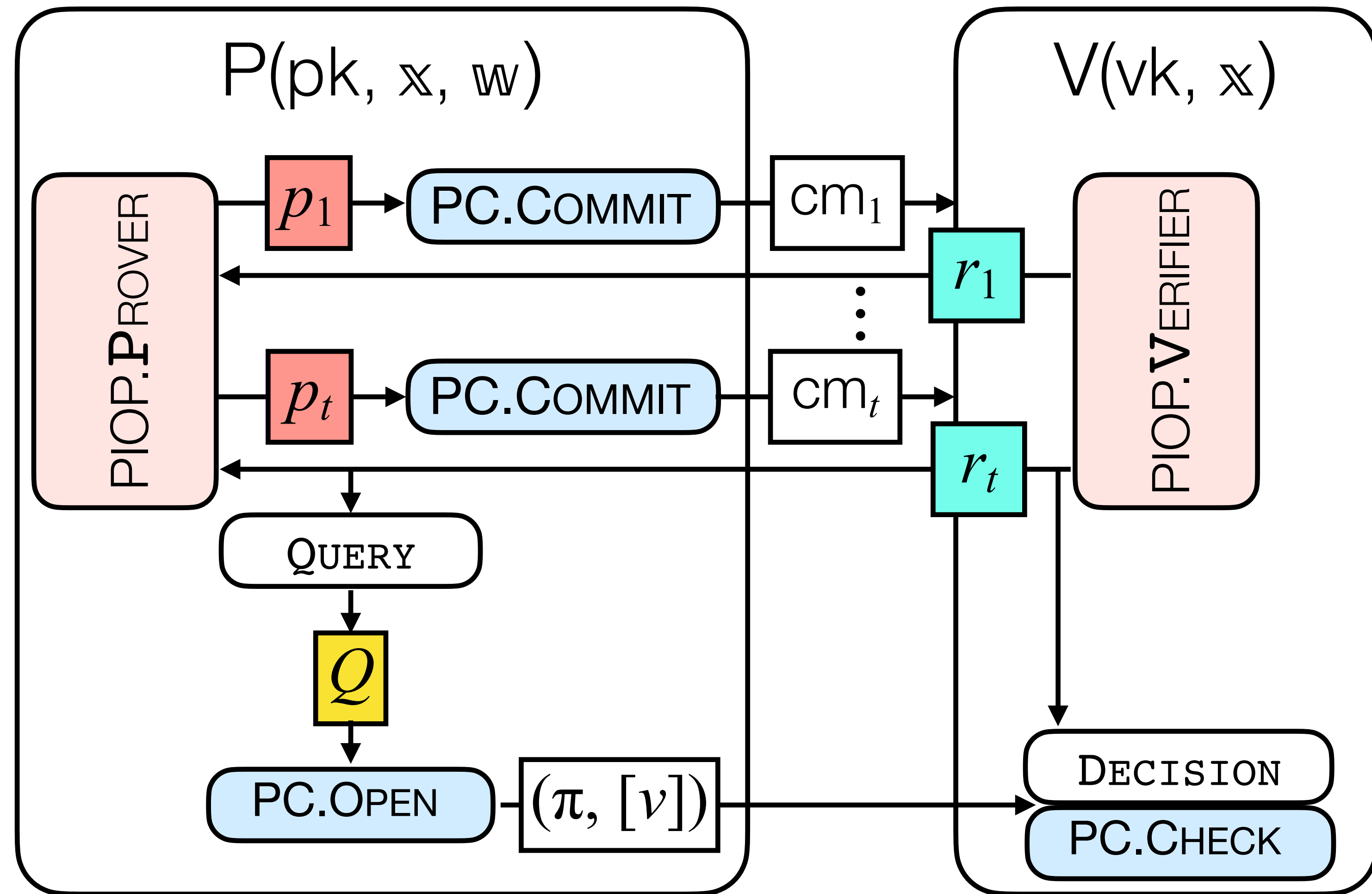
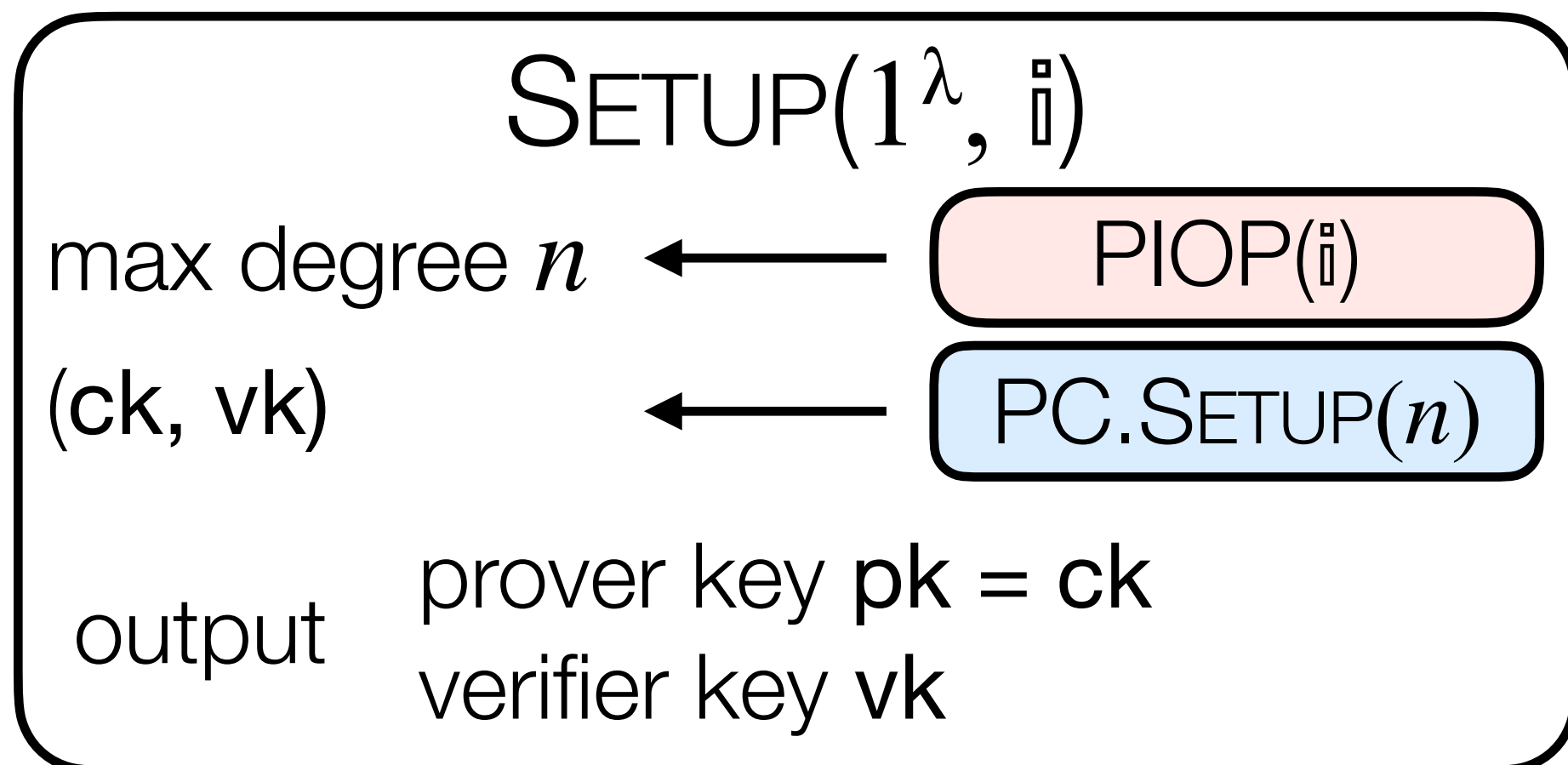
PIOPs + PC Schemes \rightarrow SNARK

[CHMMVW20, BFS20]



PIOPs + PC Schemes \rightarrow SNARK

[CHMMVW20, BFS20]



+ Fiat—Shamir to get non-interactivity

Prior PIOP-based SNARKs for R1CS

[CHMMVW20, S21]

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R1CS consists of triples $((A, B, C), x, w)$ such that the following holds for $z = (x, w)$:

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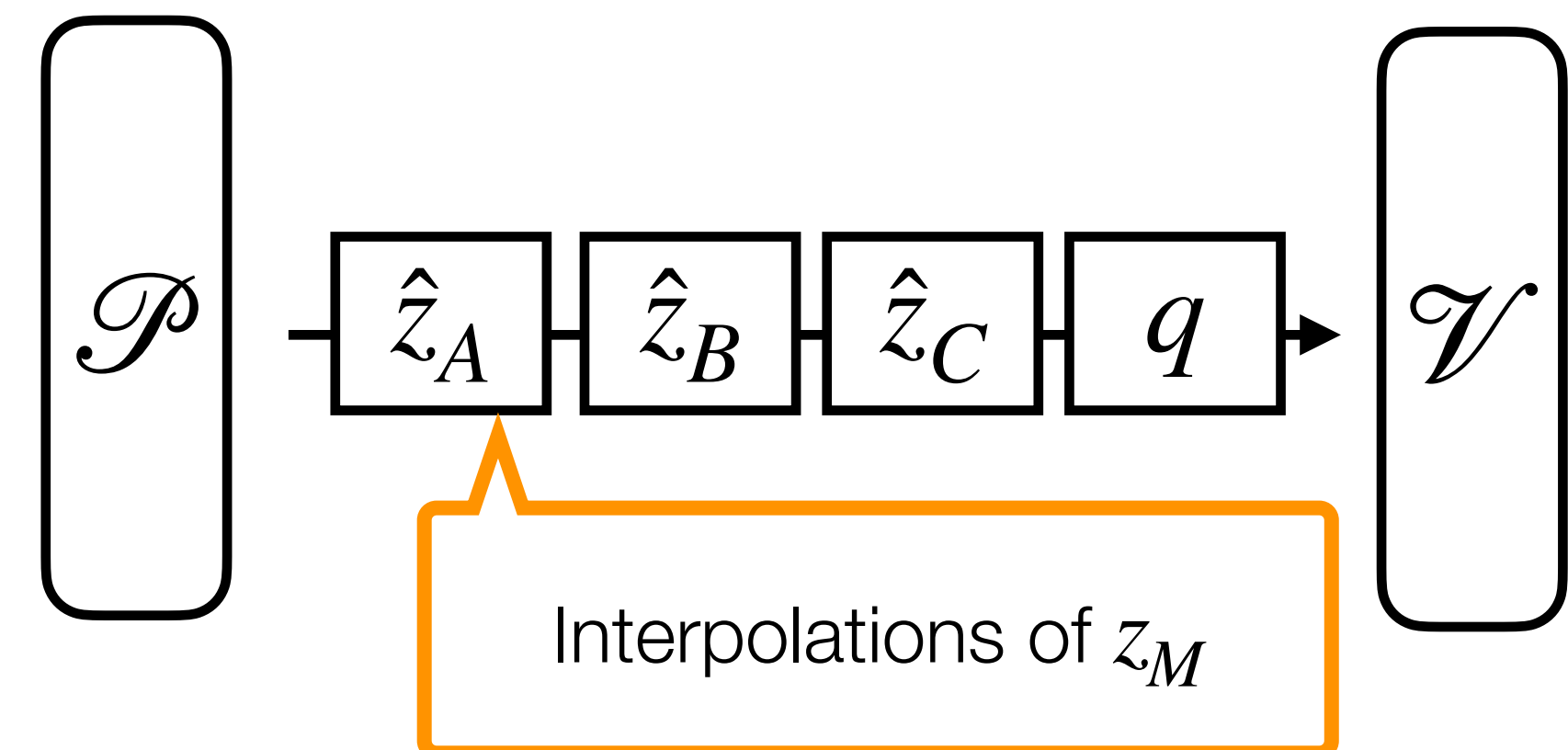
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Rowcheck subPIOP

Usually quite cheap!



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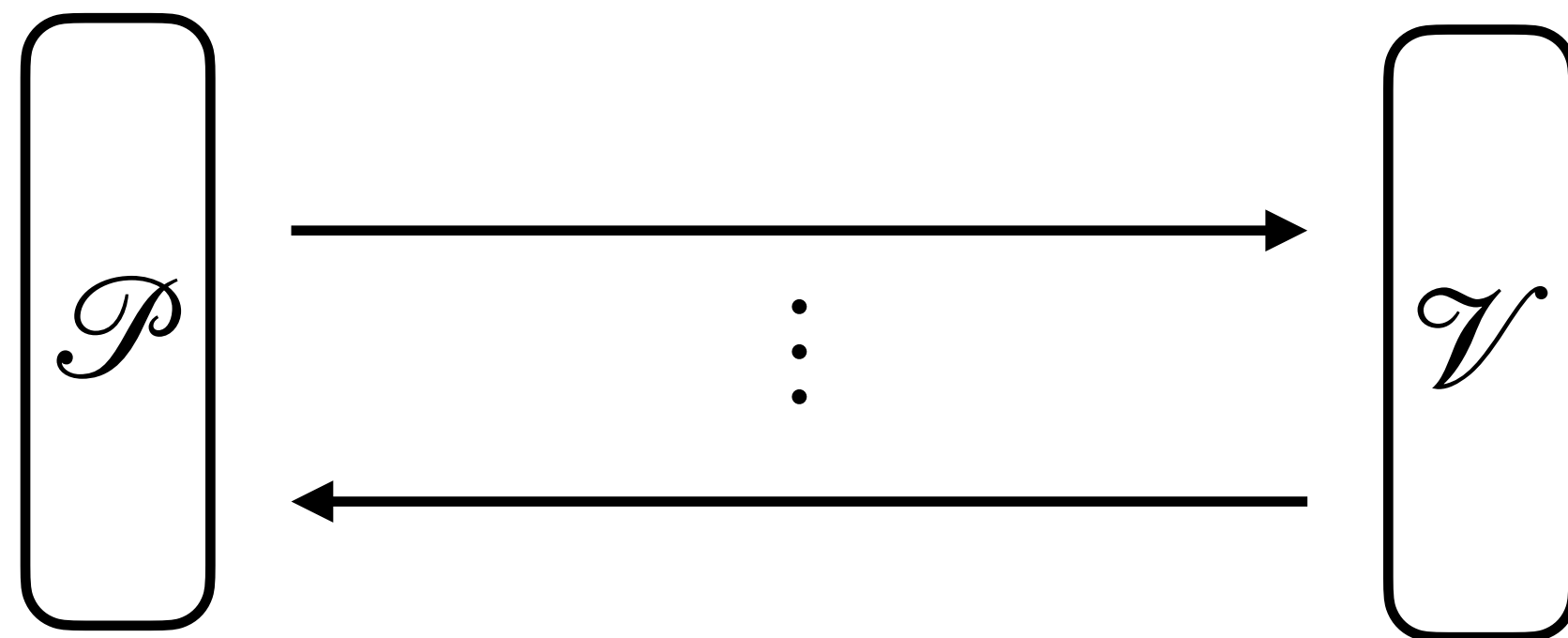
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Lincheck subPIOP

Usually most expensive part!

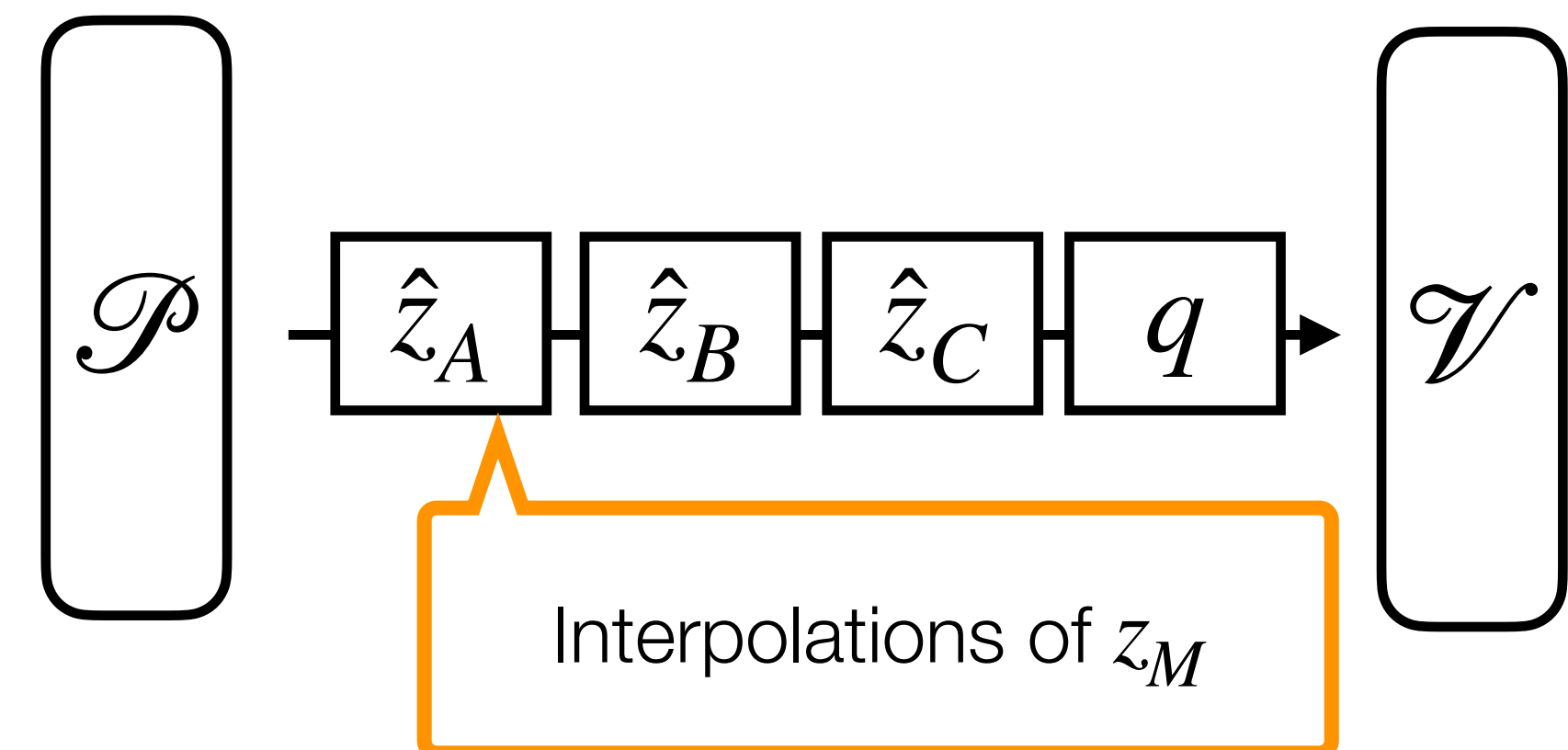


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Lincheck subPIOP

Usually most expensive part!

Requires numerous commitments, openings, and evaluation proofs

In contrast, circuit-specific SNARKs like Groth16 require no extra group elements

Nonlinear “row” checks:

$$z_A \circ z_B = z_C$$

Rowcheck subPIOP

Usually quite cheap!

Compiles to only 4 group elements!

A New Lincheck

Linchecks via coefficient-equality

Linchecks via coefficient-equality

Step 1 : Express Lincheck as a linear combination of matrix column vectors.

Linchecks via coefficient-equality

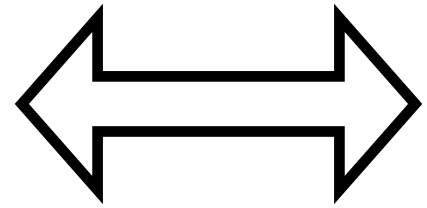
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$$A \times z = z_A$$

Linchecks via coefficient-equality

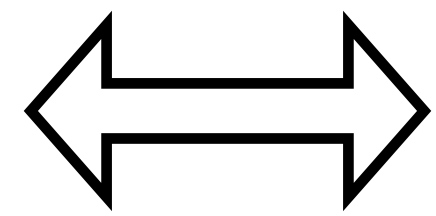
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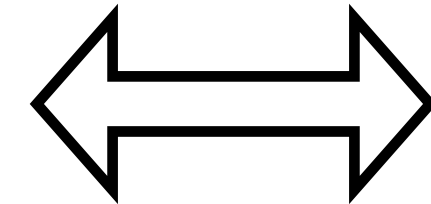
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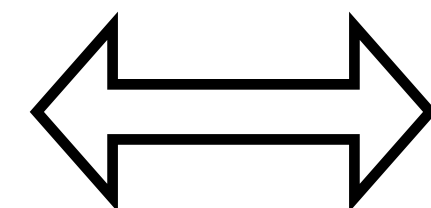
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$$\begin{array}{c} A \\ \left[\begin{array}{c} \updownarrow \\ a_1 \\ \updownarrow \end{array} \quad \begin{array}{c} \updownarrow \\ a_2 \\ \updownarrow \end{array} \quad \begin{array}{c} \updownarrow \\ a_3 \\ \updownarrow \end{array} \quad \dots \quad \begin{array}{c} \updownarrow \\ a_n \\ \updownarrow \end{array} \right] \end{array} \times \begin{array}{c} z \\ \left[\begin{array}{c} z_1 \\ z_2 \\ \vdots \\ z_n \end{array} \right] \end{array} = \begin{array}{c} z_A \\ \left[\begin{array}{c} z_A \end{array} \right] \end{array}$$

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↔

$$\begin{bmatrix} \begin{array}{c} \updownarrow \\ a_1 \\ \updownarrow \end{array} & \begin{array}{c} \updownarrow \\ a_2 \\ \updownarrow \end{array} & \begin{array}{c} \updownarrow \\ a_3 \\ \updownarrow \end{array} & \dots & \begin{array}{c} \updownarrow \\ a_n \\ \updownarrow \end{array} \end{bmatrix} \times \begin{bmatrix} z_1 \\ z_2 \\ \vdots \\ z_n \end{bmatrix} = \begin{bmatrix} z_A \end{bmatrix}$$

↔

$$z_1 \cdot \begin{bmatrix} \begin{array}{c} \updownarrow \\ a_1 \\ \updownarrow \end{array} \end{bmatrix} + z_2 \cdot \begin{bmatrix} \begin{array}{c} \updownarrow \\ a_2 \\ \updownarrow \end{array} \end{bmatrix} + \dots + z_n \cdot \begin{bmatrix} \begin{array}{c} \updownarrow \\ a_n \\ \updownarrow \end{array} \end{bmatrix} = \begin{bmatrix} z_A \end{bmatrix}$$

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Interpolate a_i -s over $\{1, \dots, n\}$

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$$z_1 \cdot \hat{a}_1(X) + z_2 \cdot \hat{a}_2(X) + \dots + z_n \cdot \hat{a}_n(X) = \hat{z}_A(X)$$

Now we can express the Lincheck in the language of polynomials:

$$\forall i \in \{1, \dots, n\}, \quad \hat{z}_A(i) = \sum_j \hat{a}_j(i) \cdot z[j]$$

Linchecks via coefficient-equality

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Step 3 : Now that Lincheck is written in the language of polynomials, we can argue that:

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Prover knows $z \in \mathbb{F}^n$ such that

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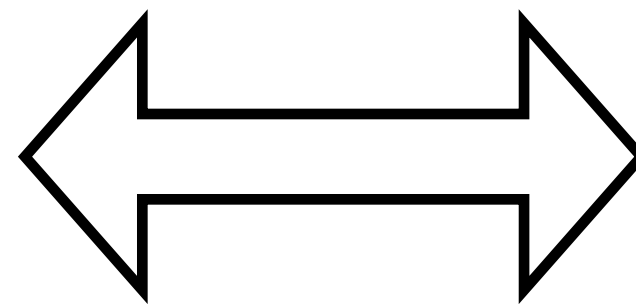
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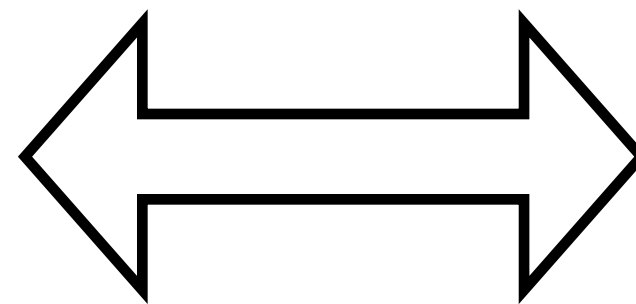
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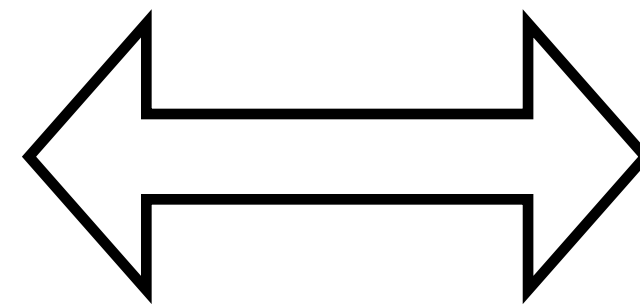
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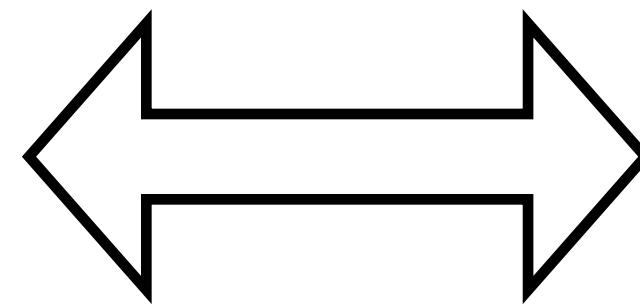
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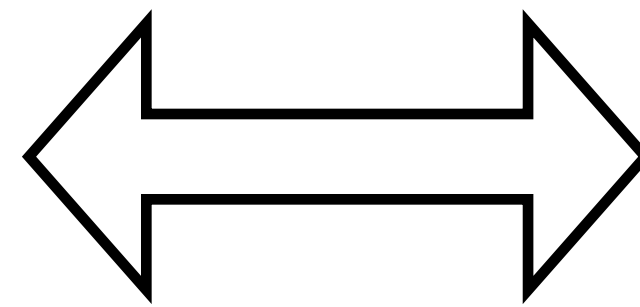
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Same coefficients in all!

New Approach for Lincheck

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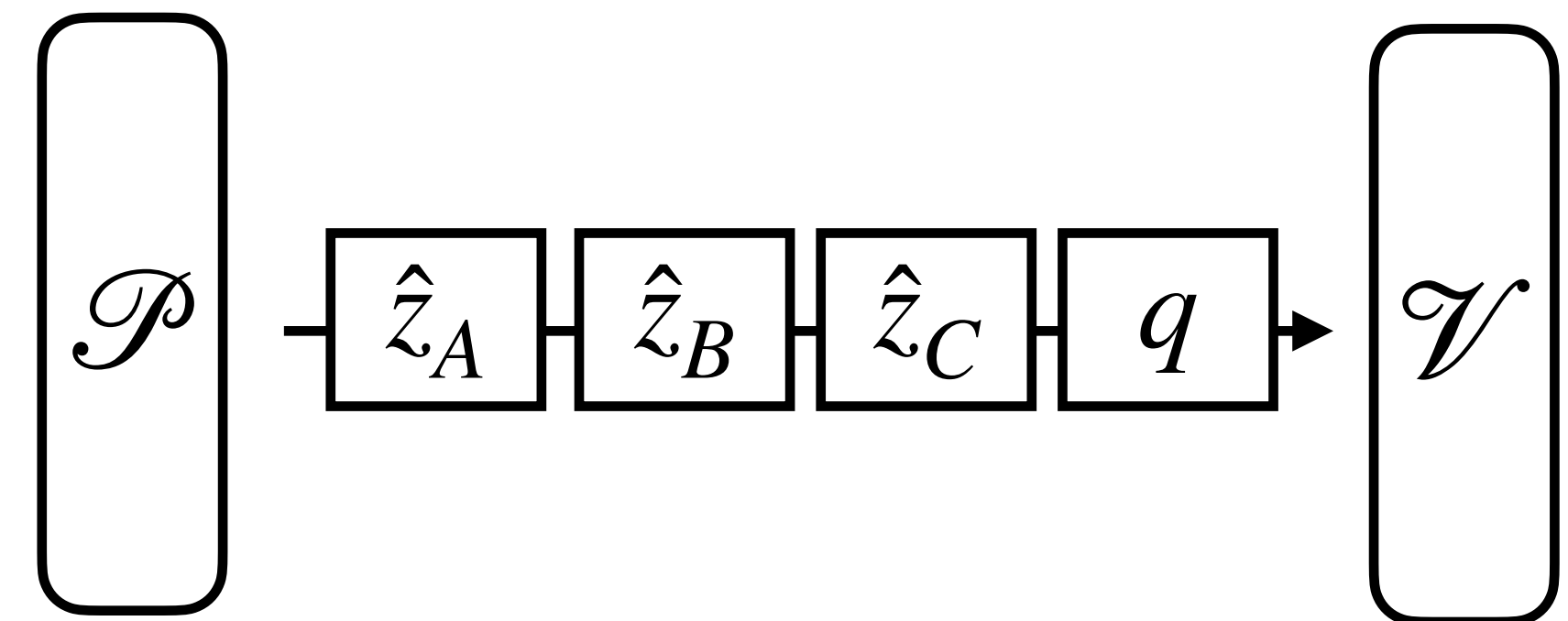
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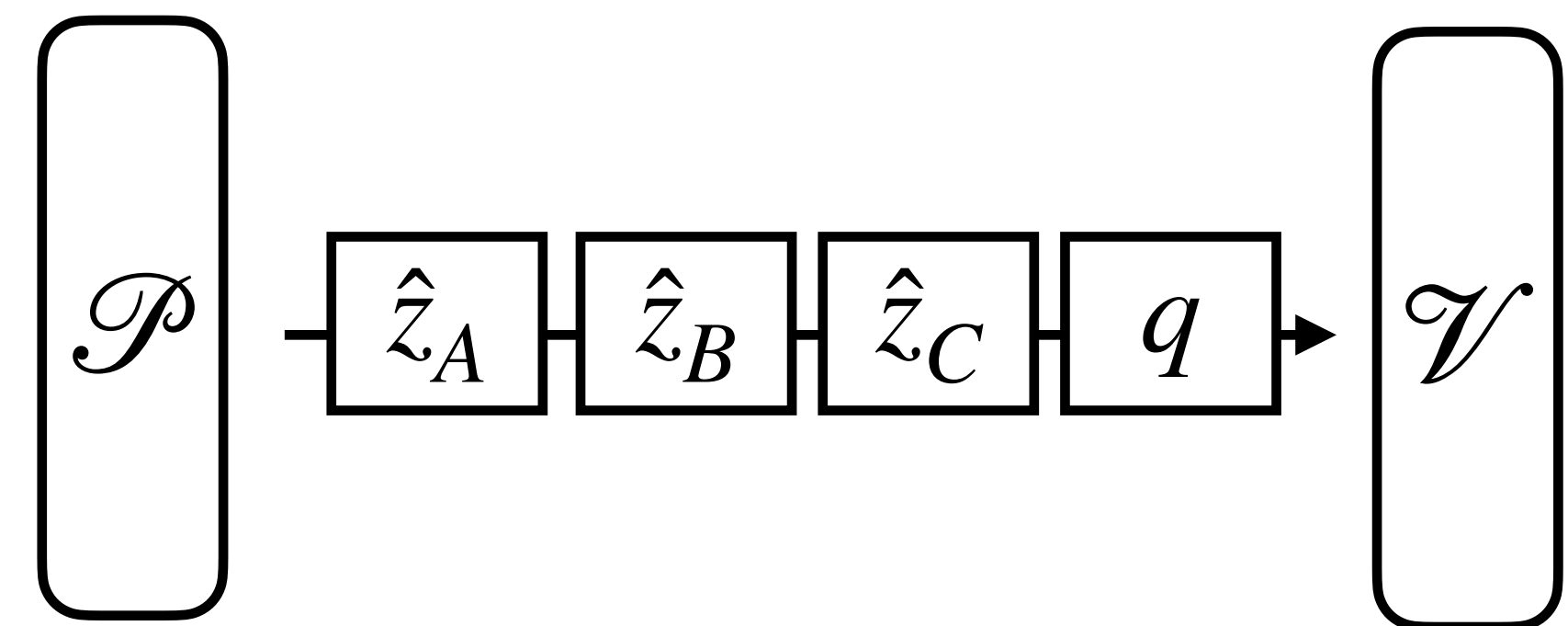
Lincheck via coefficient-equality

How to enforce?

Equi-efficient Polynomial
Commitment Schemes!

Rowcheck subPIOP

Usually quite cheap!



New Tool: EPC schemes

Equifflcient constraints

A coefficient-equality or “equifflcient” constraint is a set of bases

$$E := \{\mathcal{A}, \mathcal{B}, \mathcal{C}\}$$

where $\mathcal{A} = \{a_1, \dots, a_n\}$, $\mathcal{B} = \{b_1, \dots, b_n\}$, $\mathcal{C} = \{c_1, \dots, c_n\}$

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Polynomials $\hat{z}_A(X)$, $\hat{z}_B(X)$, $\hat{z}_C(X)$ are said to satisfy E if they have equal coefficient vectors under bases \mathcal{A} , \mathcal{B} , \mathcal{C} respectively, i.e.:

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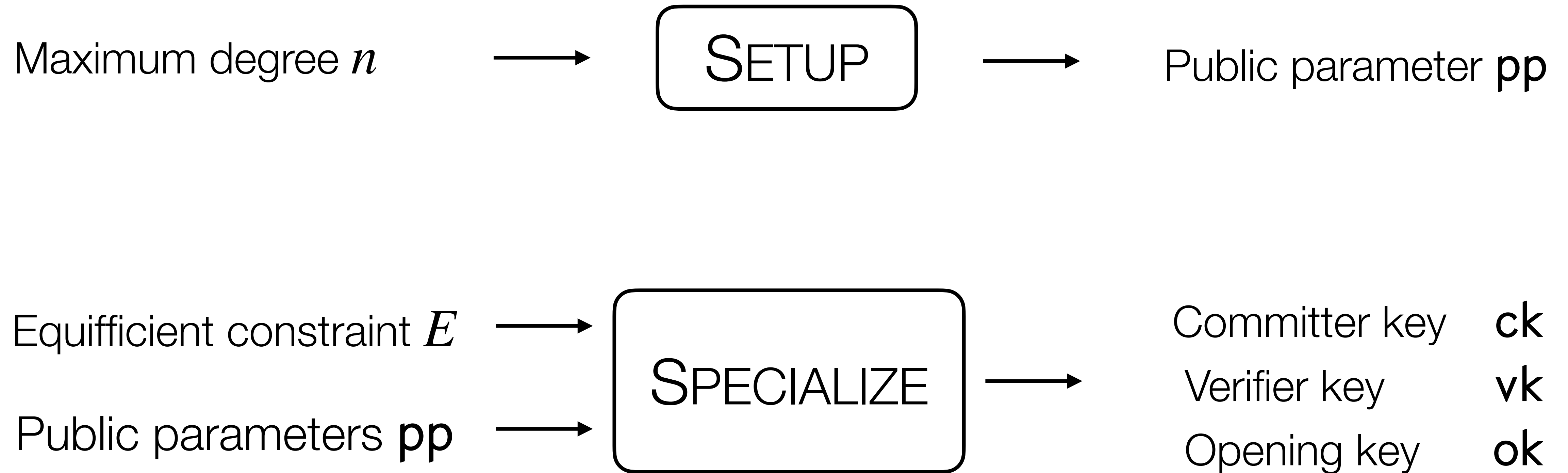
$$\begin{aligned}\hat{z}_A &= z_1 \cdot \hat{a}_1 + \dots + z_n \cdot \hat{a}_n \\ \hat{z}_B &= z_1 \cdot \hat{b}_1 + \dots + z_n \cdot \hat{b}_n \\ \hat{z}_C &= z_1 \cdot \hat{c}_1 + \dots + z_n \cdot \hat{c}_n\end{aligned}$$

Equiefficient PC (EPC) schemes

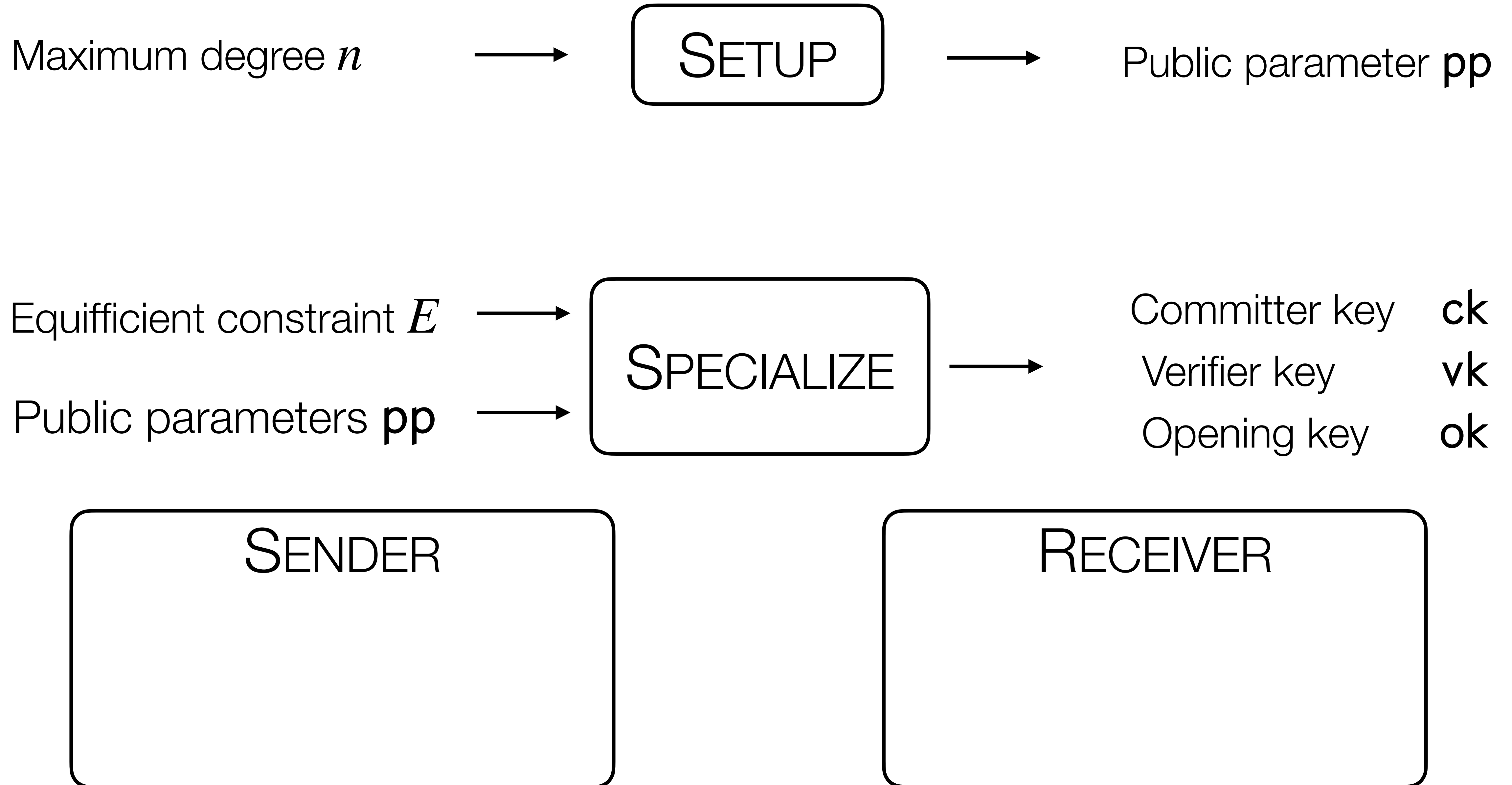
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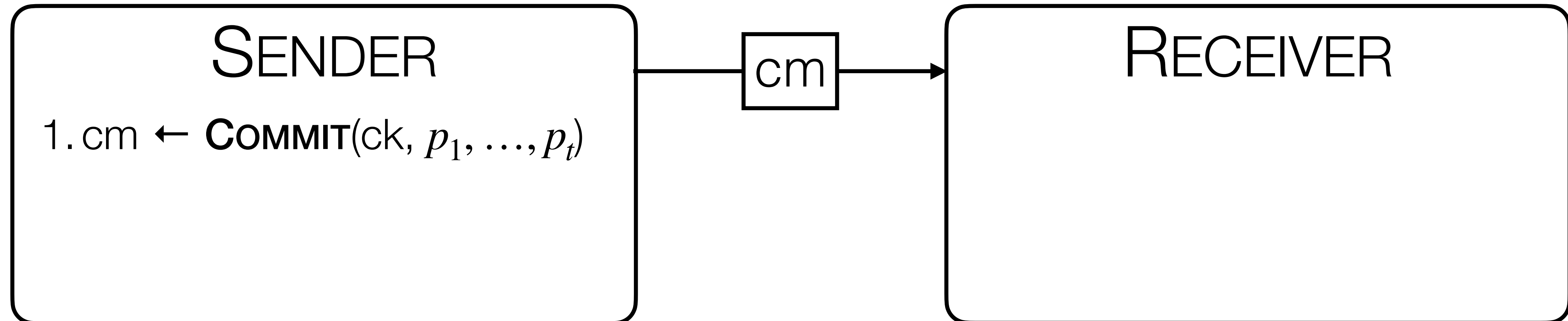


SENDER

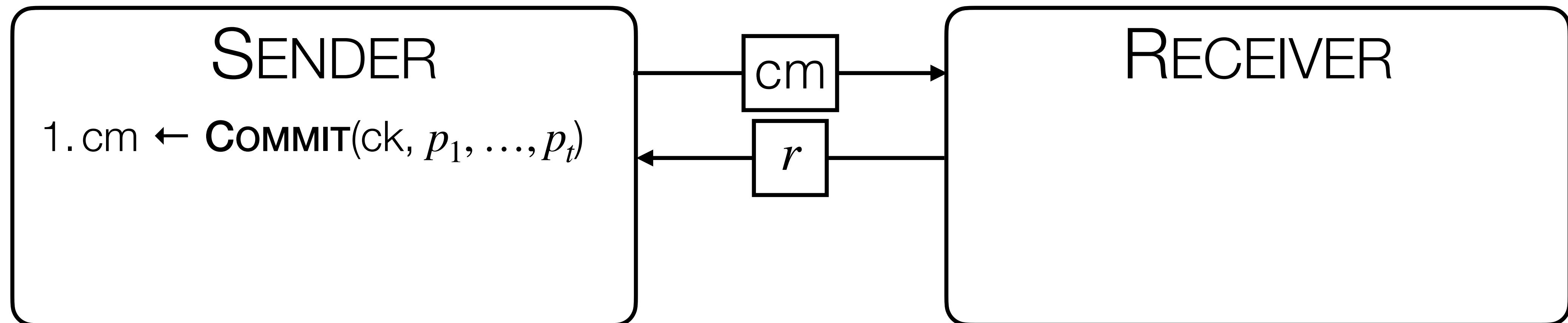
$1.\text{cm} \leftarrow \mathbf{COMMIT}(\mathbf{ck}, p_1, \dots, p_t)$

RECEIVER

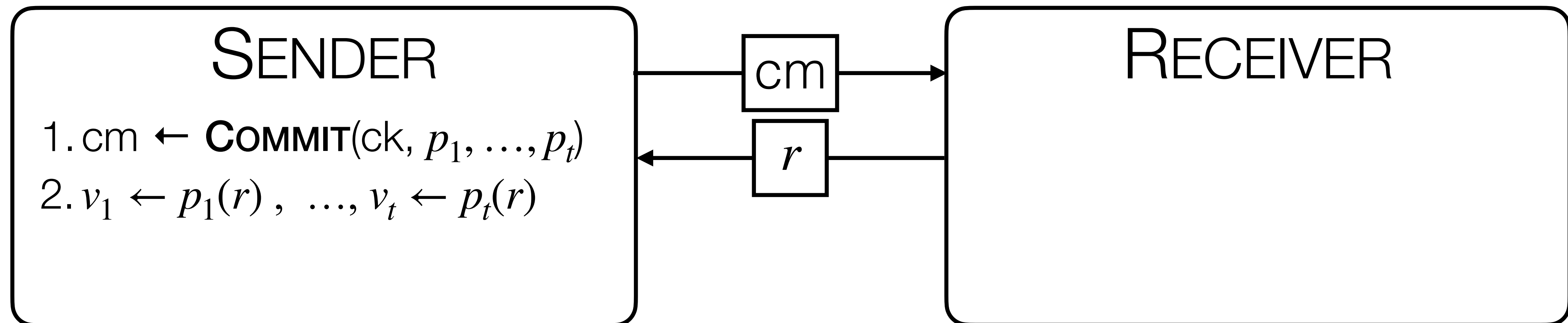
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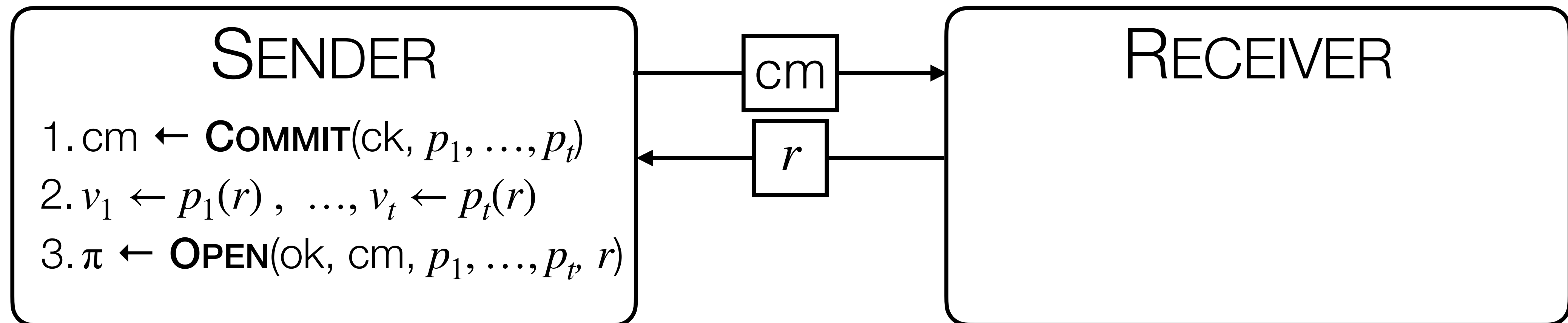
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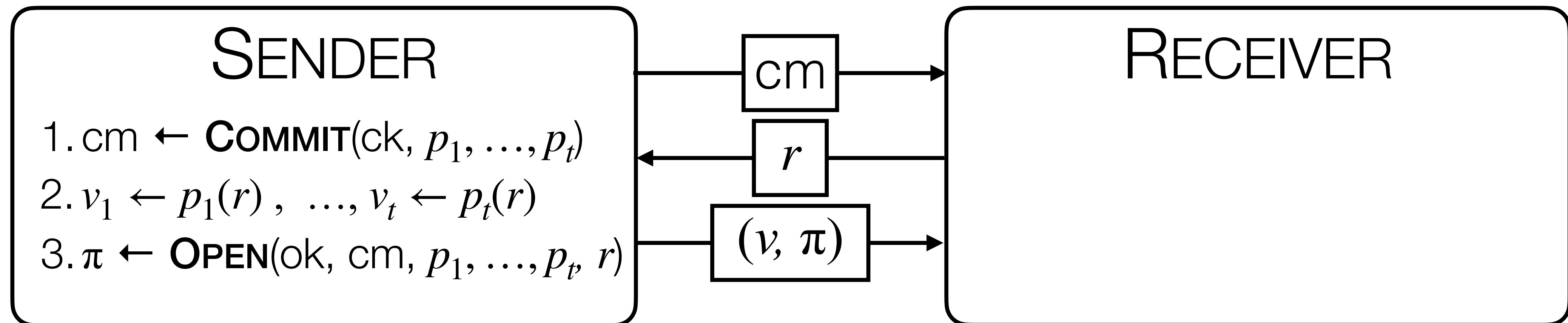
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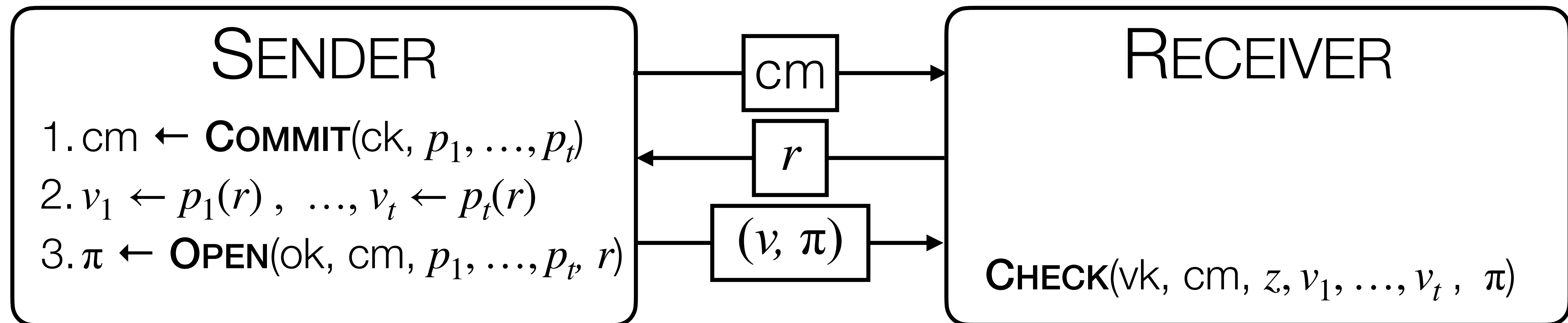
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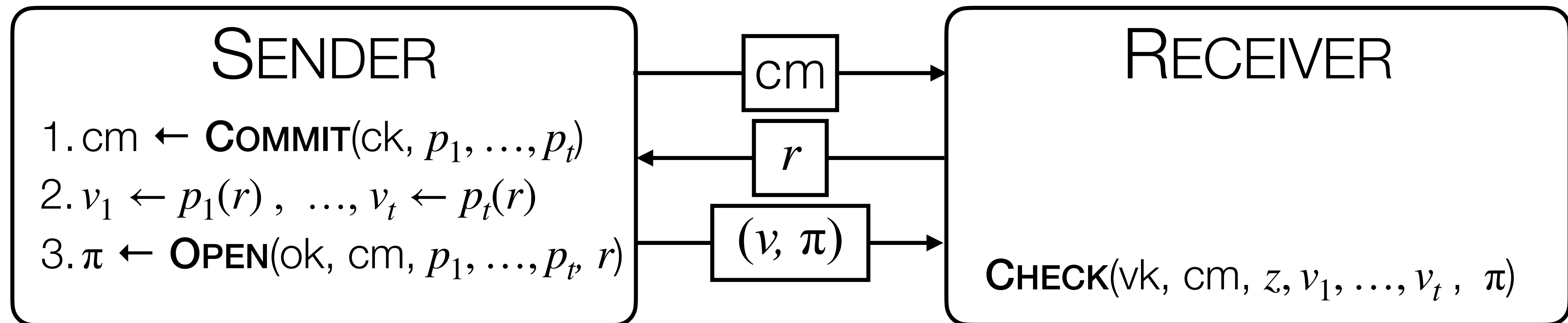
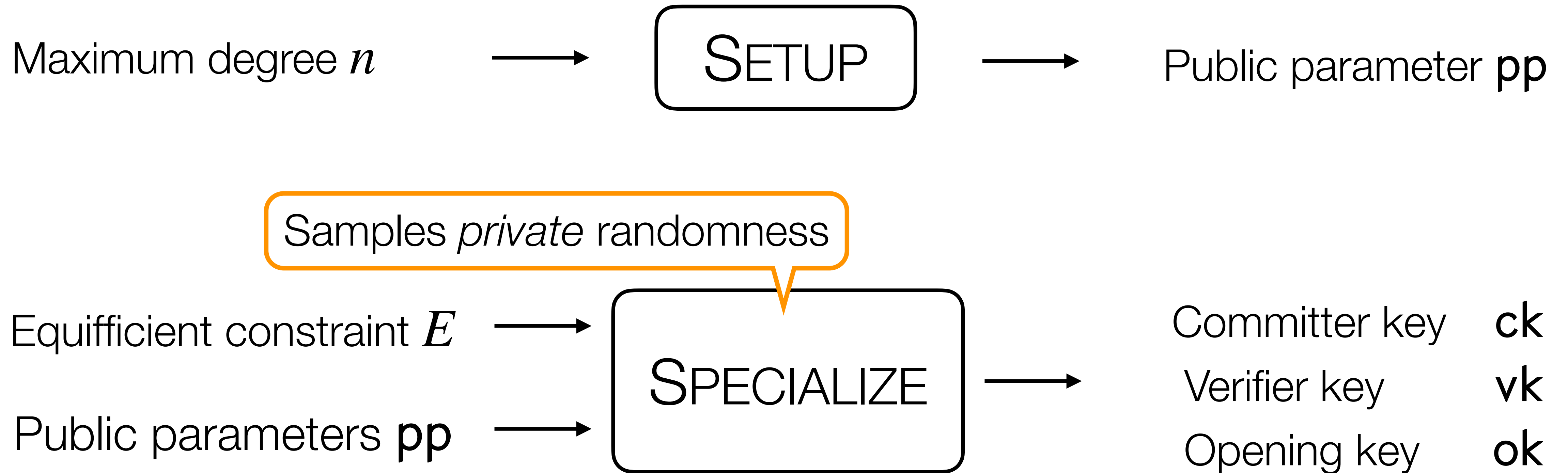
Equiefficient PC (EPC) schemes



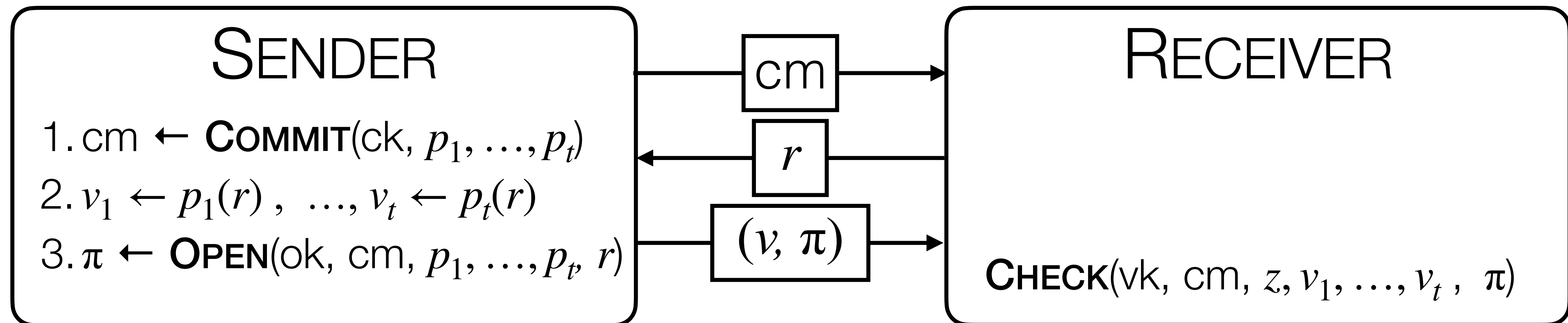
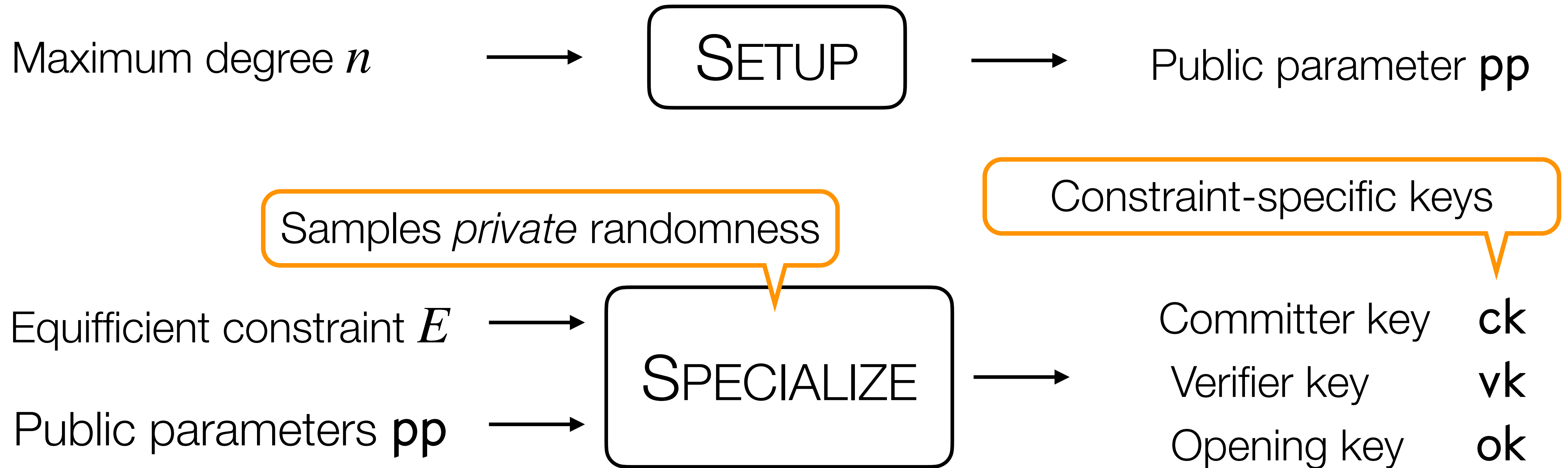
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Properties of EPC schemes

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Completeness:

If the committed polynomials

- satisfy the evaluation claims ($p_1(z) = v_1, \dots, p_n(z) = v_n$), and
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then the receiver accepts the evaluation proof

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Extractability

If adversary outputs a commitment & proof that convinces the receiver, then it must know p_1, \dots, p_n such that the following holds:

- **PC Extractability:** $p_1(z) = v_1, \dots, p_n(z) = v_n$
- **Equifflcient constraint satisfaction:** p_1, \dots, p_n are equifflcient wrt E

KZG-based EPC Construction

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$$\text{KZG} . \text{Commit}(\text{ck}, \hat{z}_A) \rightarrow c_A$$

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$$\text{where } c_M := \sum_i z_M[i] \cdot \tau^i \cdot G \quad \text{for } M \in \{A, B, C\}$$

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$$\begin{aligned}\mathcal{A} = \{\hat{a}_1, \dots, \hat{a}_n\} &\iff \text{ck}_A = [\hat{a}_1(\tau)G, \hat{a}_2(\tau)G, \hat{a}_3(\tau)G, \dots, \hat{a}_n(\tau)G] \\ \mathcal{B} = \{\hat{b}_1, \dots, \hat{b}_n\} &\iff \text{ck}_B = [\hat{b}_1(\tau)G, \hat{b}_2(\tau)G, \hat{b}_3(\tau)G, \dots, \hat{b}_n(\tau)G] \\ \mathcal{C} = \{\hat{c}_1, \dots, \hat{c}_n\} &\iff \text{ck}_C = [\hat{c}_1(\tau)G, \hat{c}_2(\tau)G, \hat{c}_3(\tau)G, \dots, \hat{c}_n(\tau)G]\end{aligned}$$

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$$\begin{aligned} c^* &= \langle z, \text{ck}^* \rangle \\ &= \alpha \cdot (z_1 \cdot \hat{a}_1(\tau) + z_2 \cdot \hat{a}_2(\tau) + \dots + z_n \cdot \hat{a}_n(\tau)) \cdot G + \\ &\quad \beta \cdot (z_1 \cdot \hat{b}_1(\tau) + z_2 \cdot \hat{b}_2(\tau) + \dots + z_n \cdot \hat{b}_n(\tau)) \cdot G + \\ &\quad \gamma \cdot (z_1 \cdot \hat{c}_1(\tau) + z_2 \cdot \hat{c}_2(\tau) + \dots + z_n \cdot \hat{c}_n(\tau)) \cdot G \end{aligned}$$

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$$\text{Commit}(\text{ck}, (z_A(X), z_B(X), z_C(X))) \rightarrow c_A, c_B, c_C, c^*$$

$$c_A = z_A(\tau) \cdot G, \quad c_B = z_B(\tau) \cdot G, \quad c_C = z_C(\tau) \cdot G$$

**Consistency
Commitment**

$$c^* = (\alpha \cdot \hat{z}_A(\tau) + \beta \cdot \hat{z}_B(\tau) + \gamma \cdot \hat{z}_C(\tau)) \cdot G$$

KZG-based EPC Construction

Equifflcient

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KZG-based EPC Construction

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Step 4: Now, in the EPC check, do regular KZG verifications for each of c_A , c_B and c_C plus a consistency check using our new commitment c^*

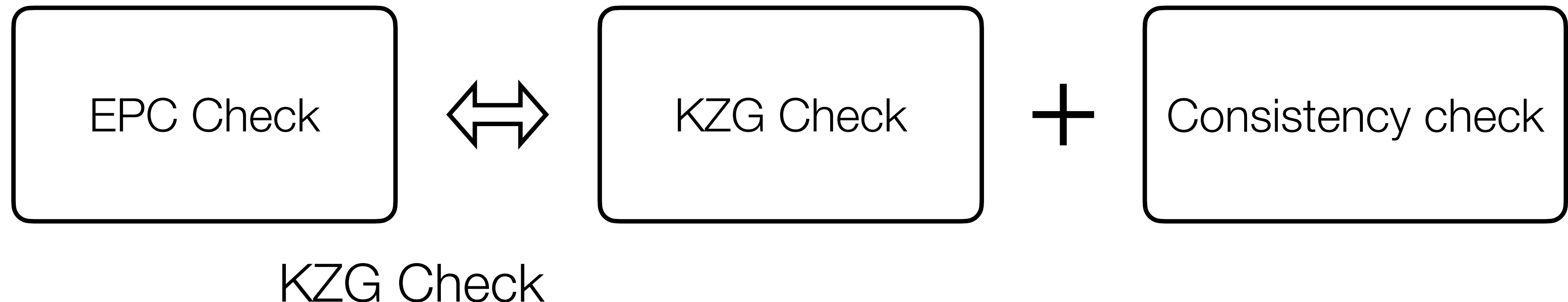
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Pass/fail \leftarrow **KZG.CHECK**(vk, c_A , v_A , π_A)

Pass/fail \leftarrow **KZG.CHECK**(vk, c_B , v_B , π_B)

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⋮

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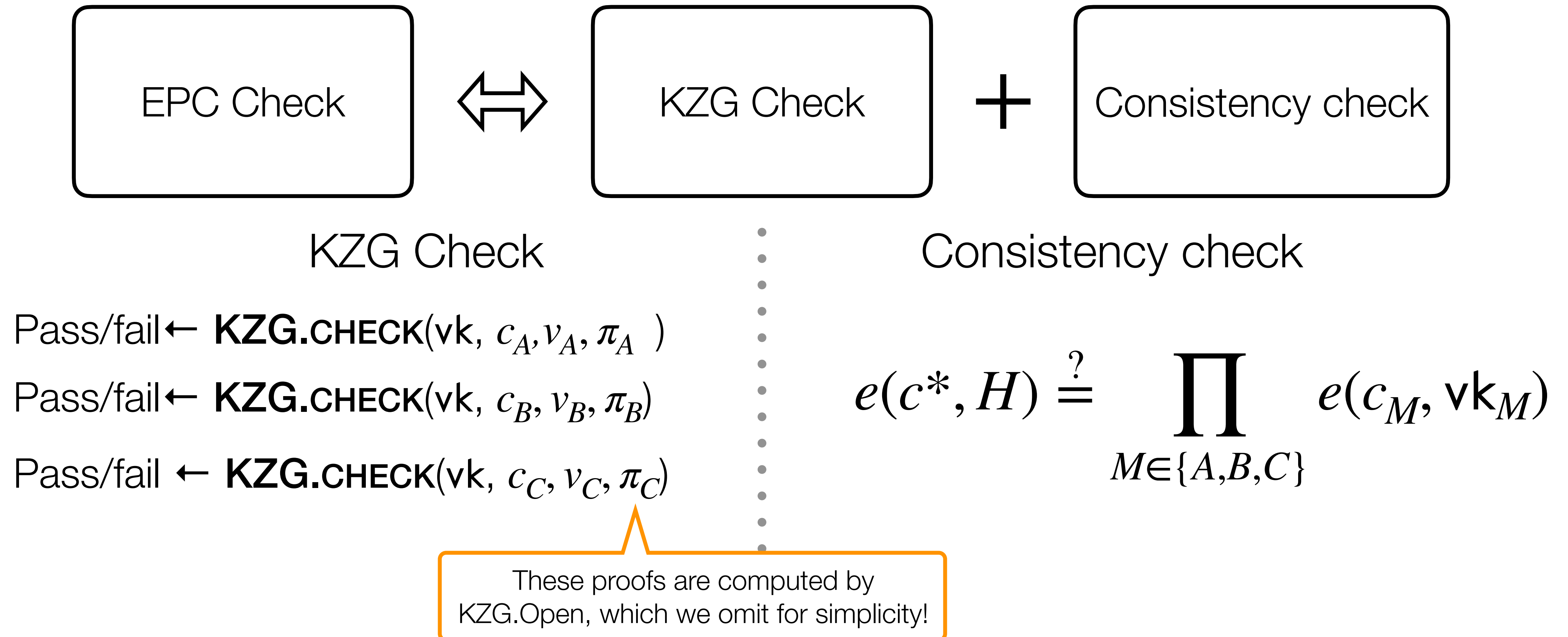
Pass/fail \leftarrow **KZG.CHECK**(vk, c_C , v_C , π_C)

Consistency check

$$e(c^*, H) \stackrel{?}{=} \prod_{M \in \{A, B, C\}} e(c_M, \text{vk}_M)$$

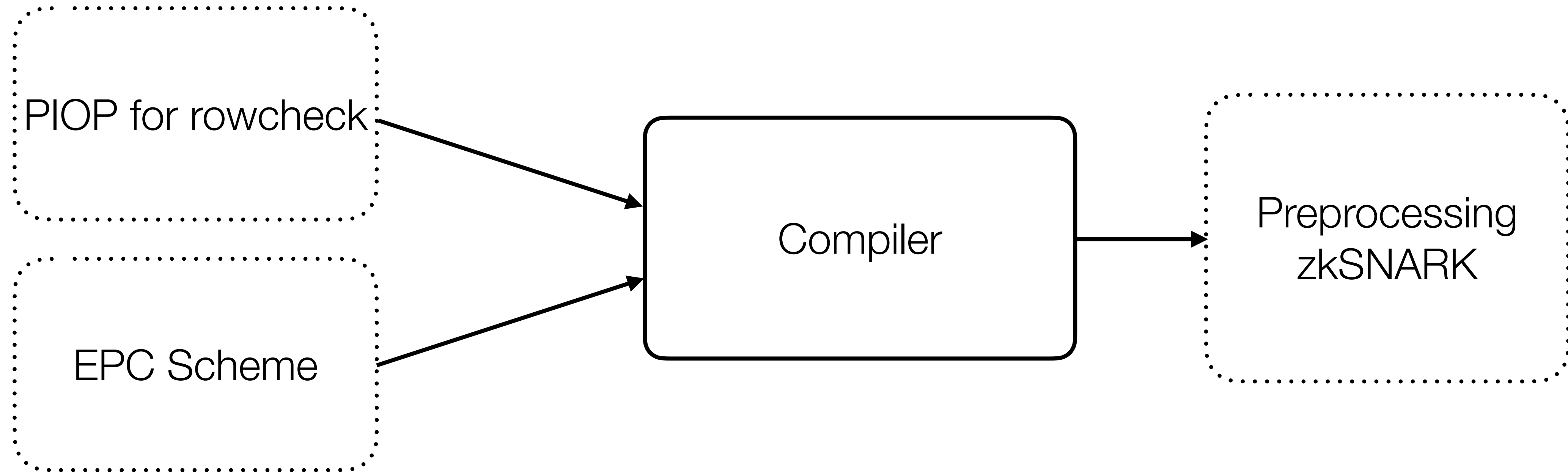
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Our SNARK Construction

R1CS SNARKs from PIOPs + EPC Schemes



PIOPs + EPC Schemes \rightarrow SNARK

$\text{SETUP}(1^\lambda, A, B, C)$

n



$\text{PIOP}(A, B, C)$

pp



$\text{EPC.SETUP}(n)$

$\text{EPC.SPECIALIZE}(\text{pp}, E = (\mathcal{A}, \mathcal{B}, \mathcal{C}))$



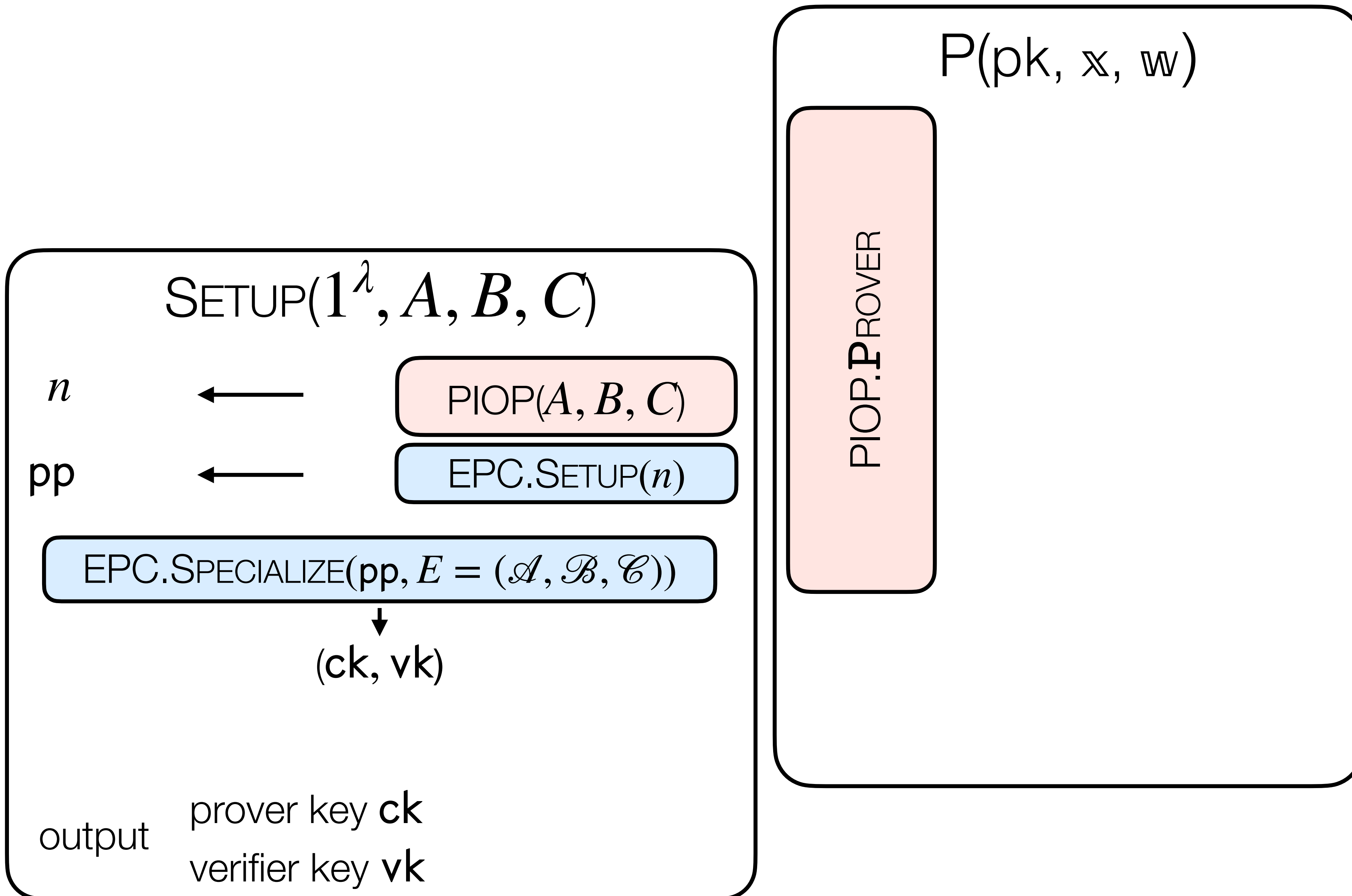
(ck, vk)

output

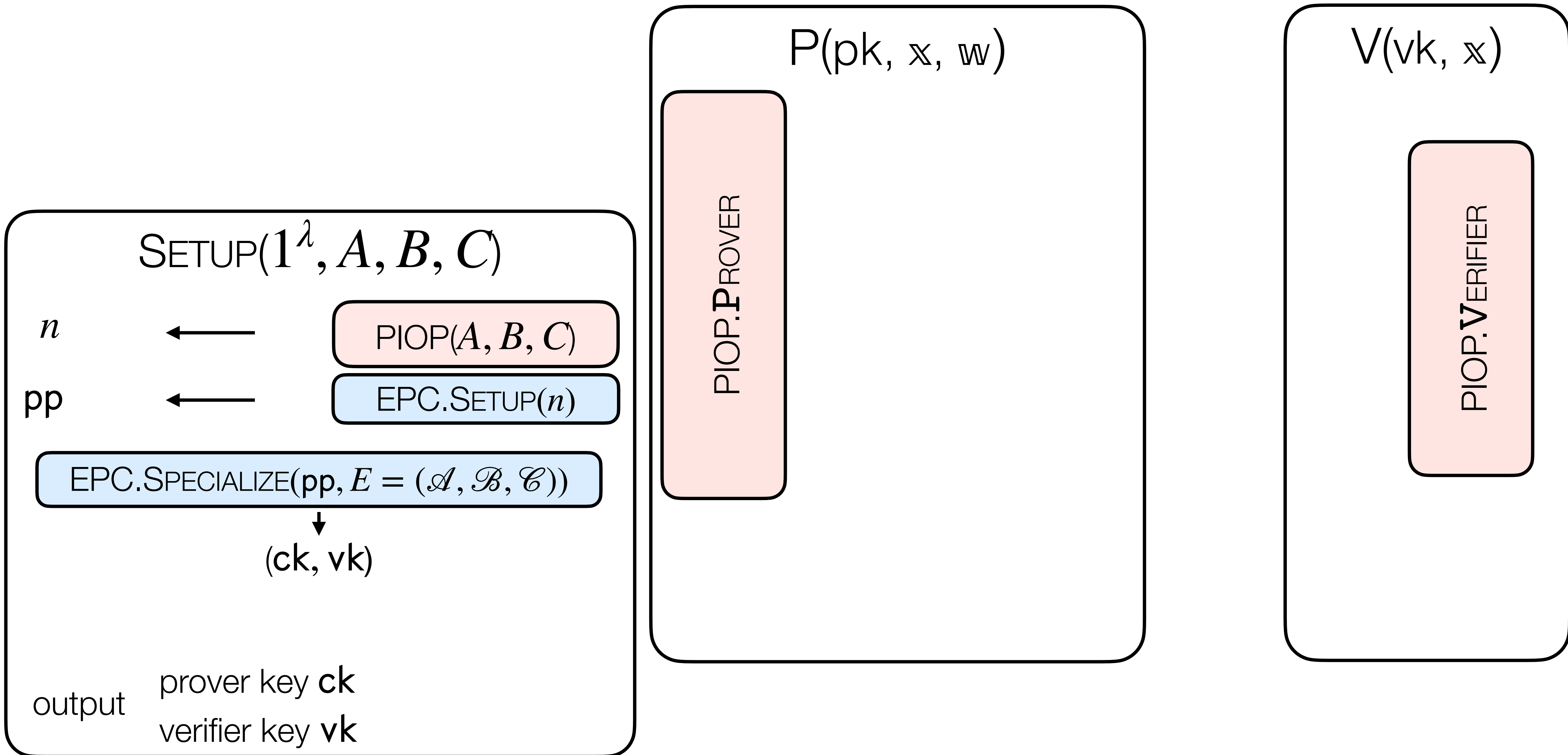
prover key ck

verifier key vk

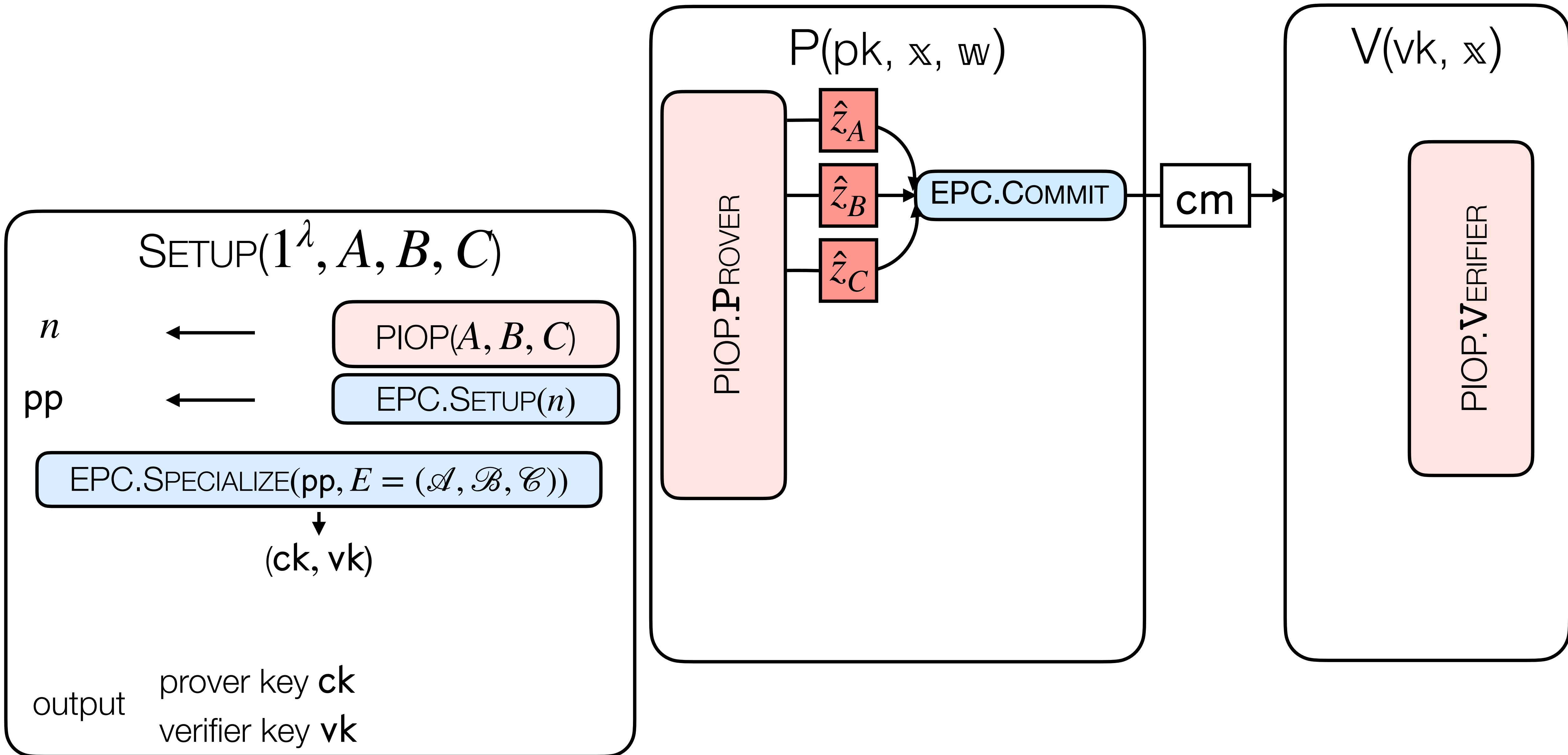
PIOPs + EPC Schemes \rightarrow SNARK



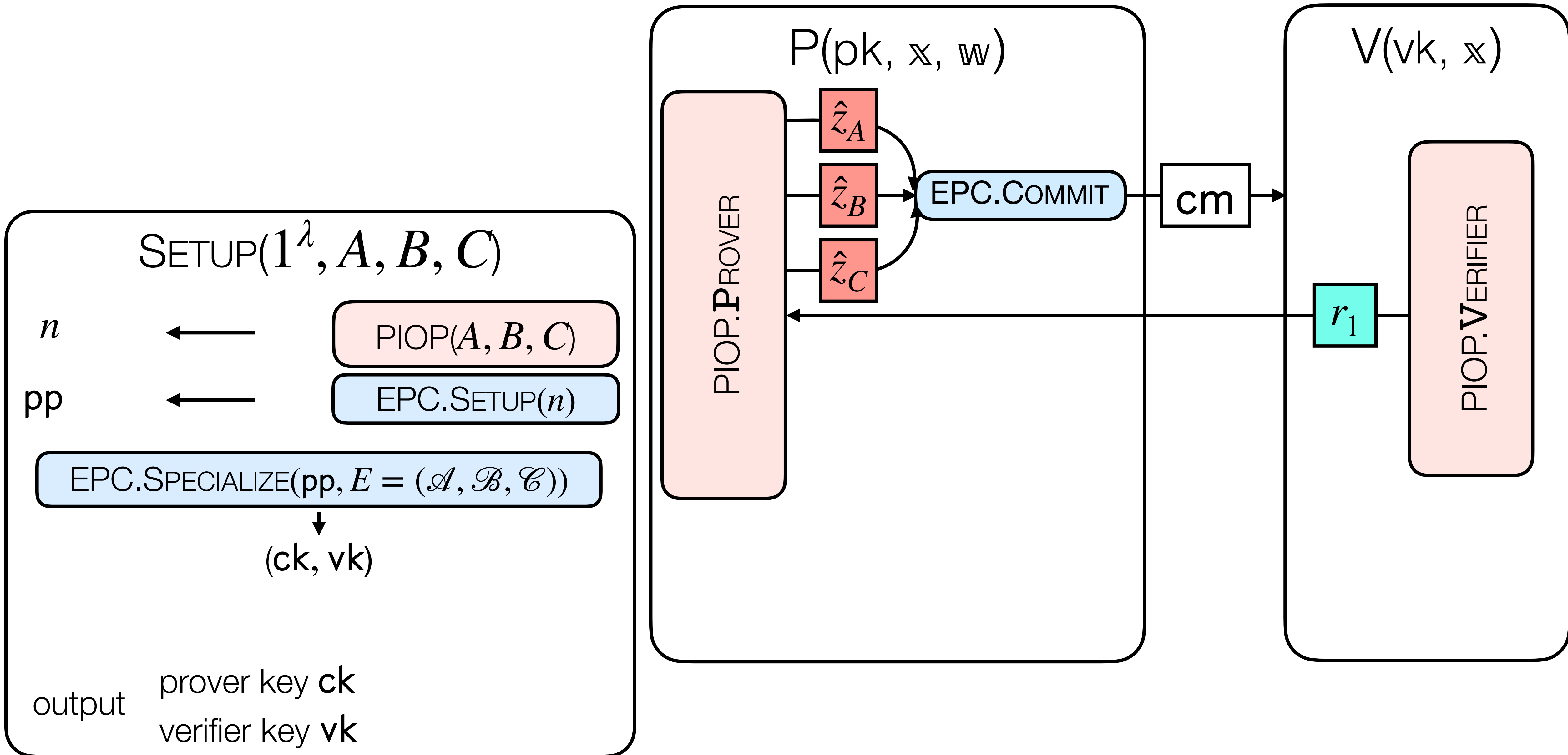
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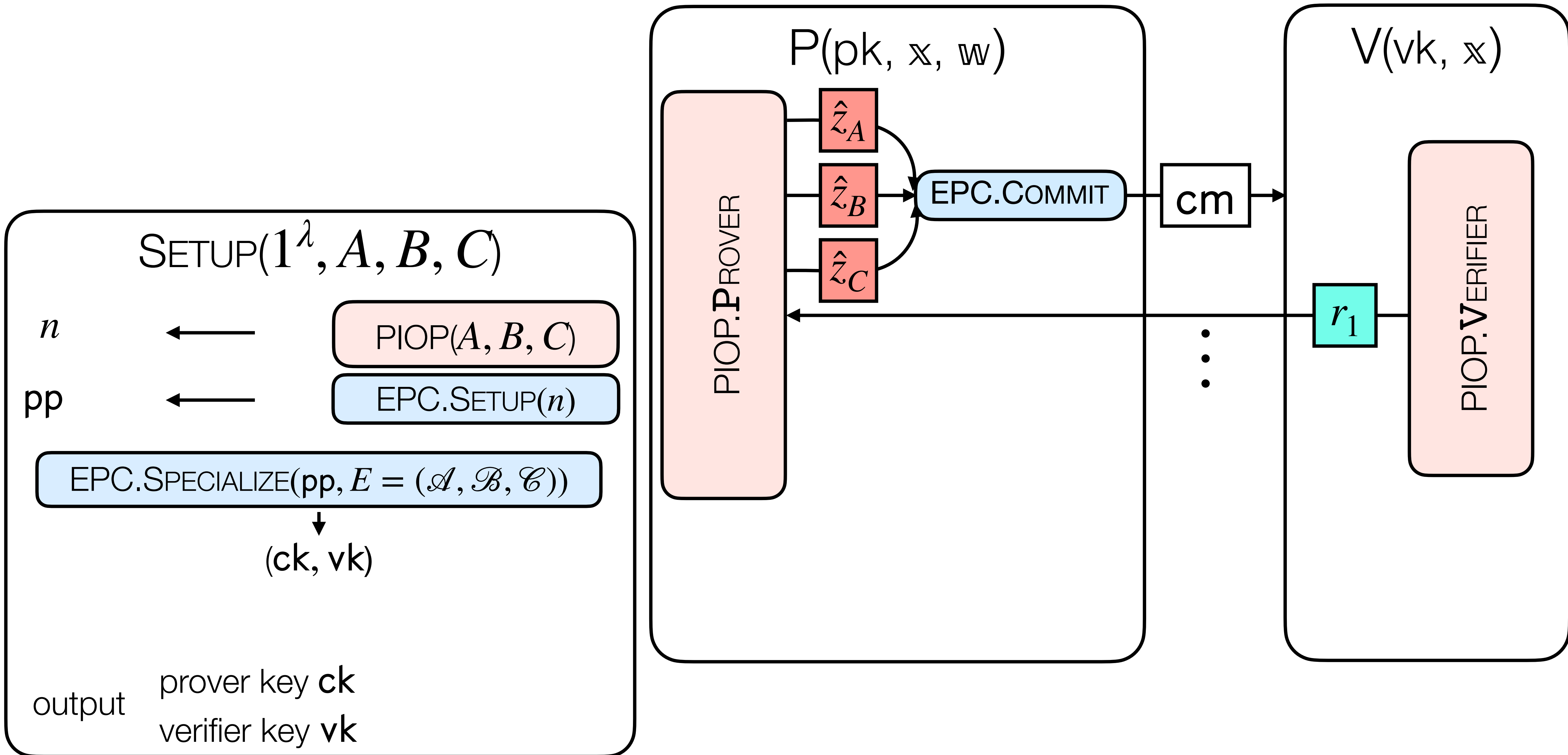
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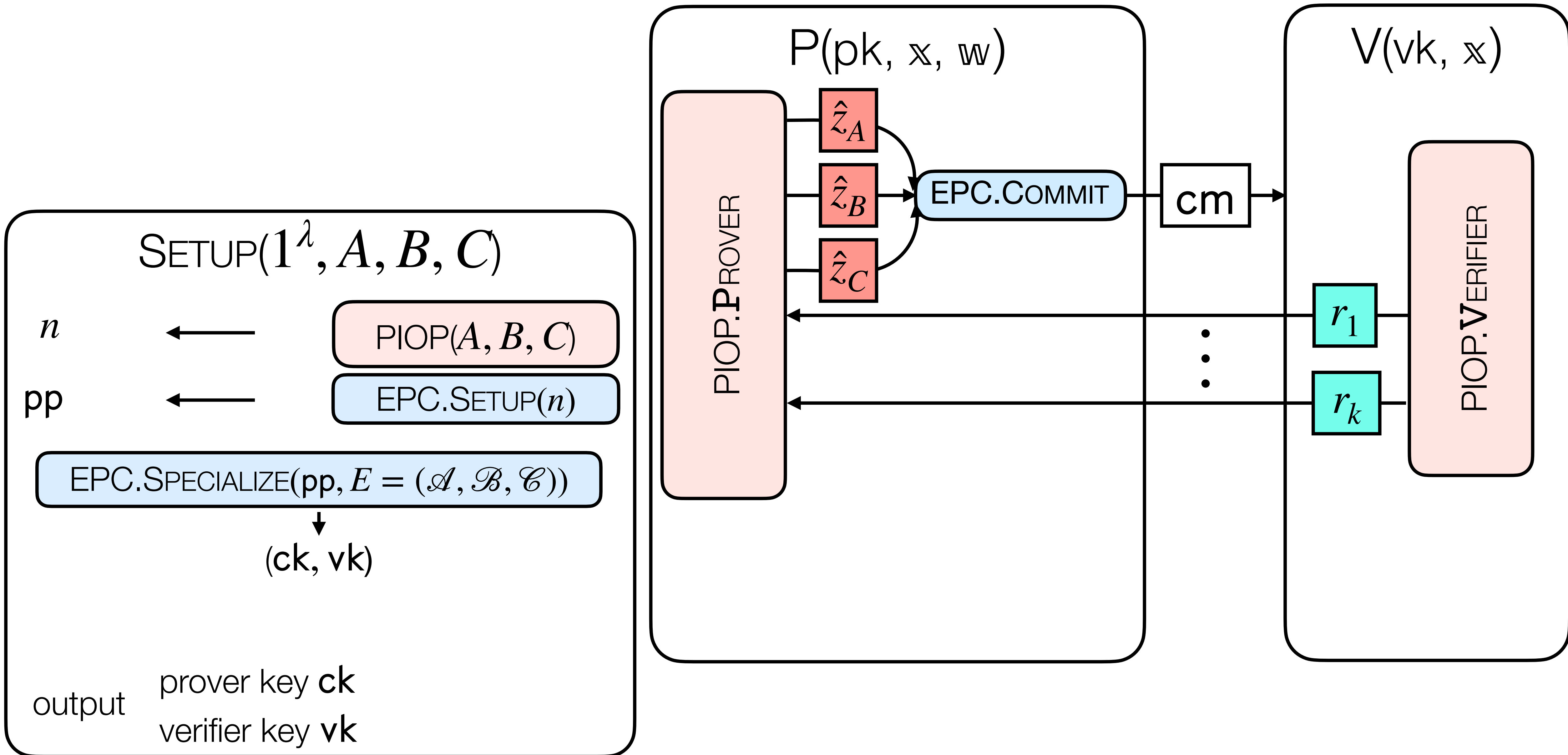
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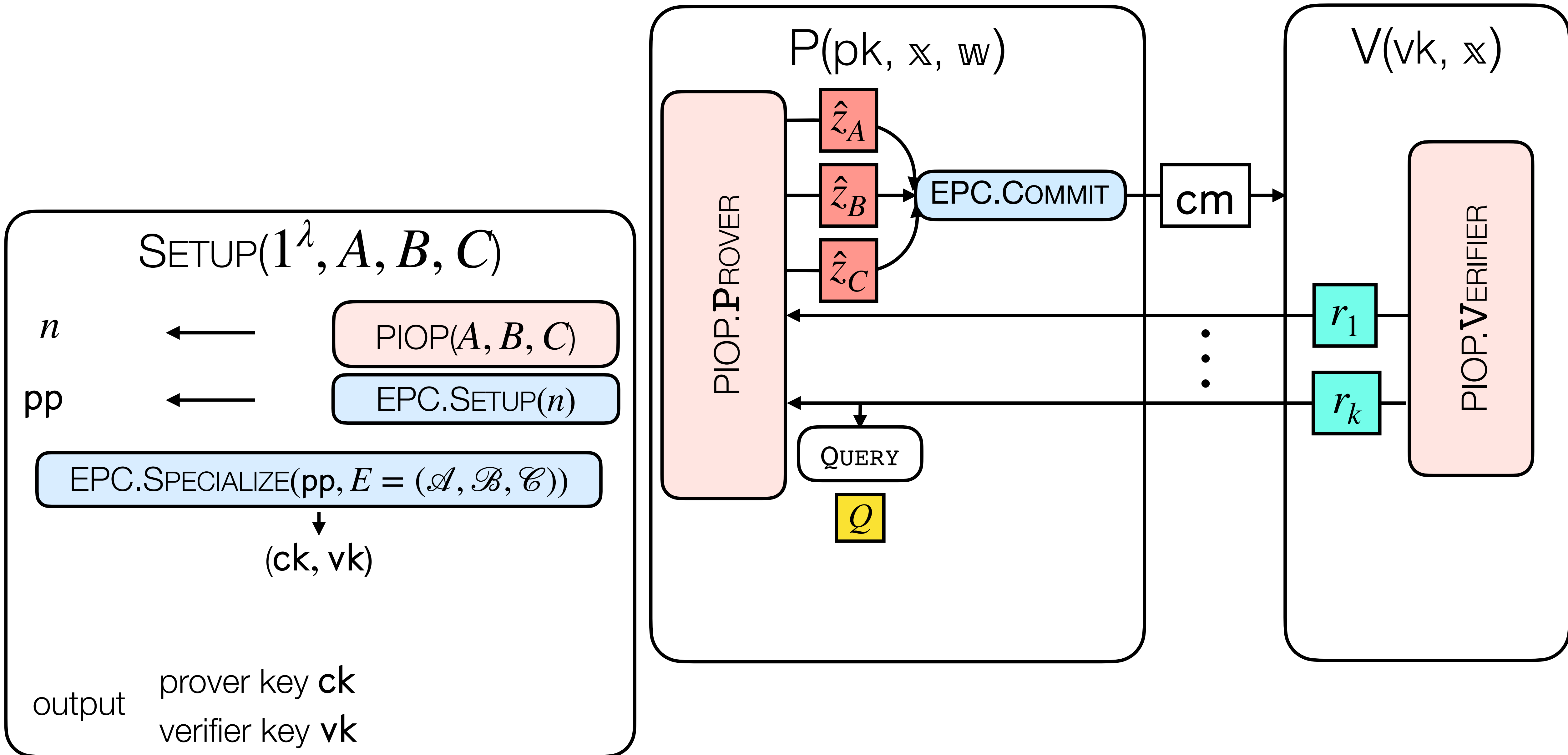
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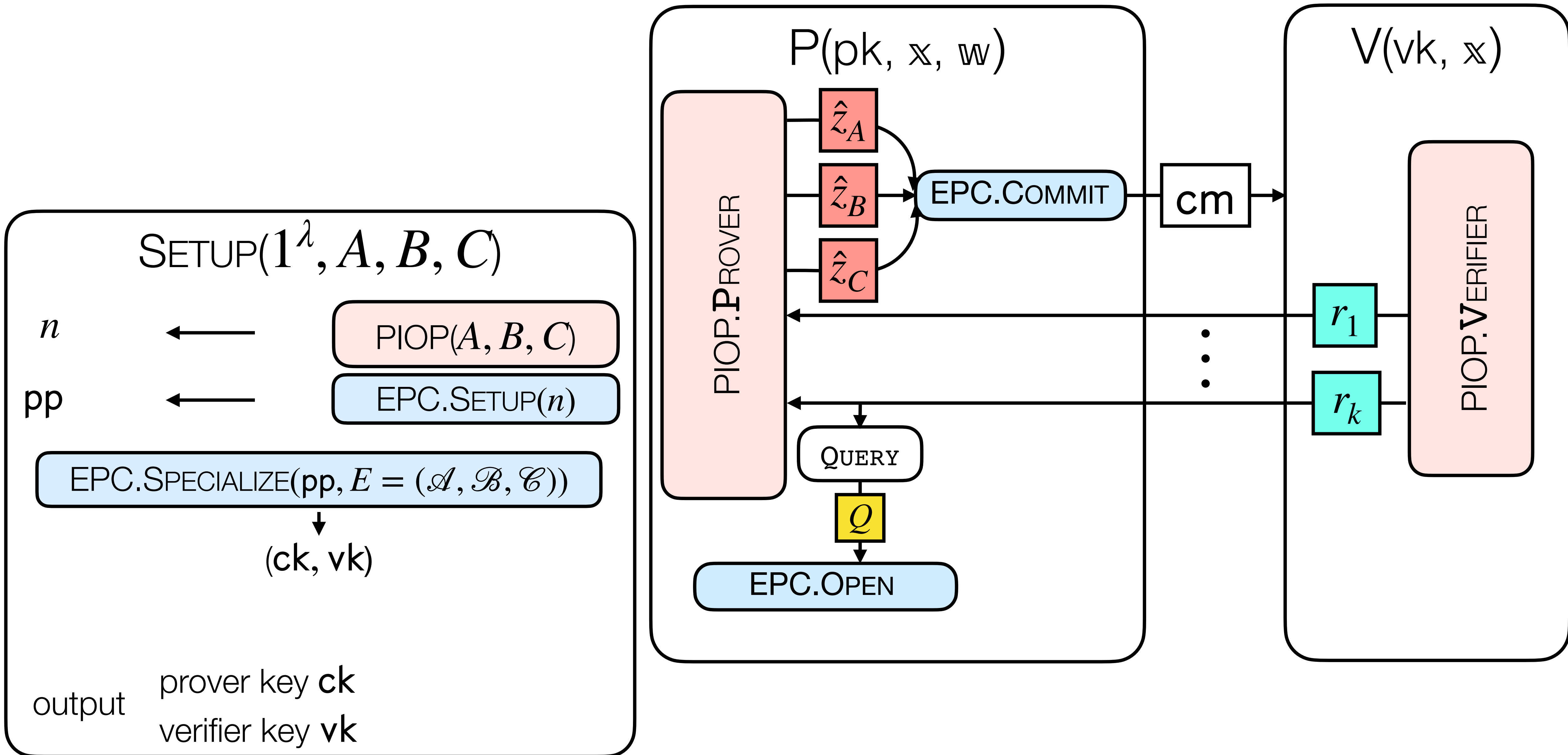
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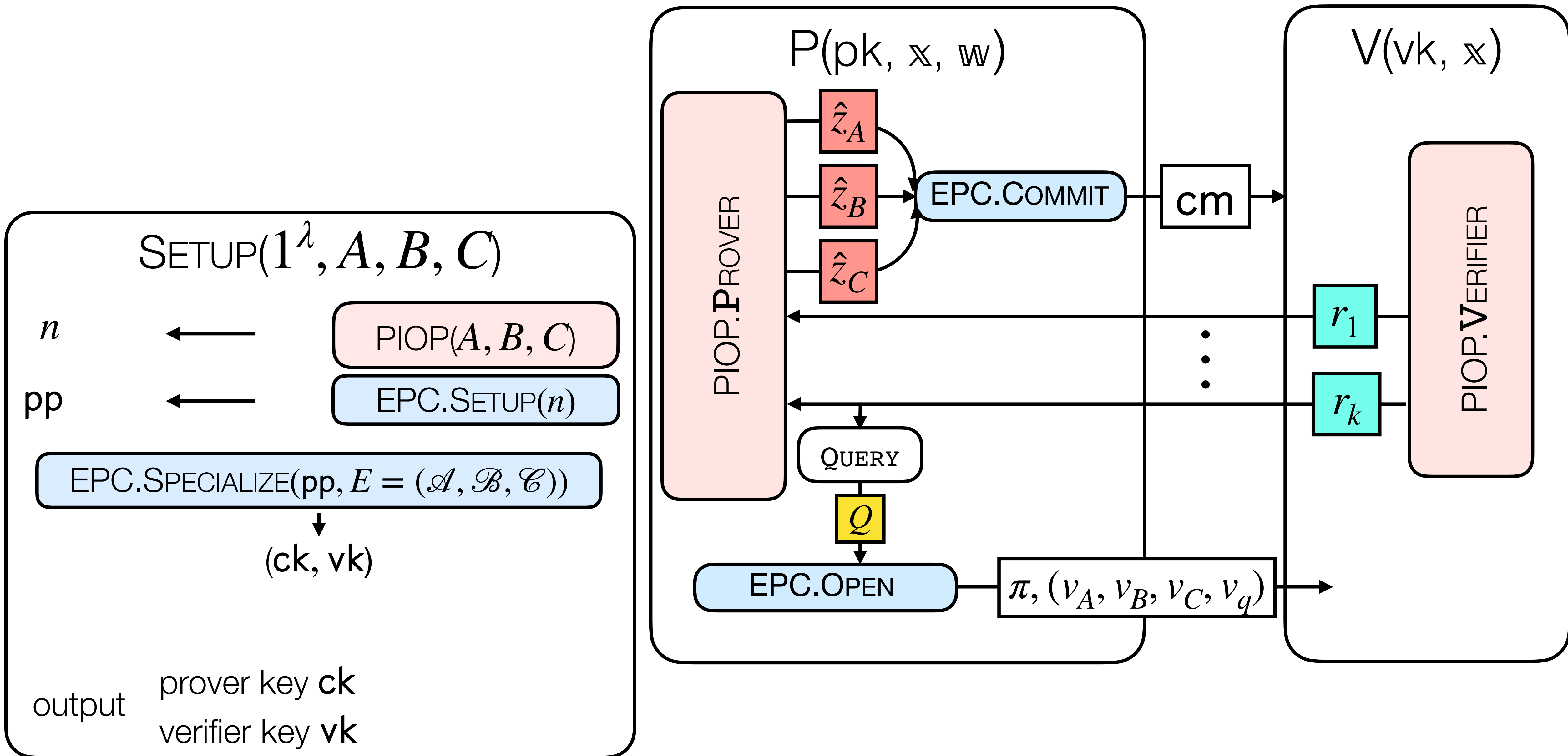
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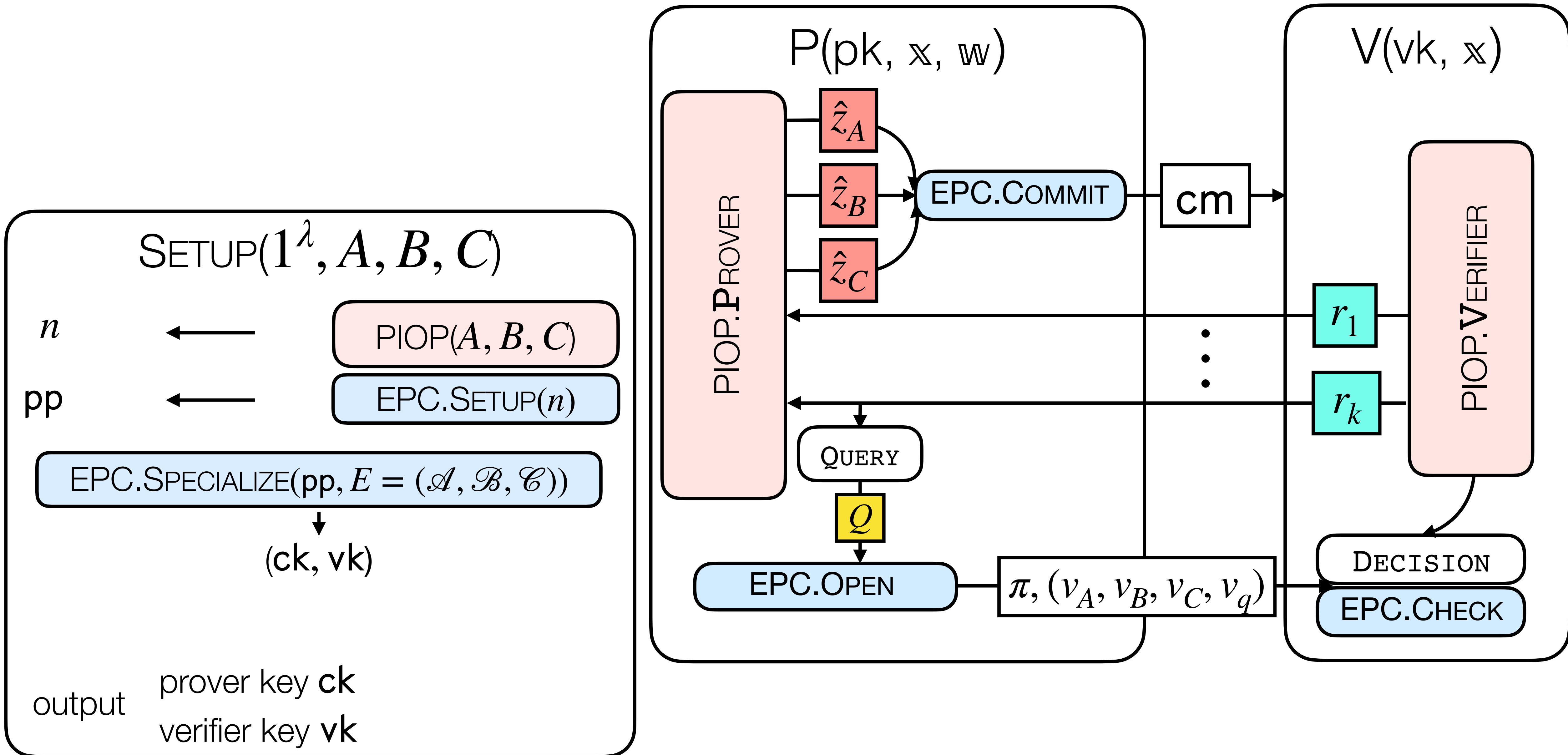
PIOPs + EPC Schemes \rightarrow SNARK



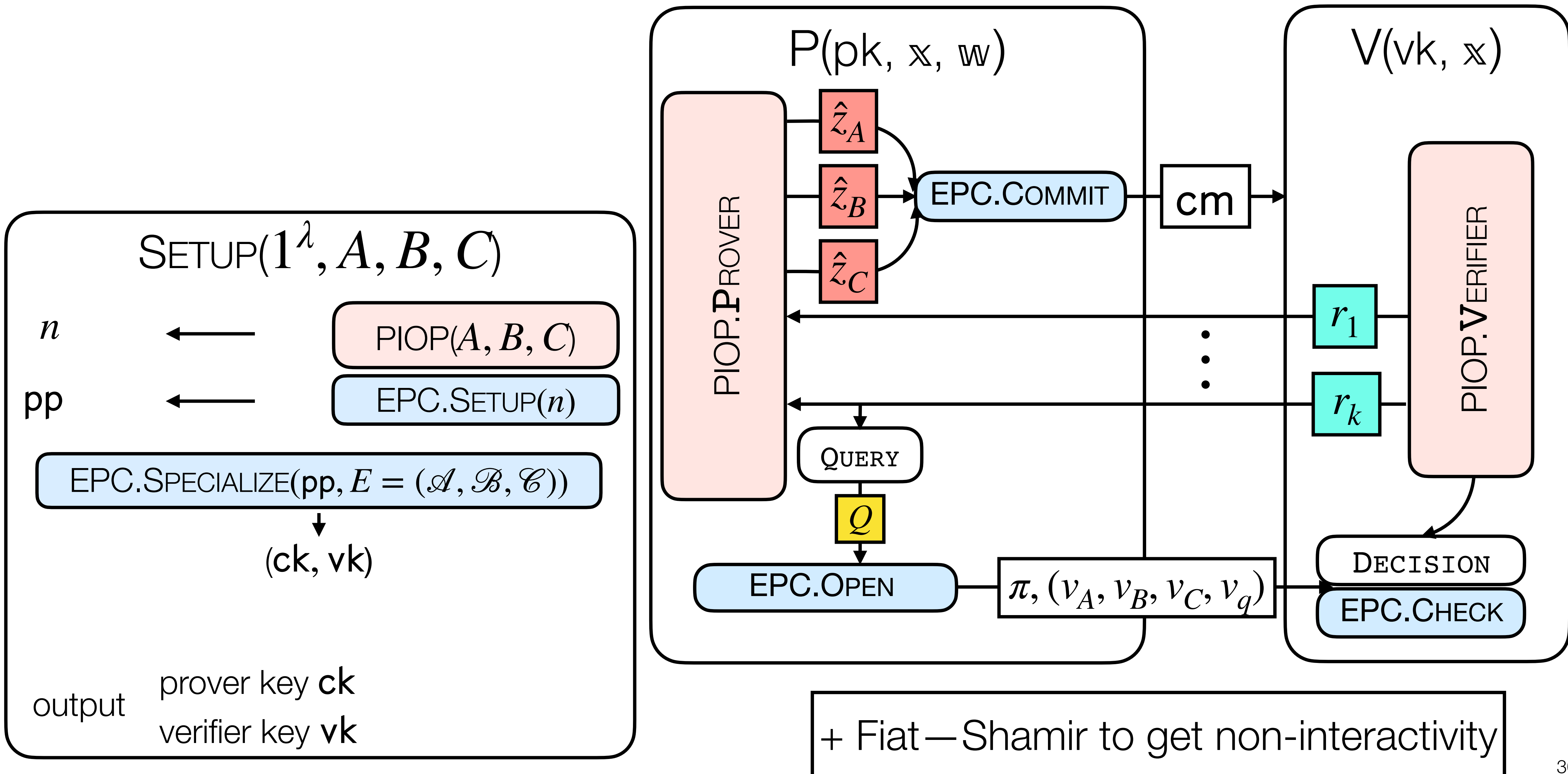
PIOPs + EPC Schemes \rightarrow SNARK



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Whenever Arg.V accepts but R1CS is not satisfied, then we can construct an adversary that either breaks PIOP soundness or EPC extractability.

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PIOP has round-by-round soundness \rightarrow ARG has state-restoration PoK [BCS16]

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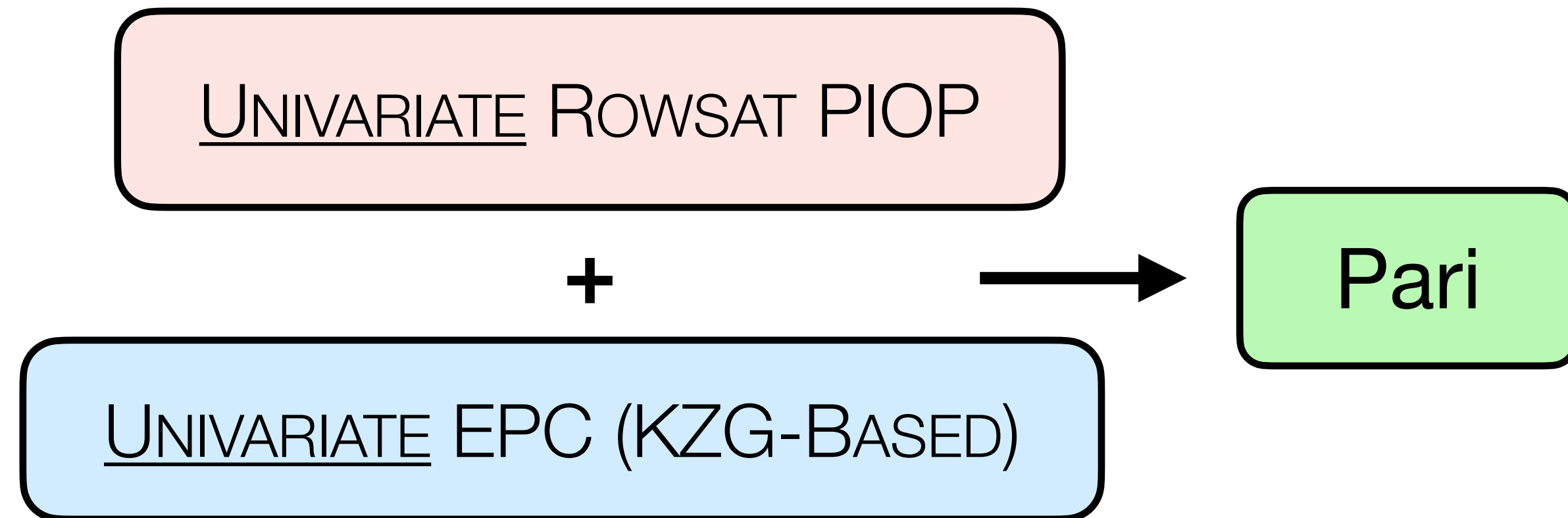
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Note: Our construction does not achieve Zero-knowledge; we leave this to future work

Instantiations: Garuda and Pari

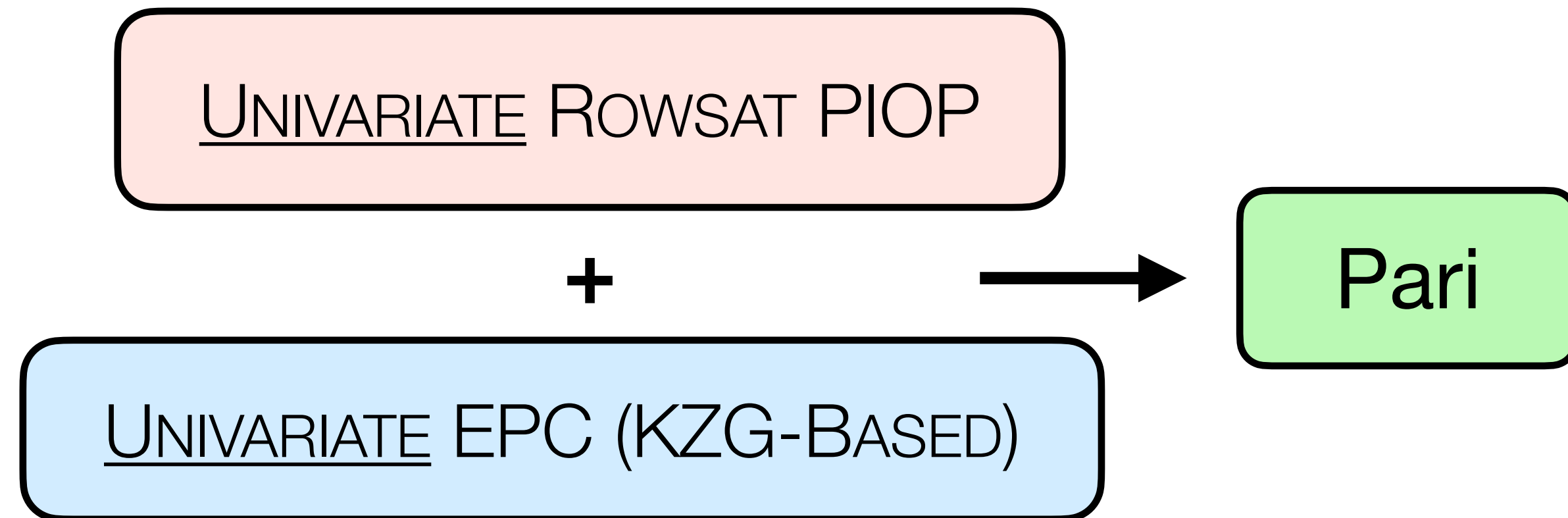
[illegible]

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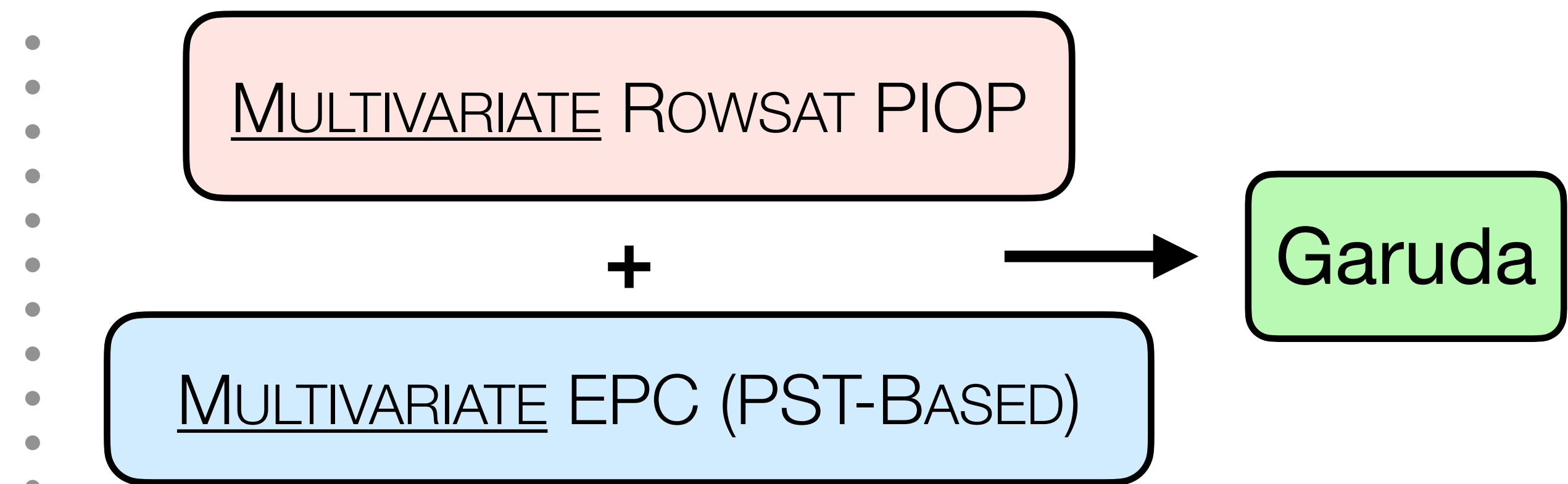


- SNARK for Square R1CS [GM17]
- Quasi-Linear Prover
- Verification needs 3 pairings
- Proof size 2 field + 2 group elements

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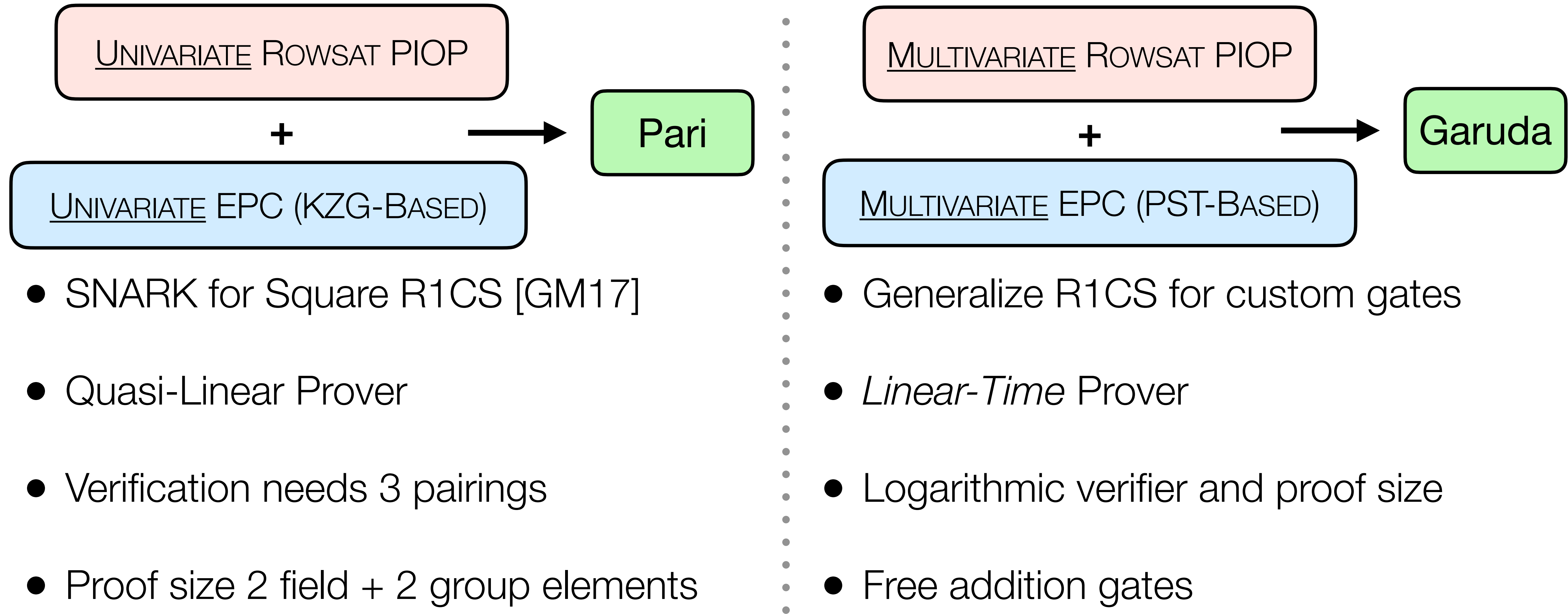


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- Generalize R1CS for custom gates
- *Linear-Time* Prover
- Logarithmic verifier and proof size
- Free addition gates

Instantiations: Garuda and Pari



Both require circuit-specific trusted setup =(

Implementation and Evaluation

Implementation in arkworks

GR1CS programming infrastructure, backward-compatible with R1CS

 **arkworks-rs/snark**

Interfaces for Relations and SNARKs for these relations

 Rust  844  234

 **arkworks-rs/r1cs-std**

R1CS constraints for bits, fields, and elliptic curves

 Rust  157  79

Garuda Implementation + Pari Implementation




Automatic Solidity Smart contract generator for Pari

 **garuda-pari**

A Rust implementation of the Garuda SNARK

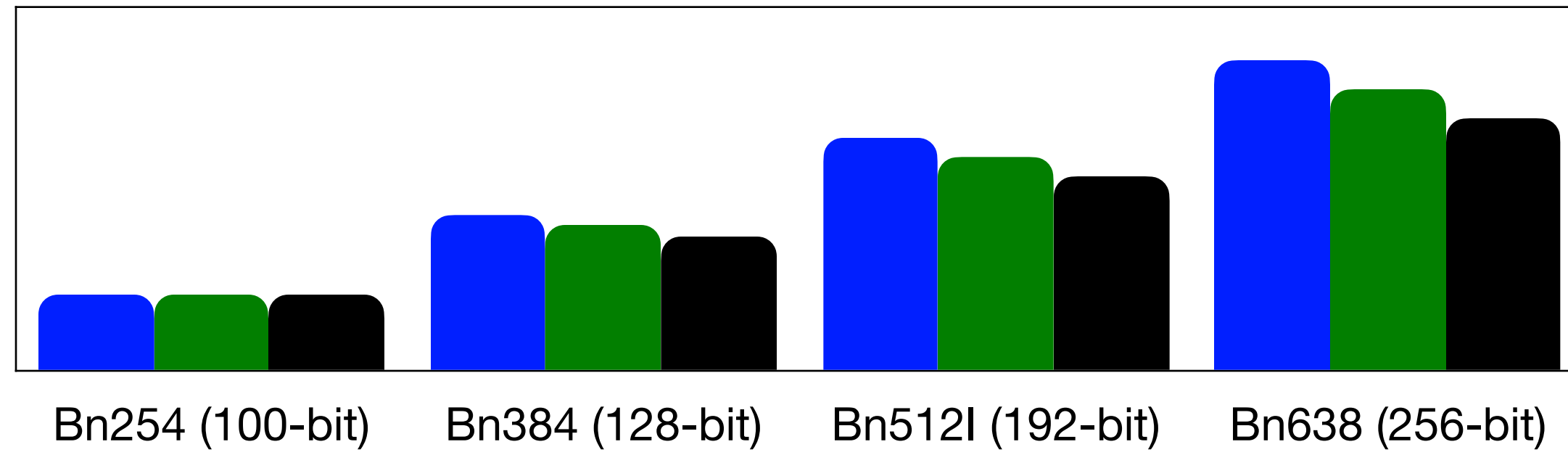
 Rust

Evaluation for Pari

Pari 
Groth16 
Polymath 

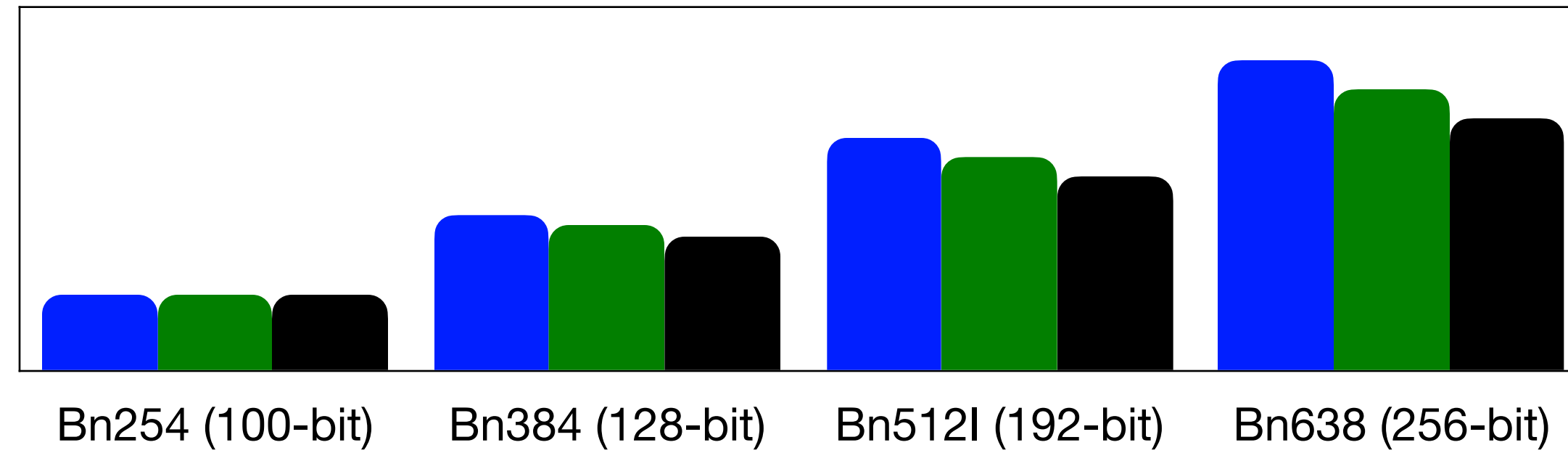
Evaluation for Pari

Proof size for BN curves

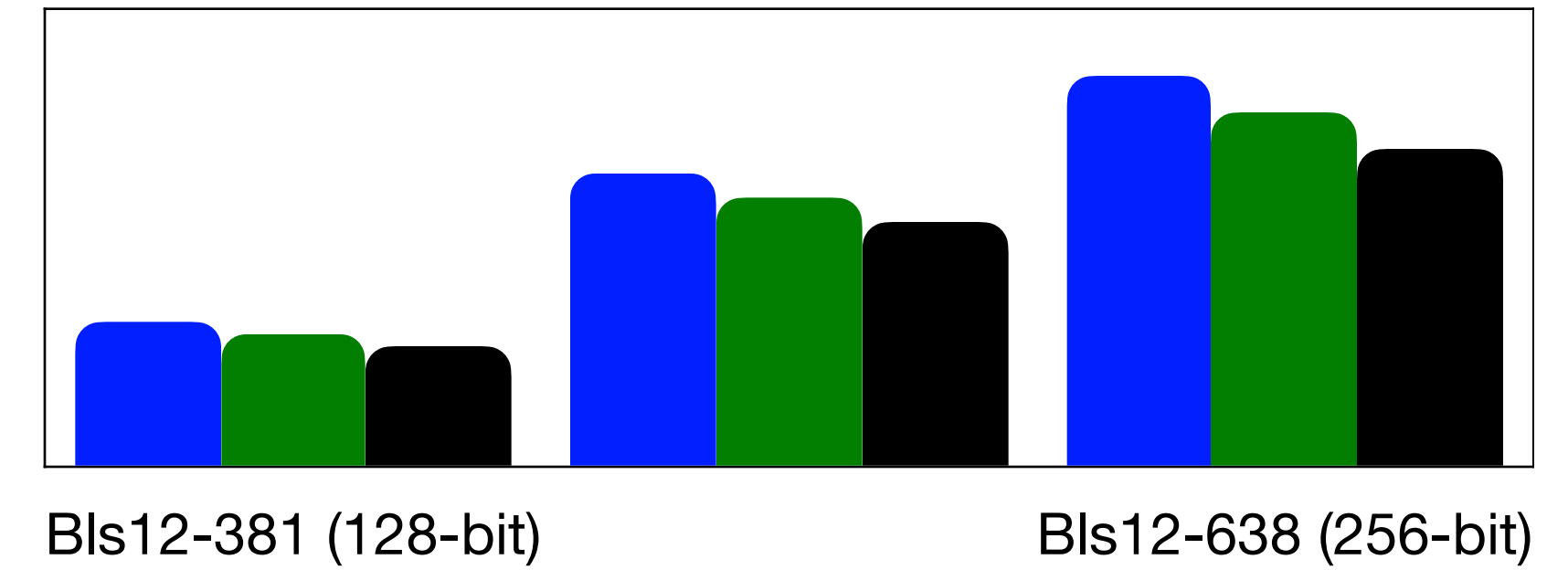



Evaluation for Pari

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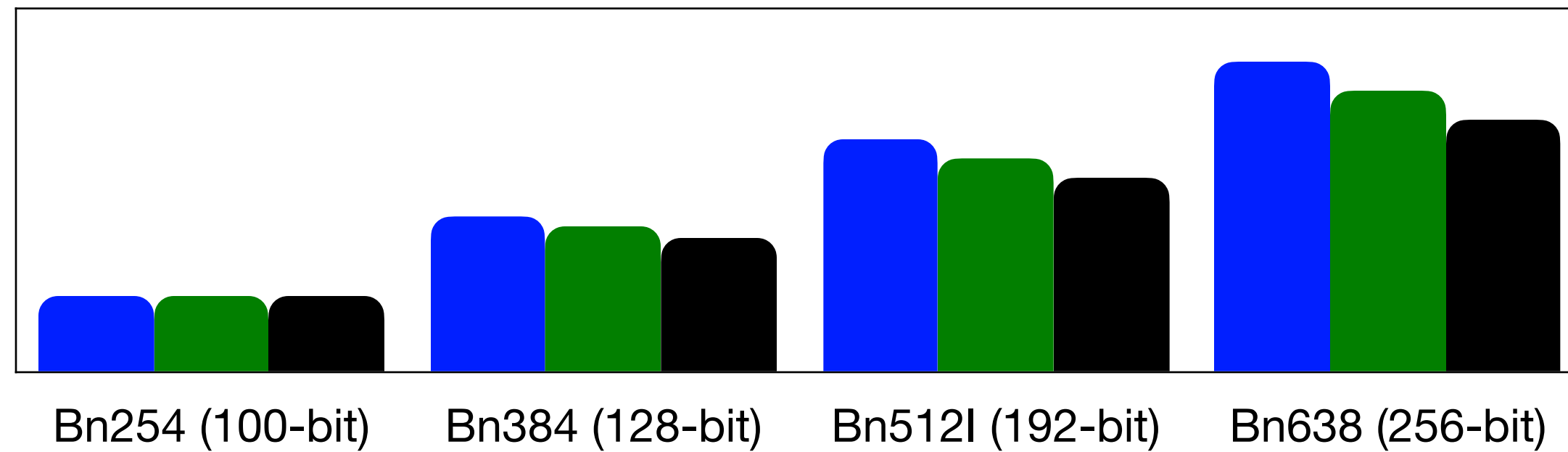
Proof size for BLS curves



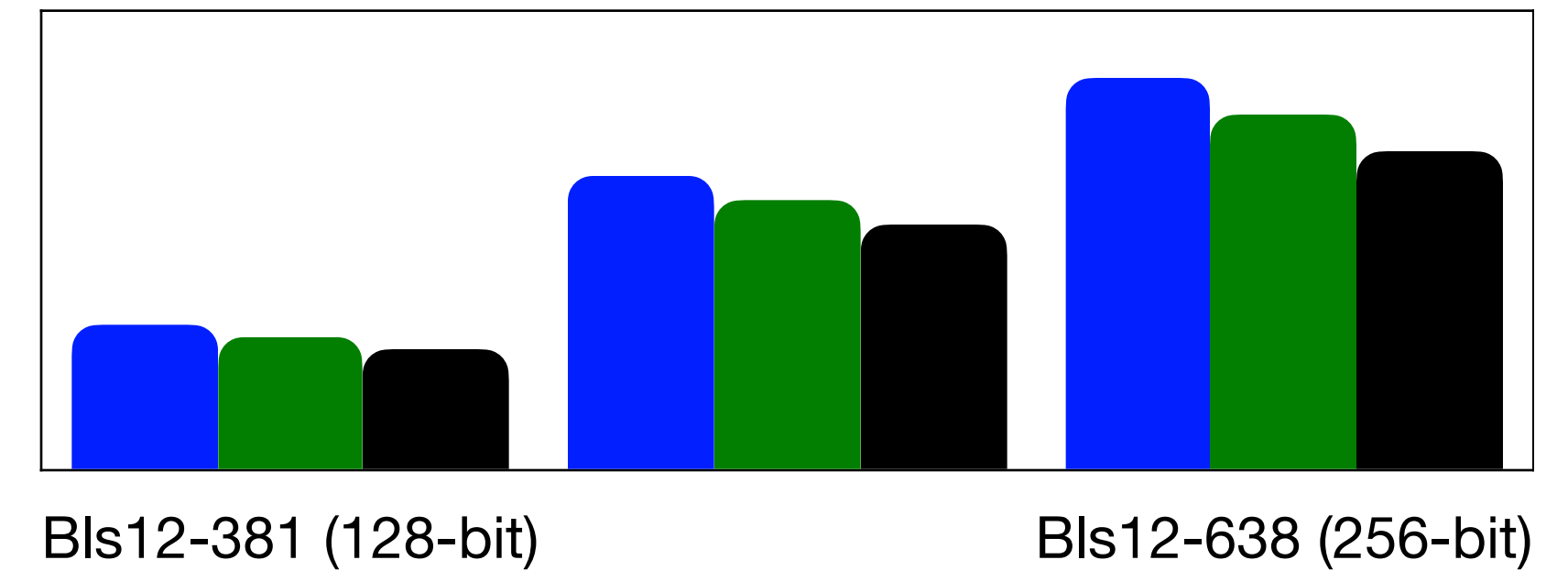
Pari 
Groth16 
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Evaluation for Pari

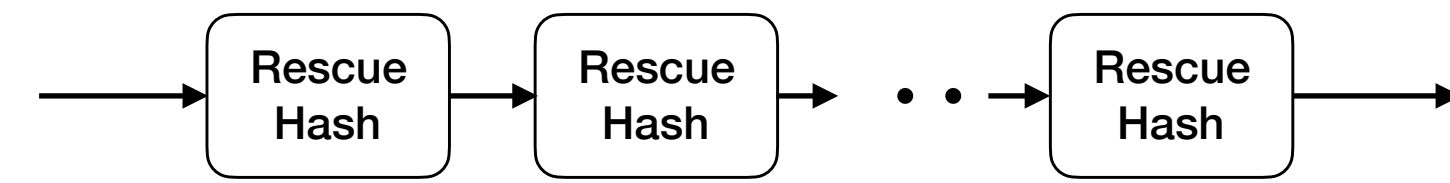
Proof size for BN curves



Proof size for BLS curves



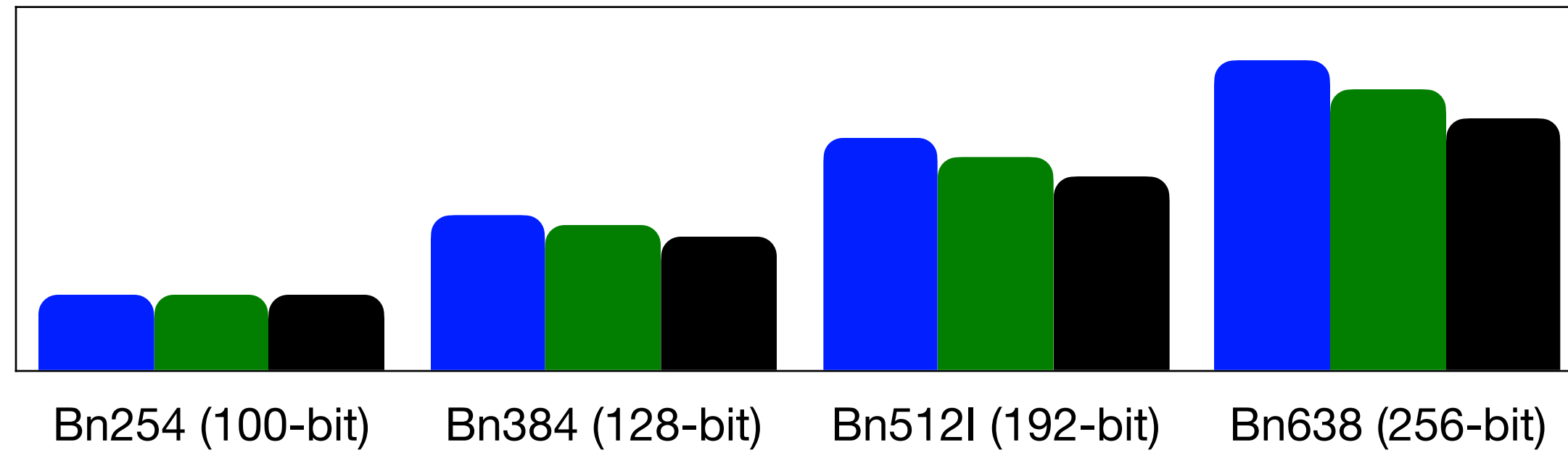
Benchmark results for a Hash-Chain circuit



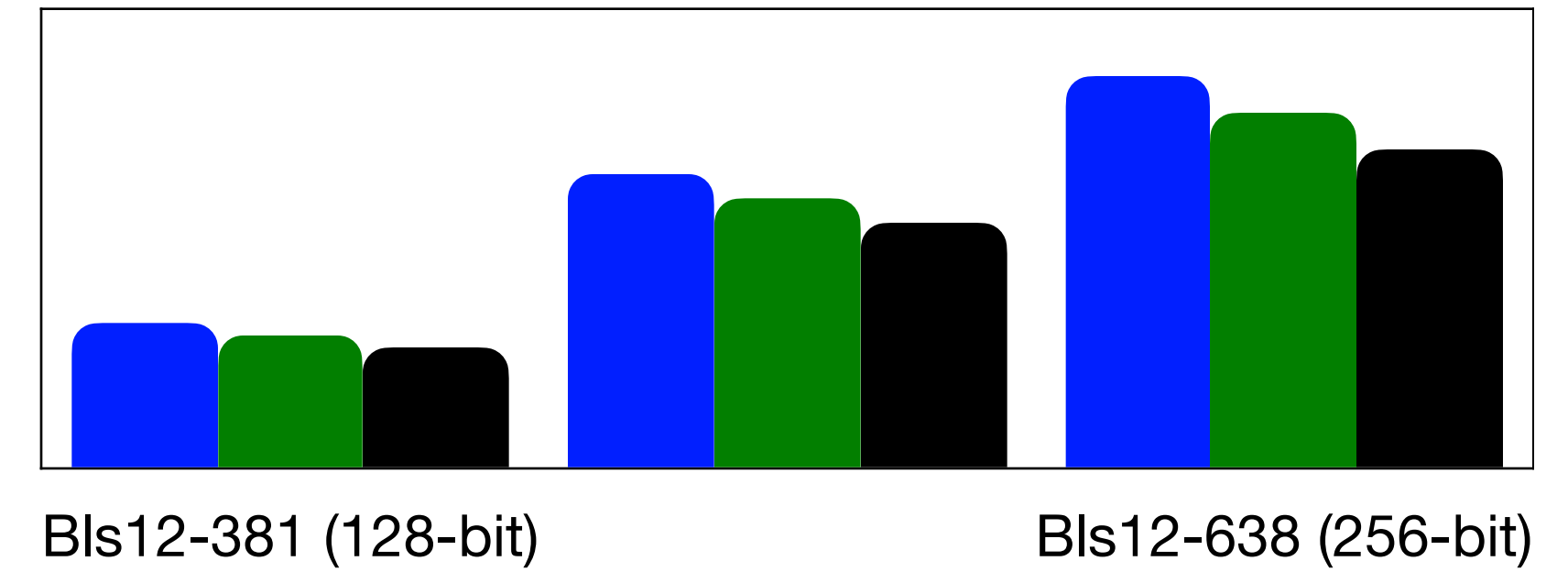
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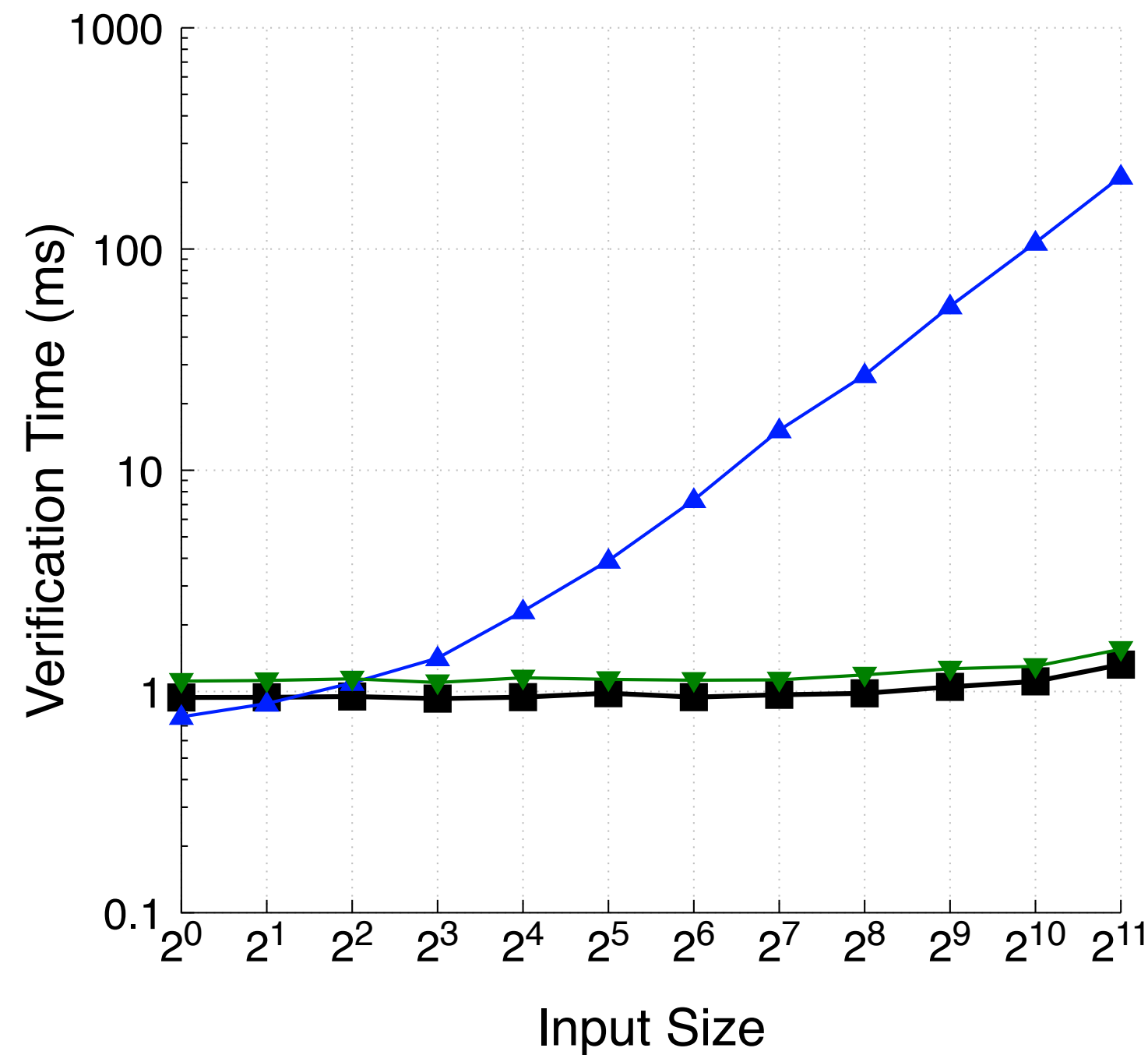
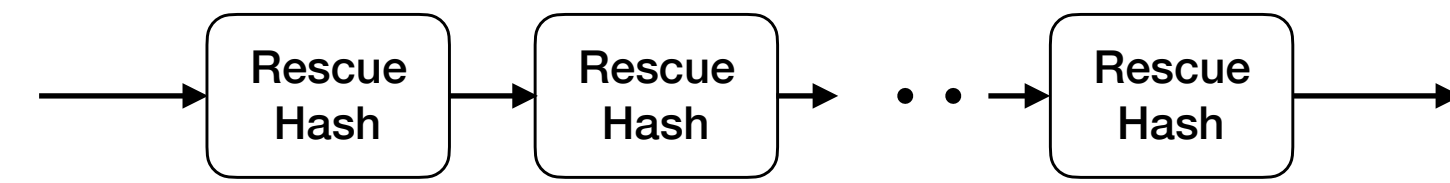
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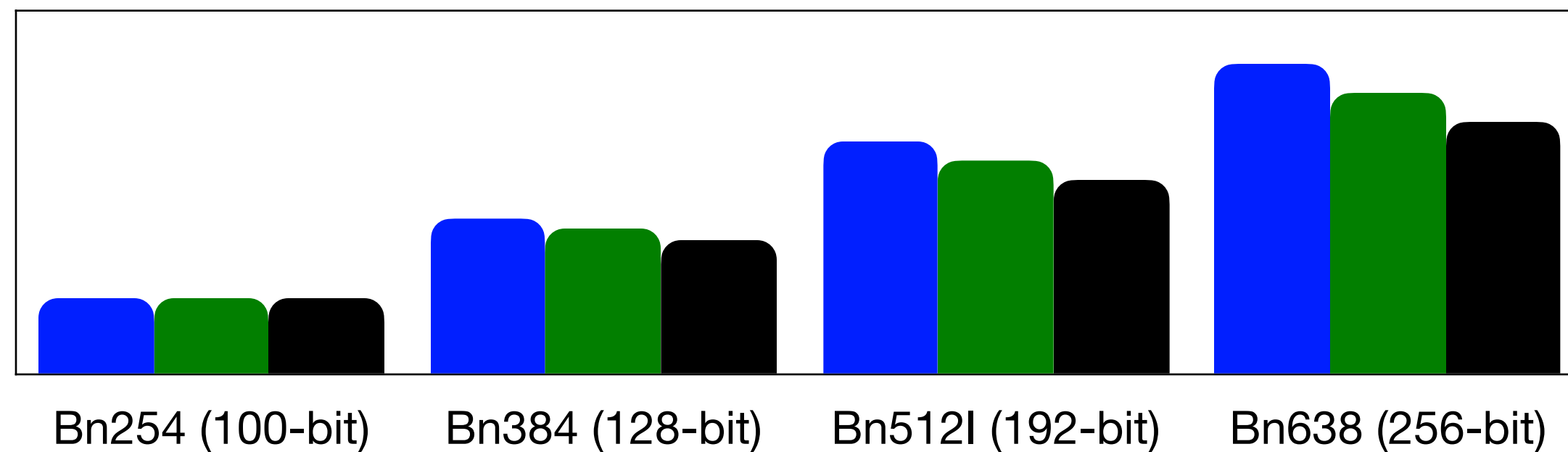
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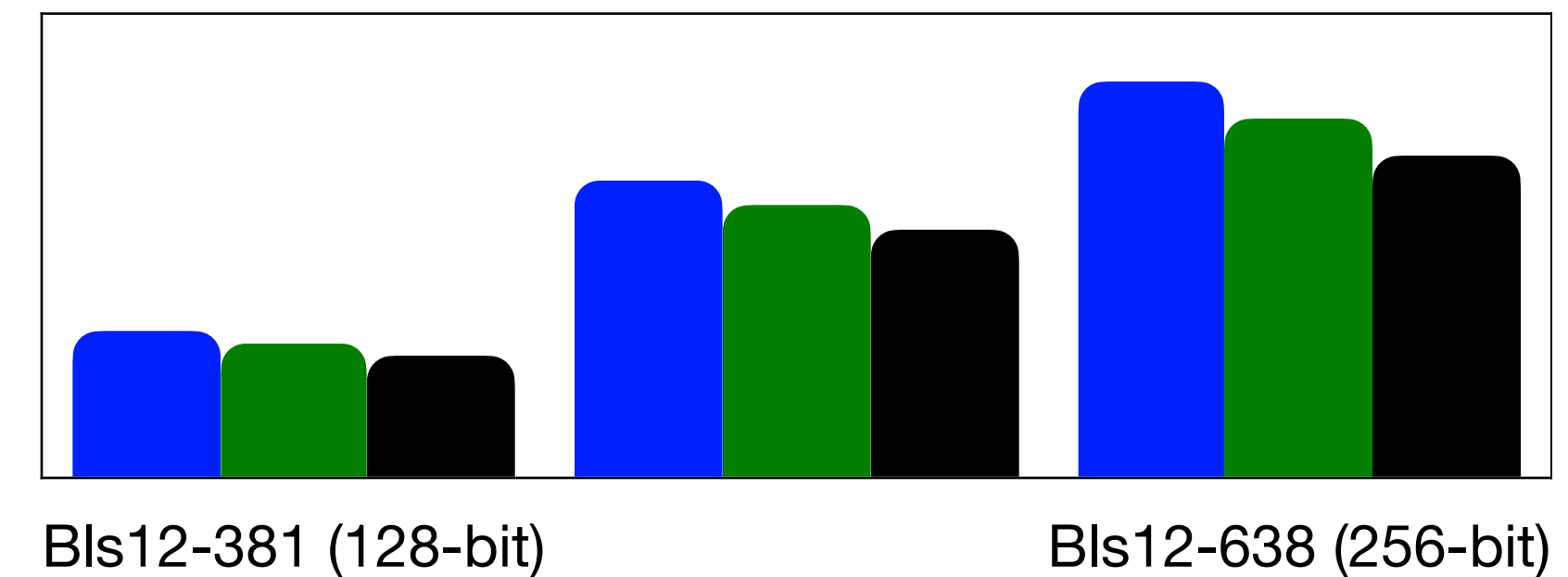
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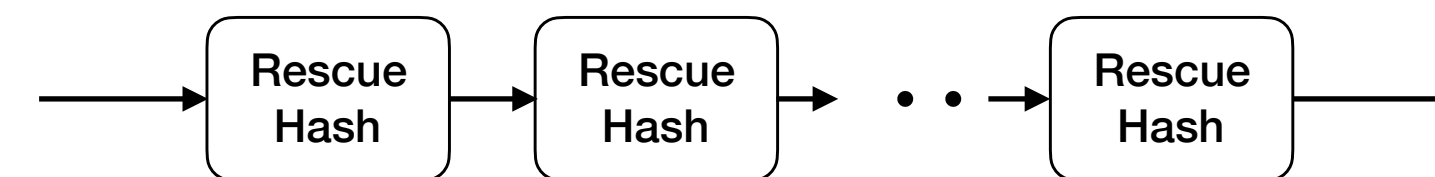
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Benchmark results for a Hash-Chain circuit



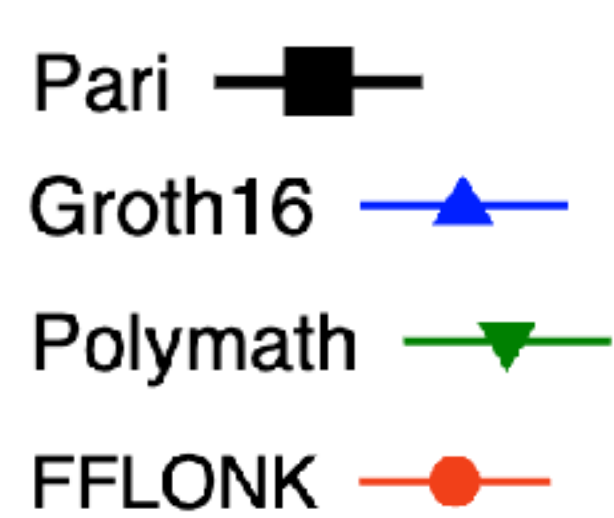
Comparison with Groth16:

No verifier MSM

0.2 ms worse for #io=1

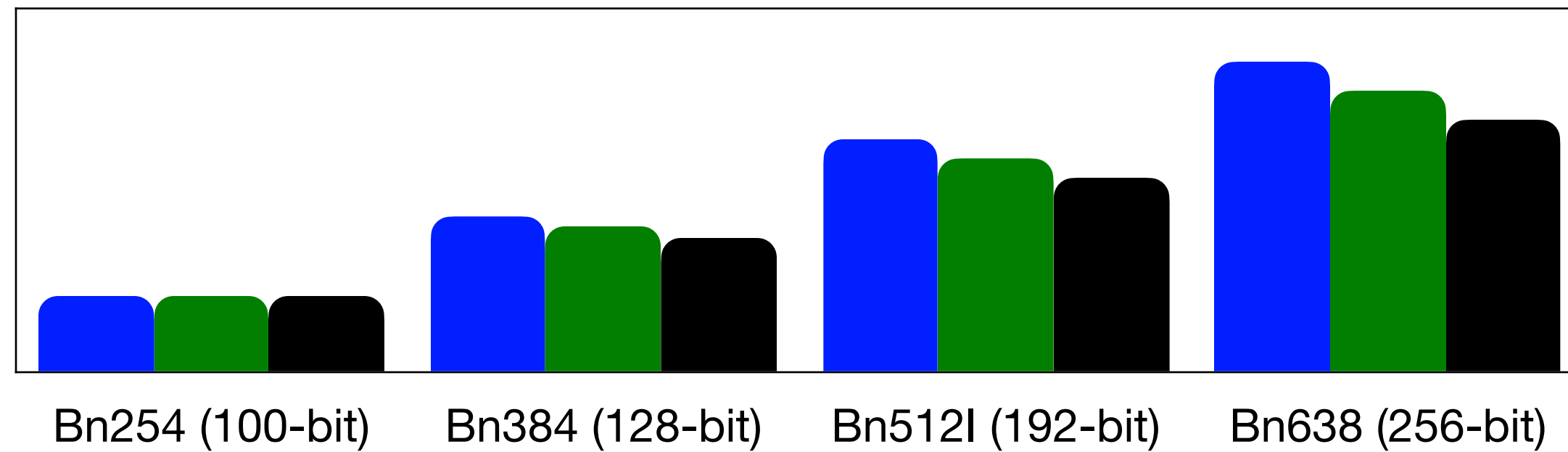
Comparison with Polymath:

~ 15% faster verifier

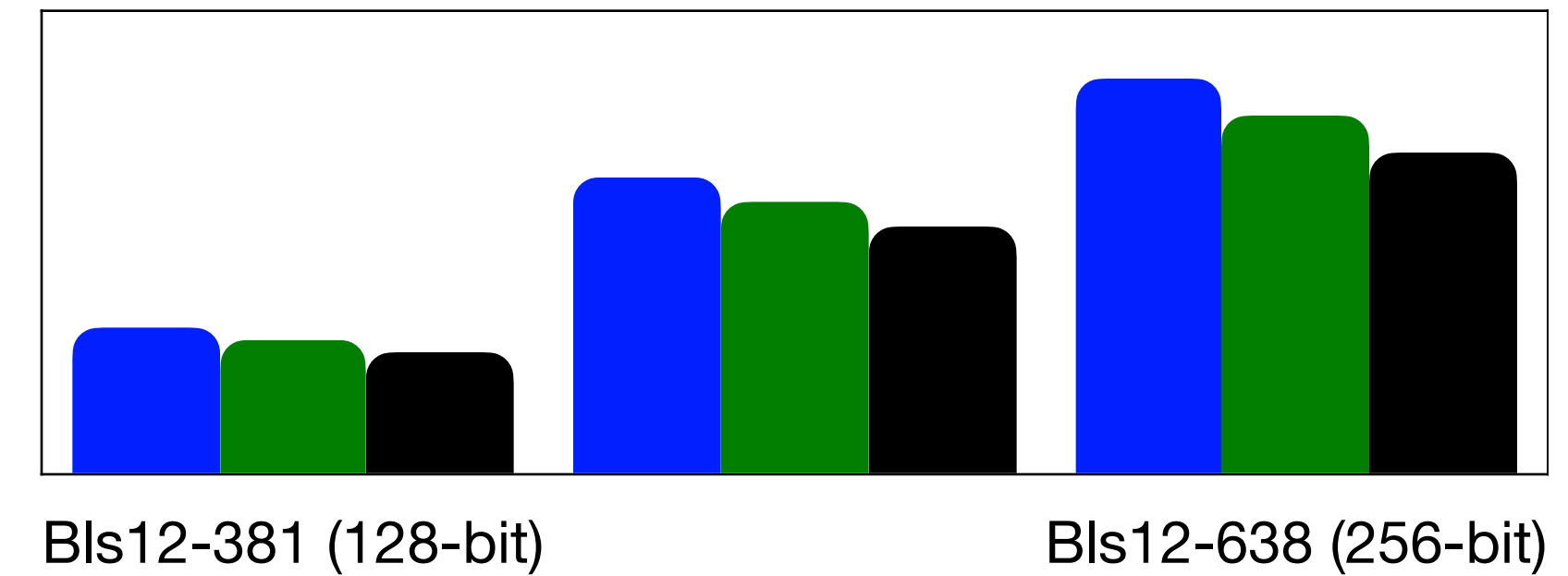


Evaluation for Pari

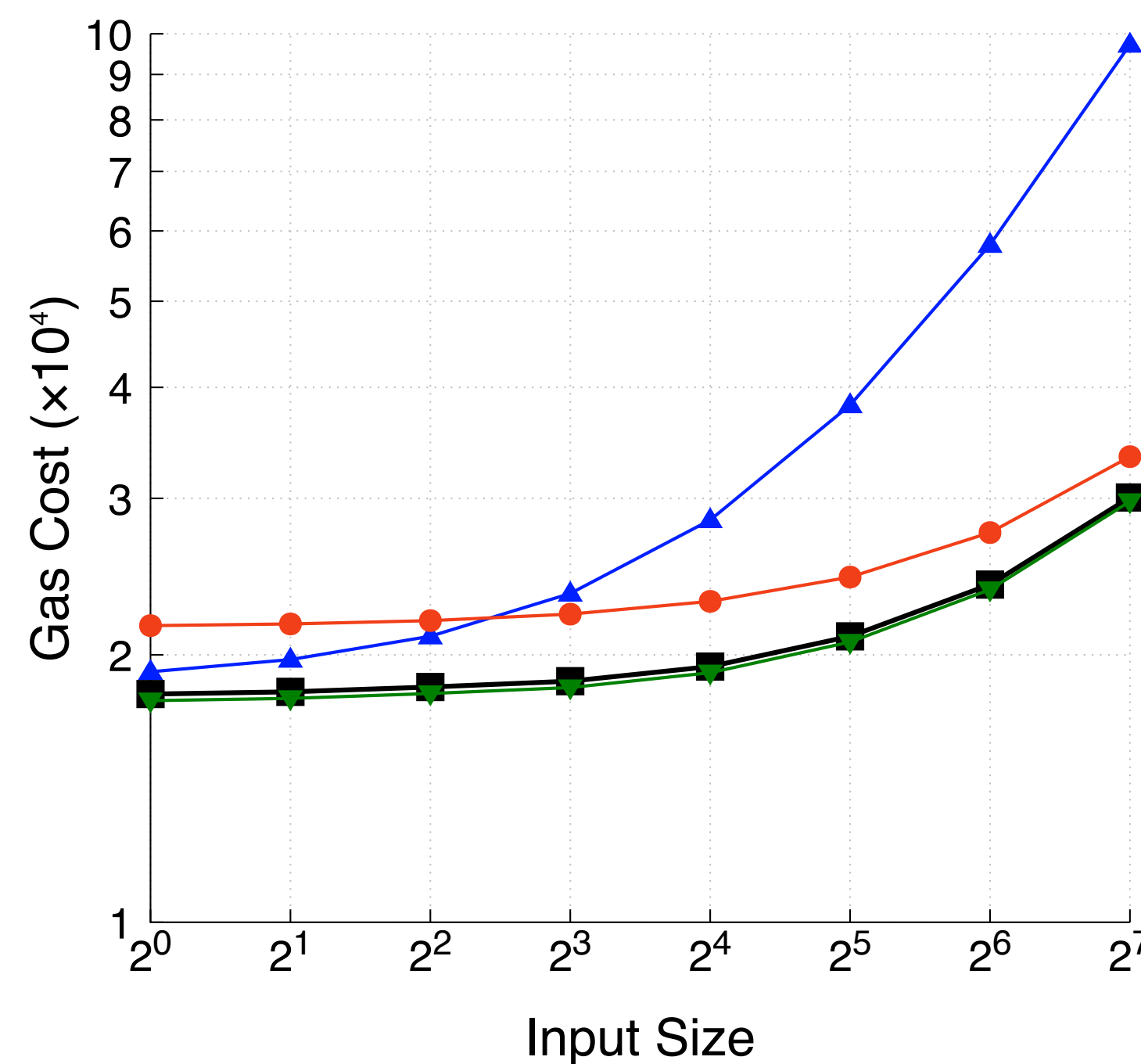
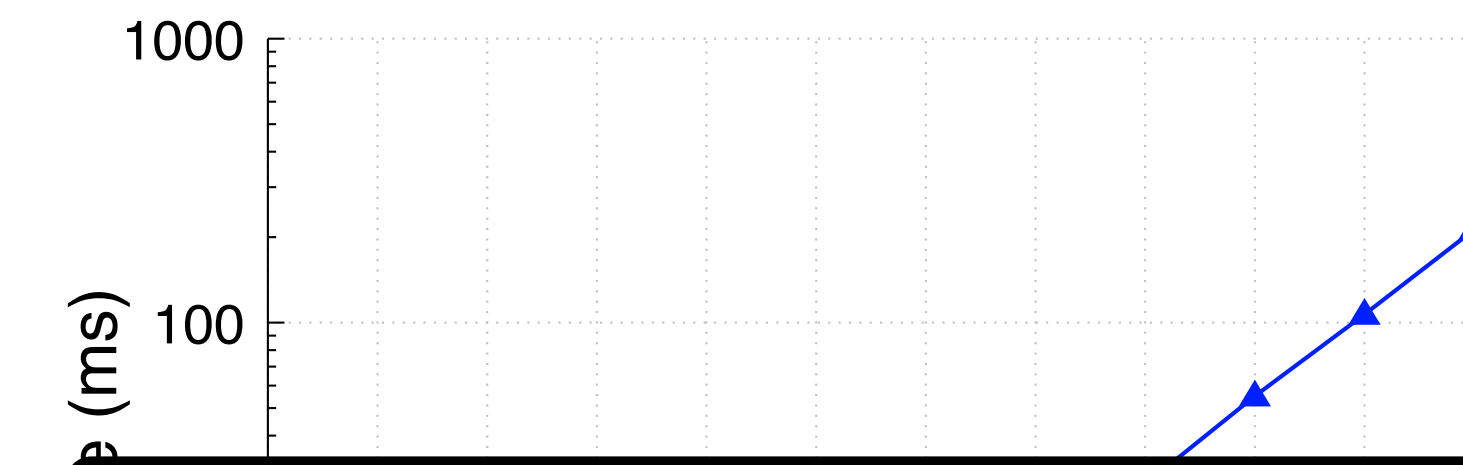
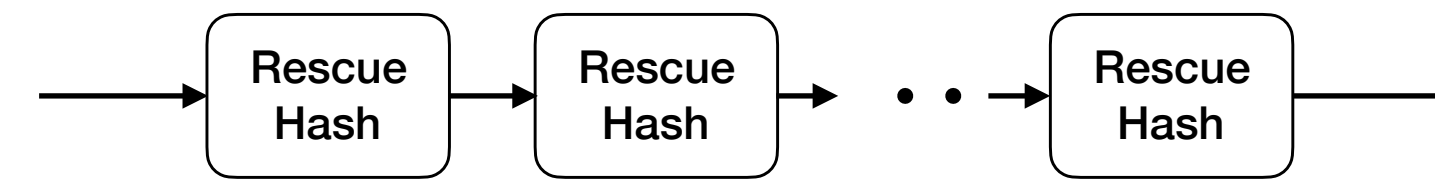
Proof size for BN curves



Proof size for BLS curves



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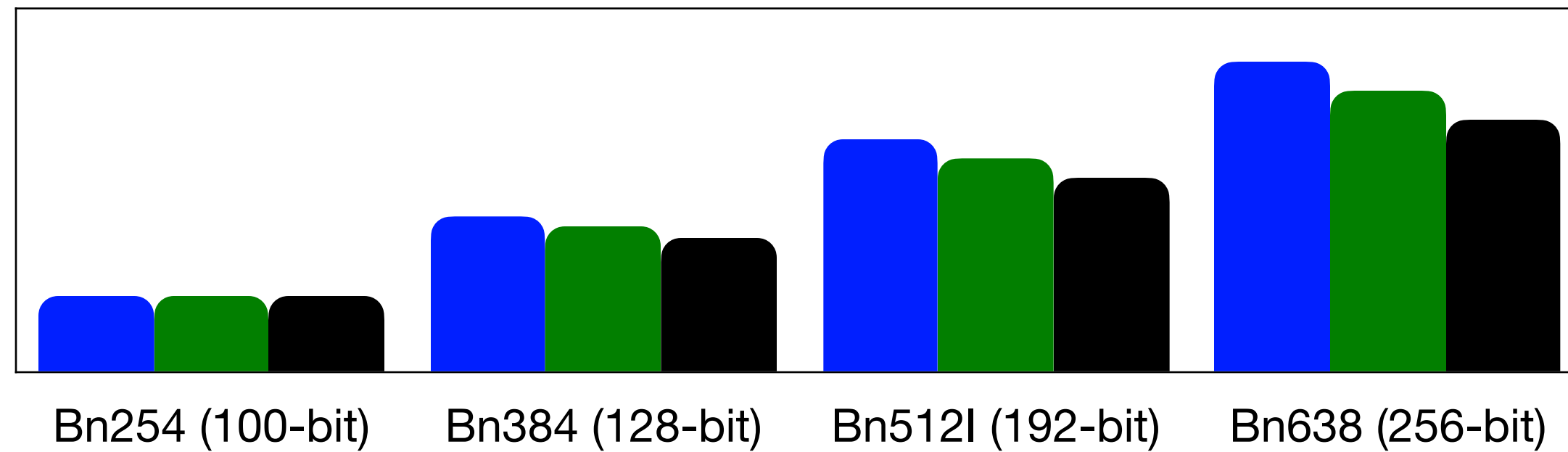
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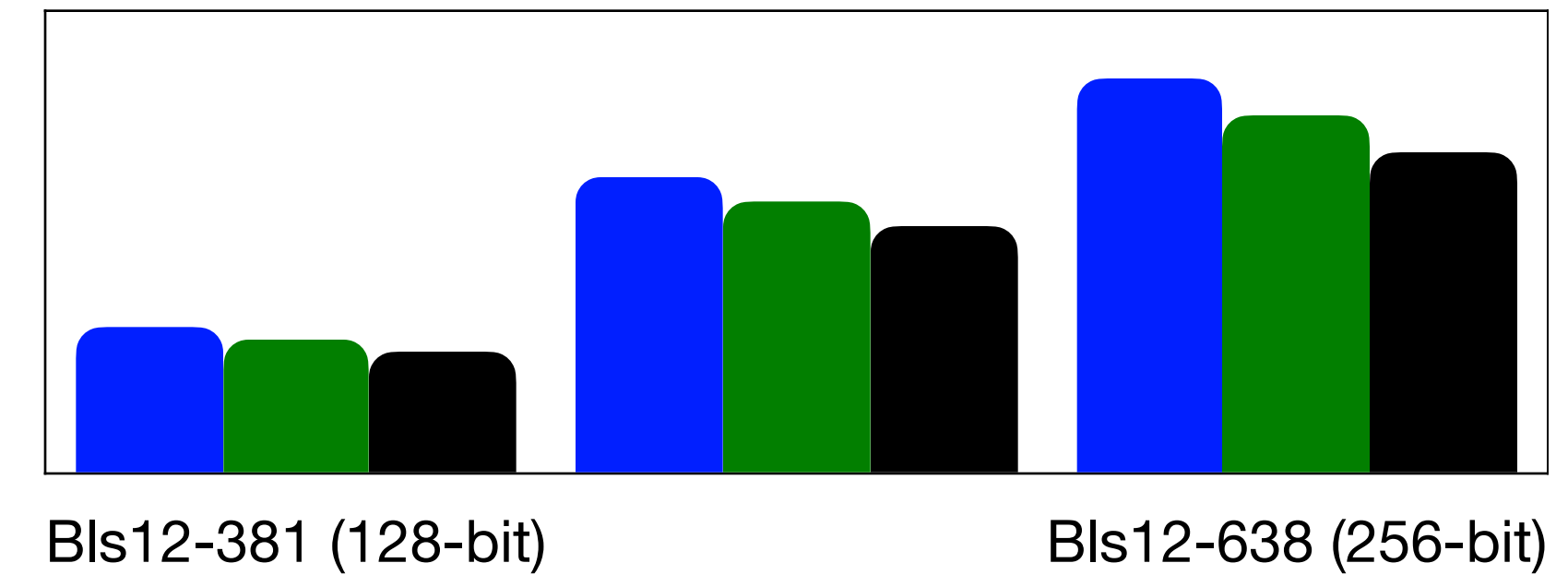
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 Groth16 
 Polymath 
 FFLONK 

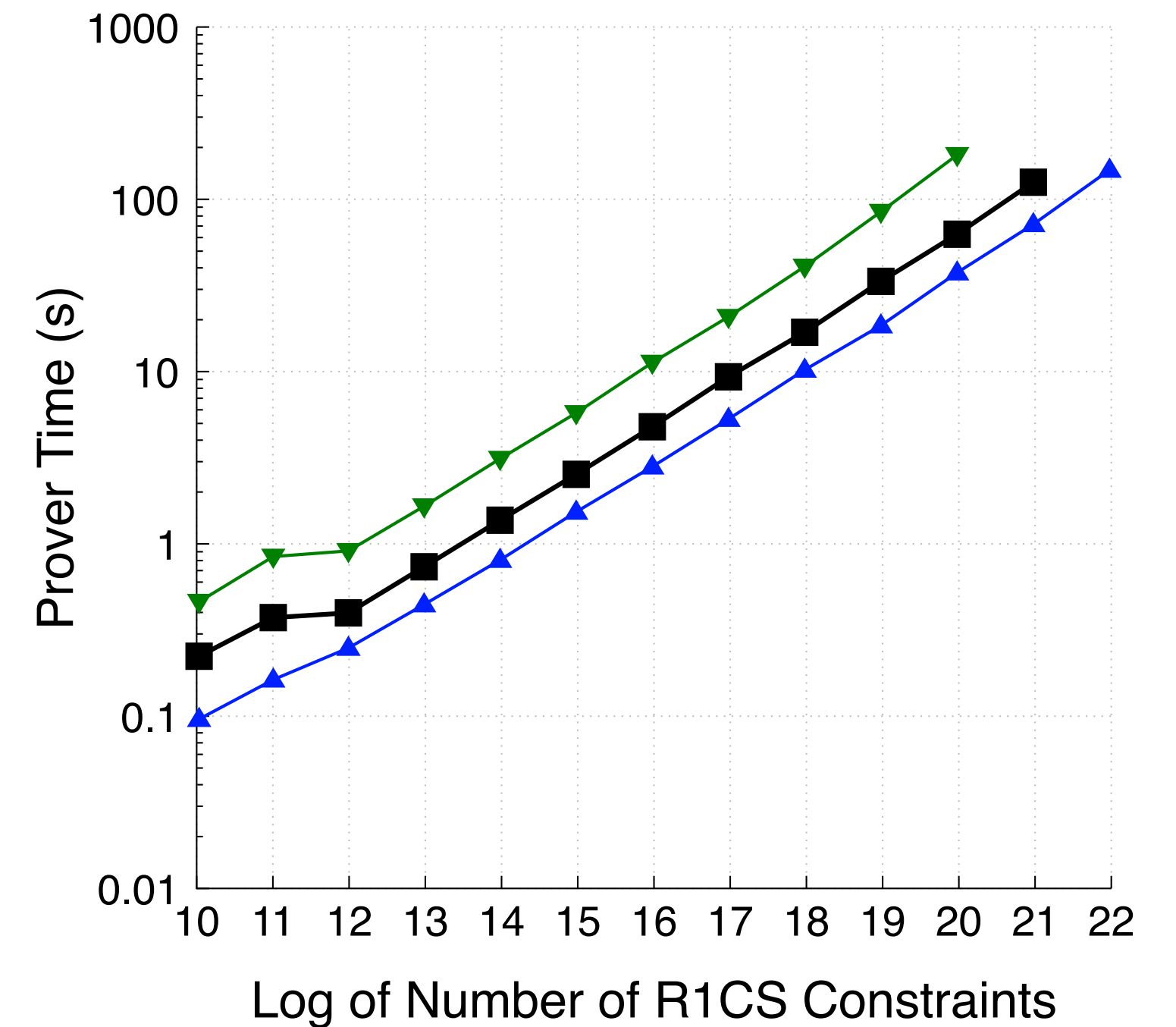
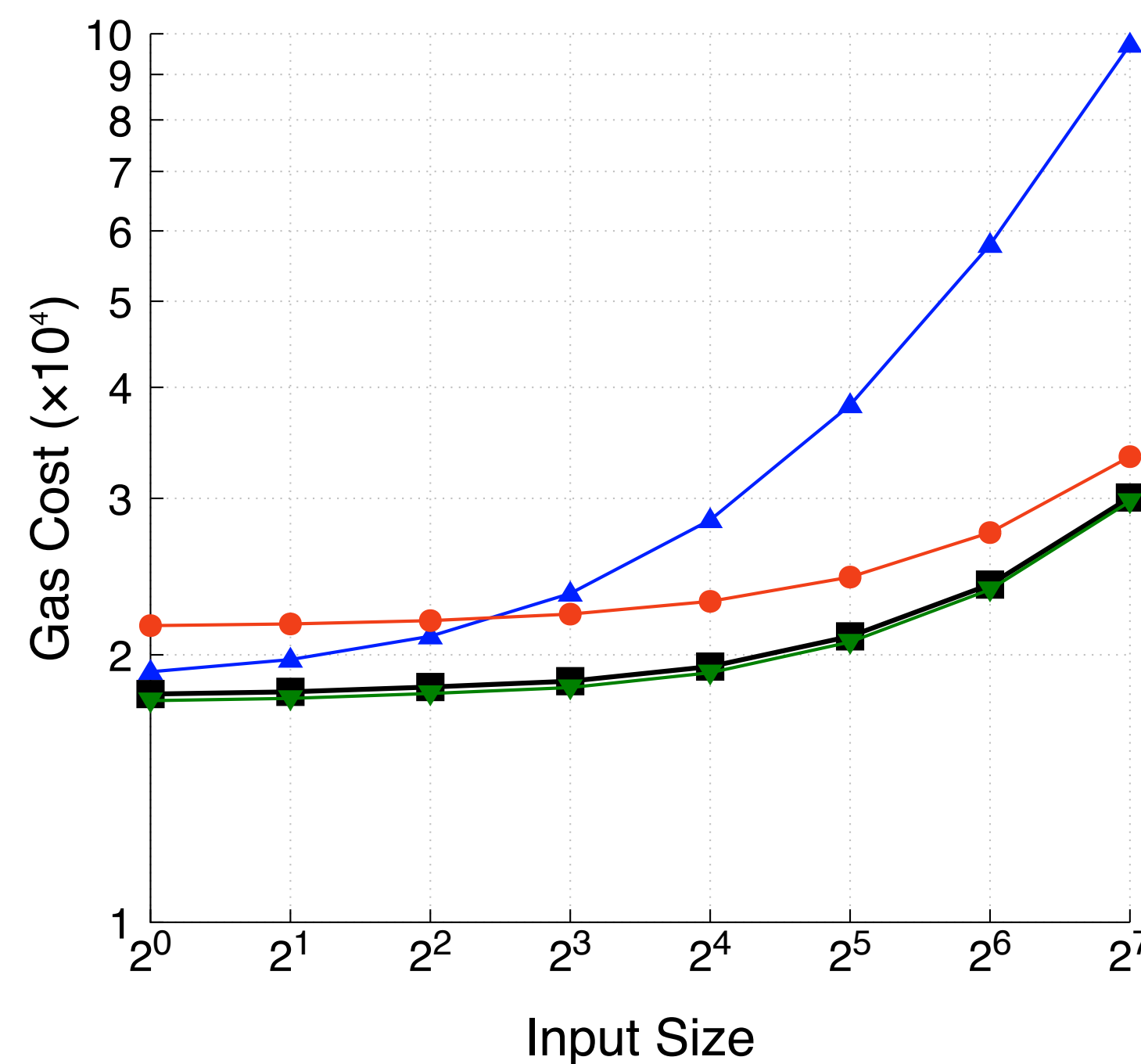
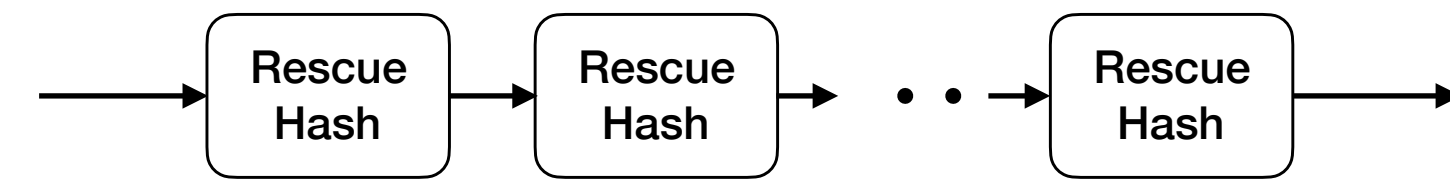
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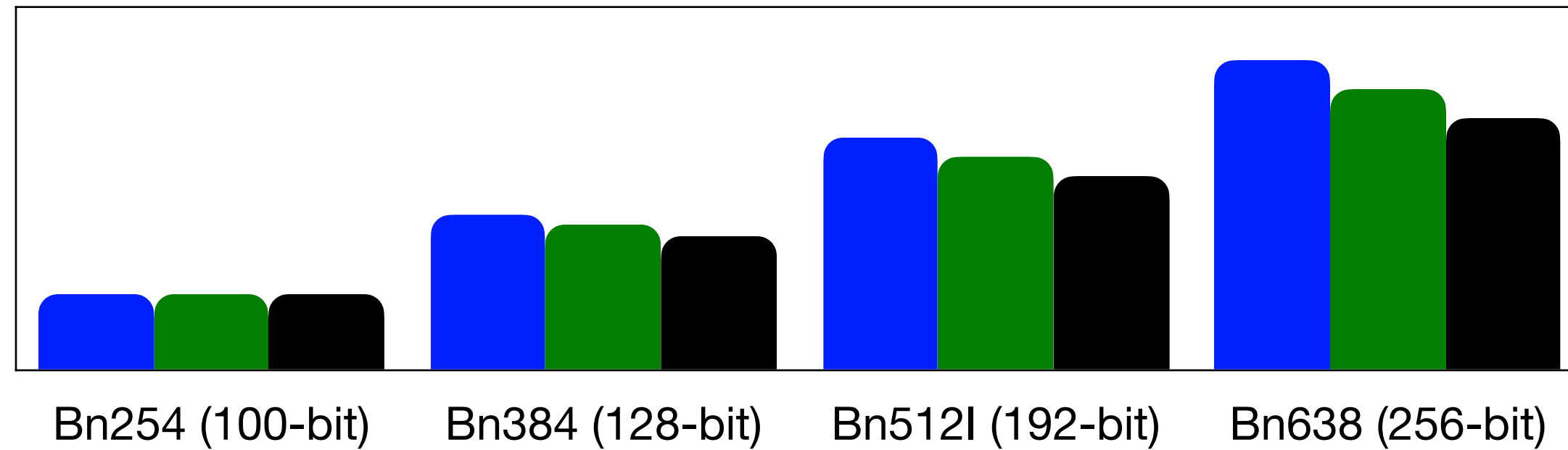
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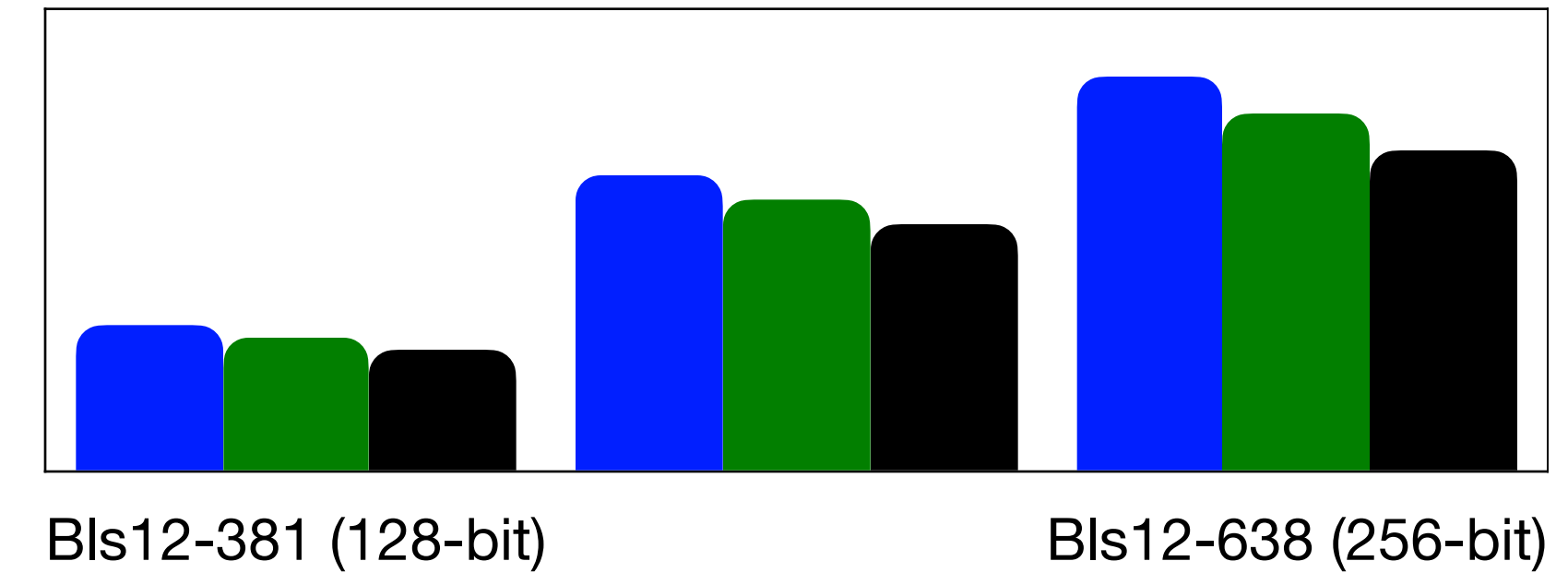
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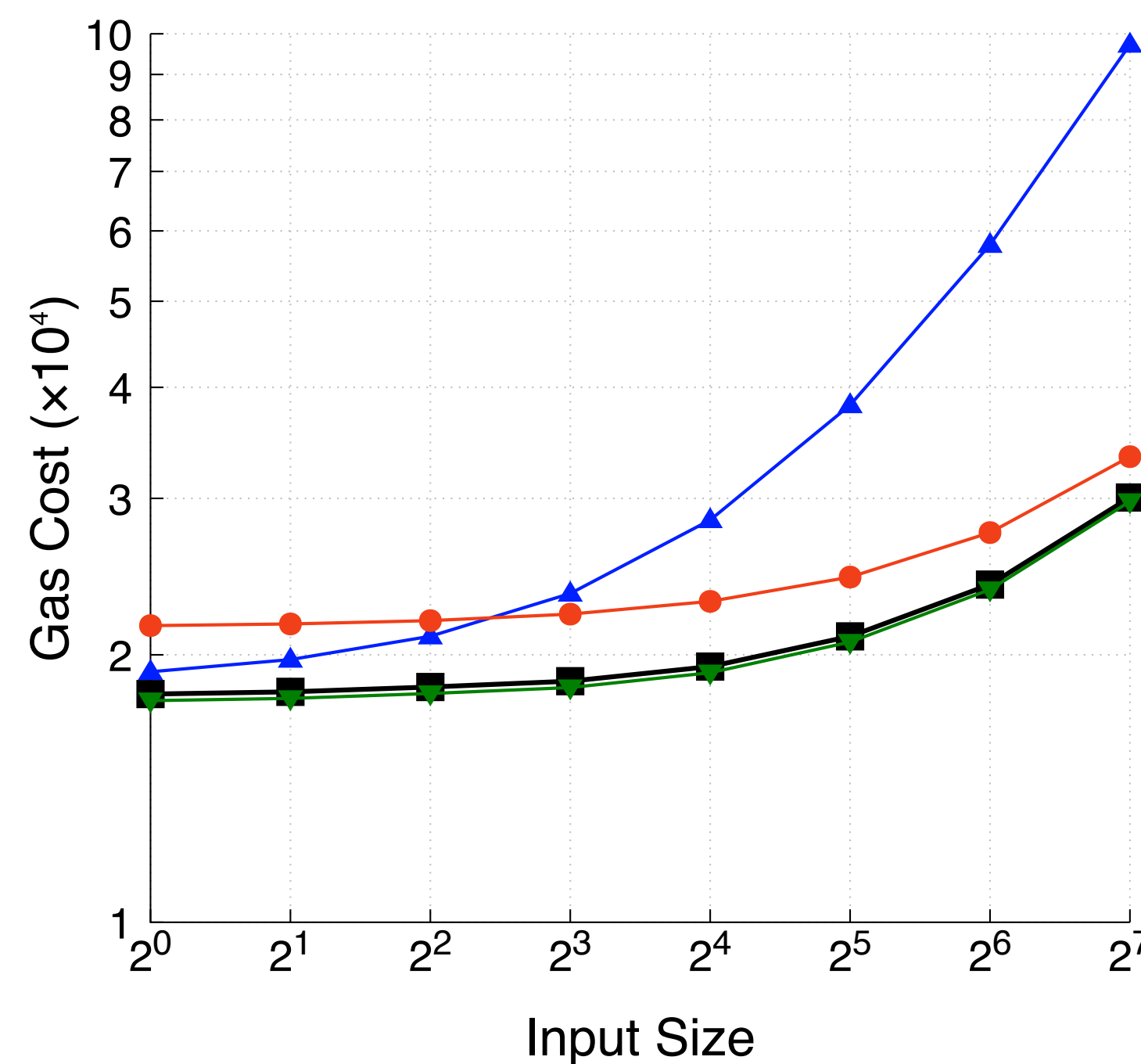
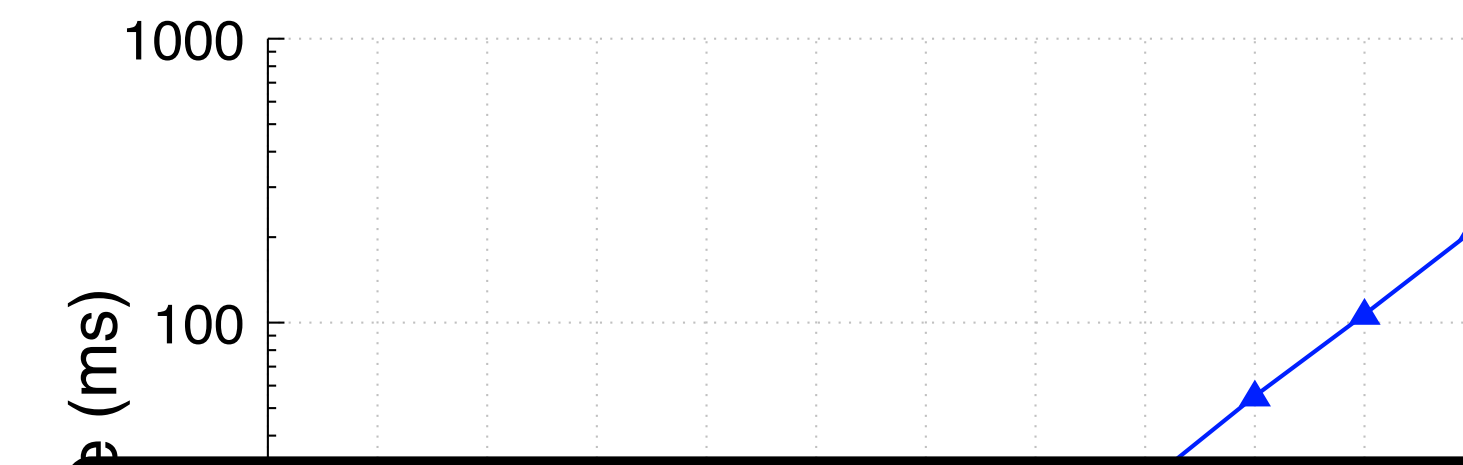
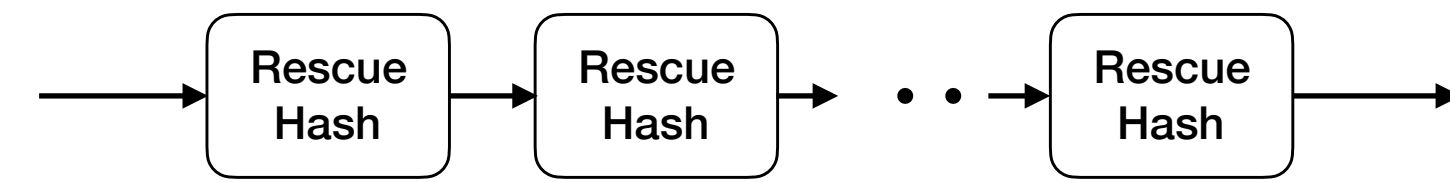
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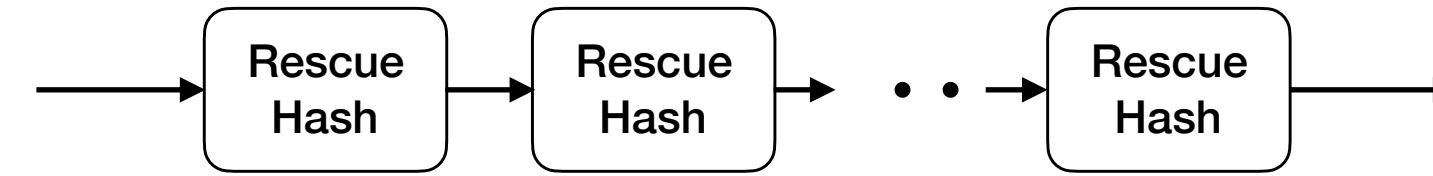
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Evaluation for Garuda



Evaluation for Garuda

Same Hash-Chain circuit

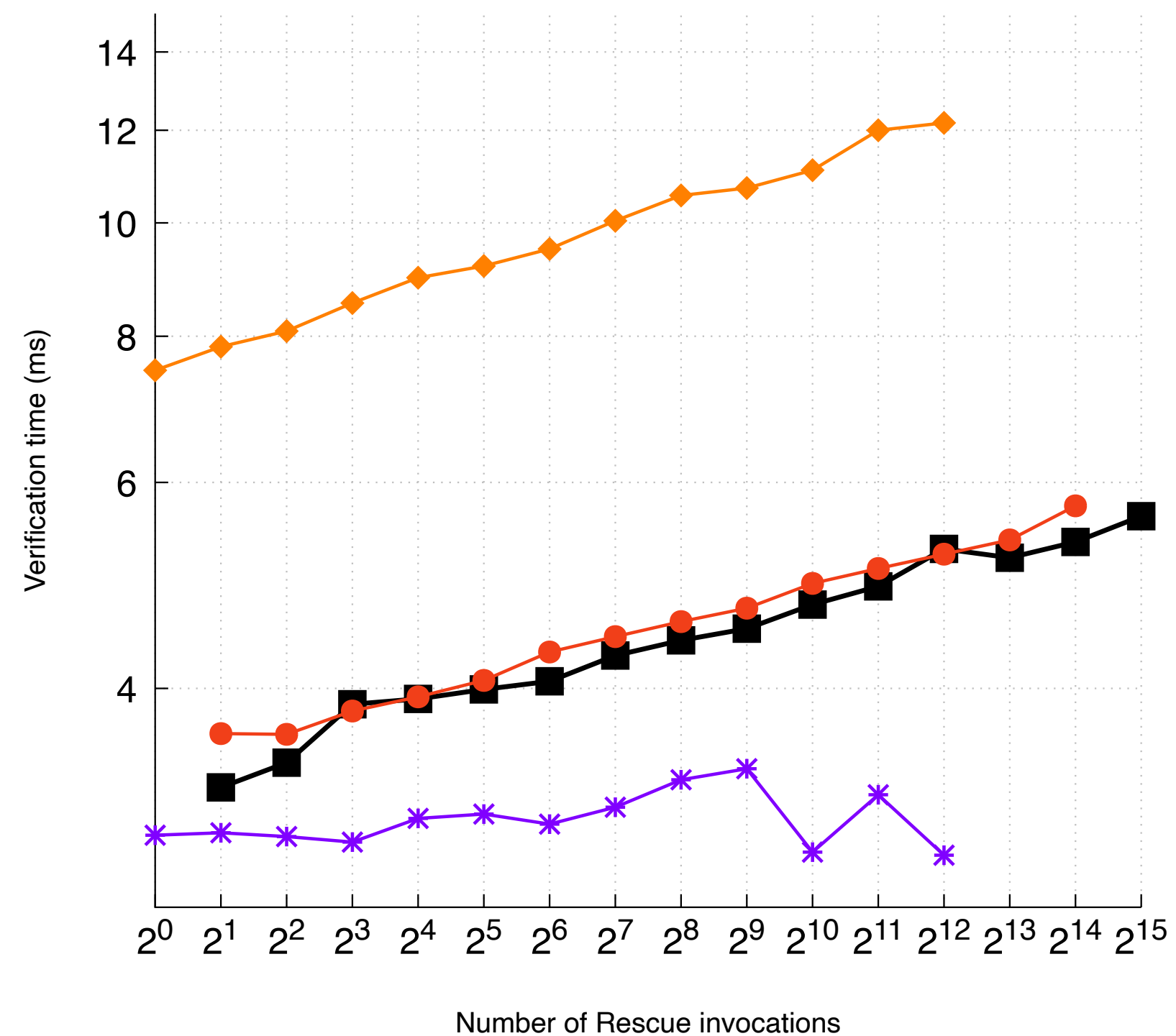
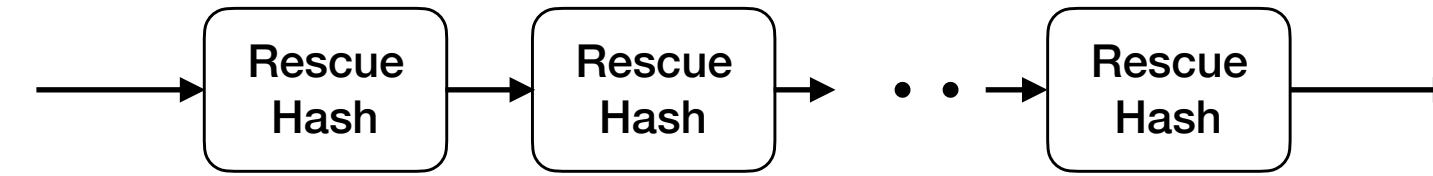




Garuda (R1CS) 
Garuda (GR1CS) 

Evaluation for Garuda



Groth16 (R1CS) 
Hyperplonk (Plonkish) 

Same Hash-Chain circuit

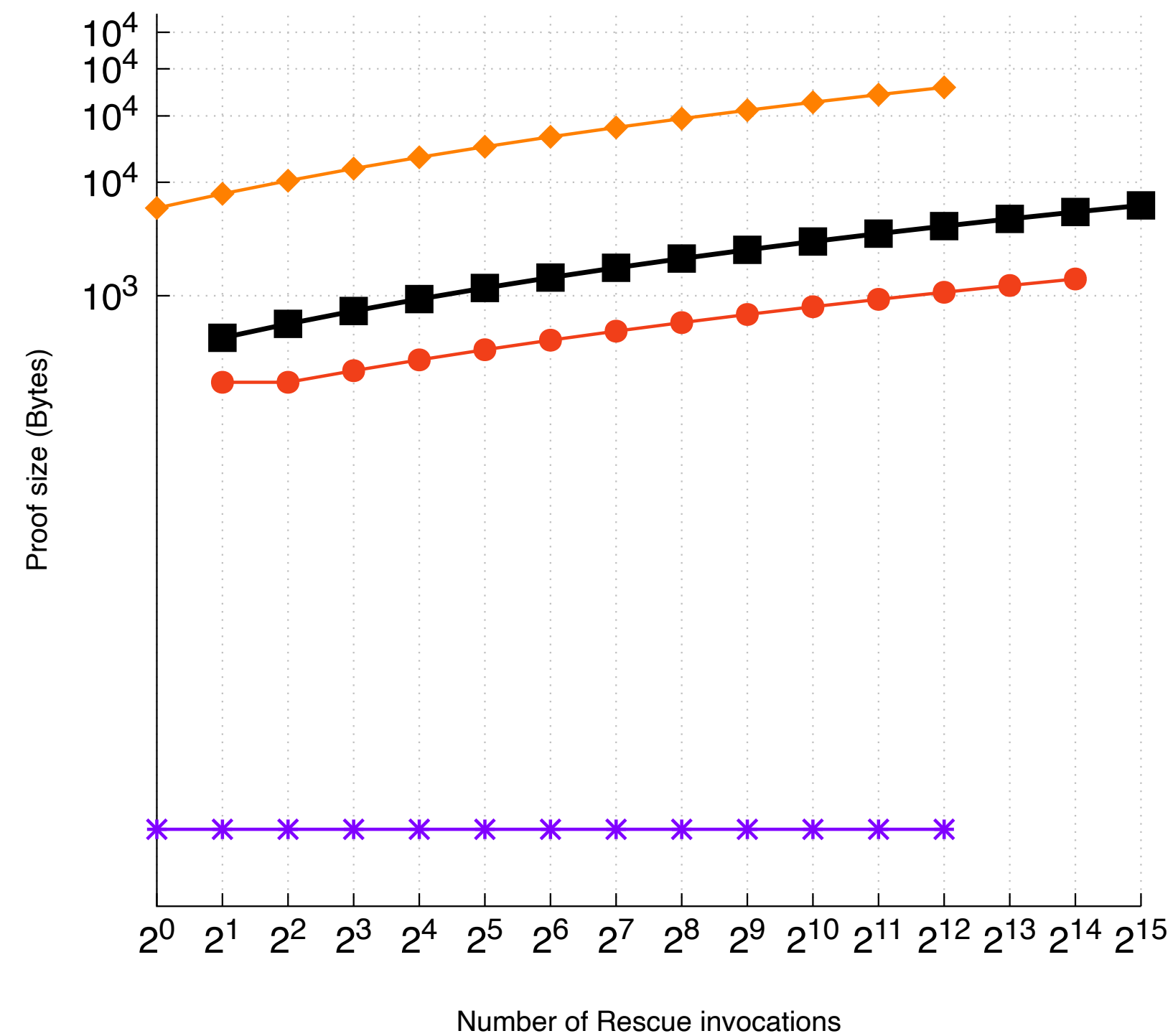
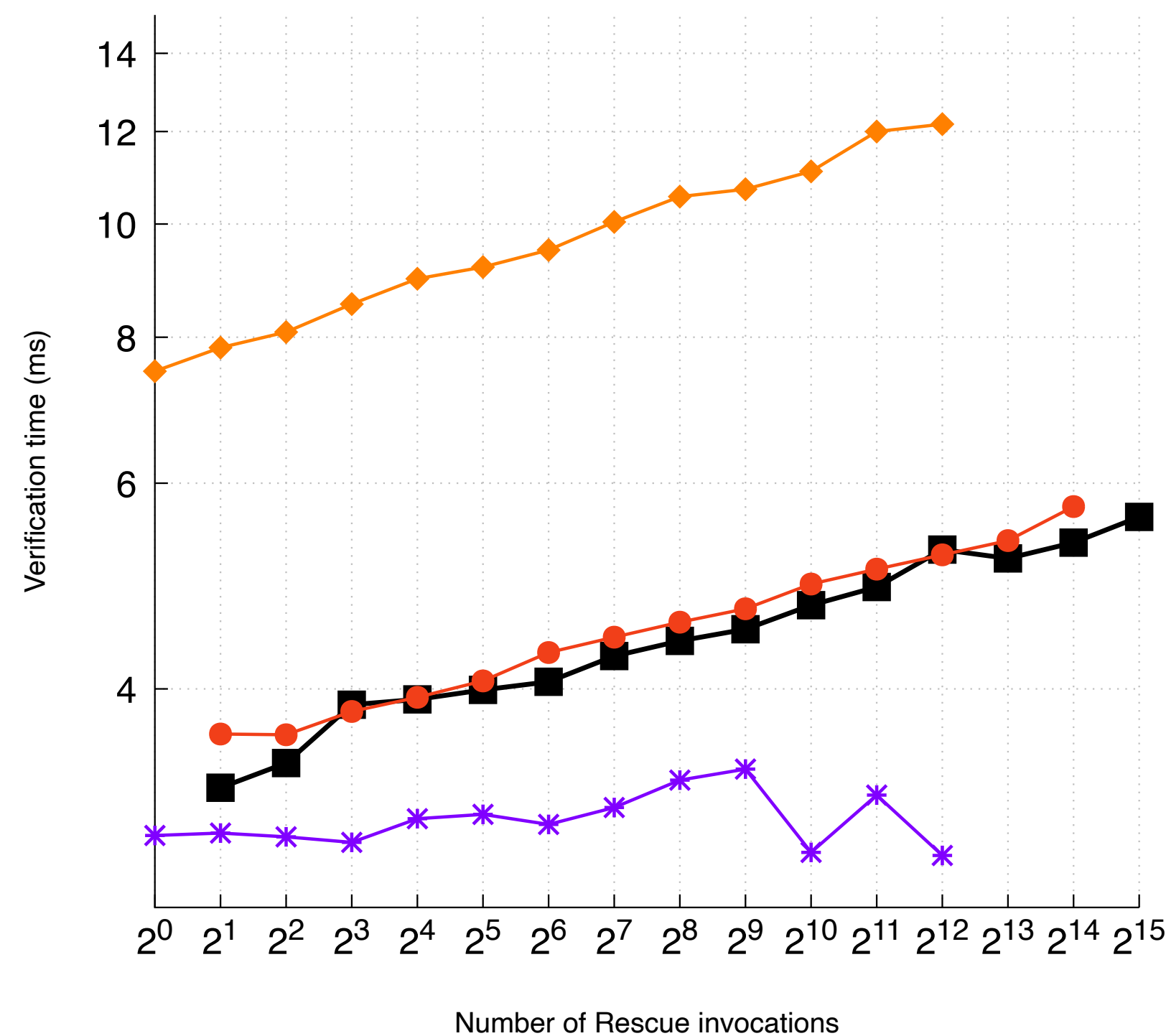
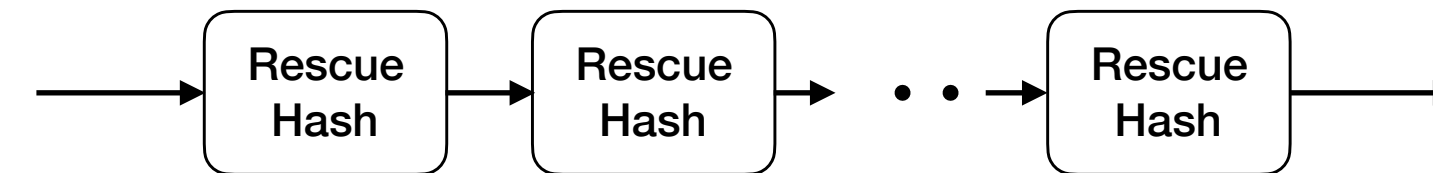


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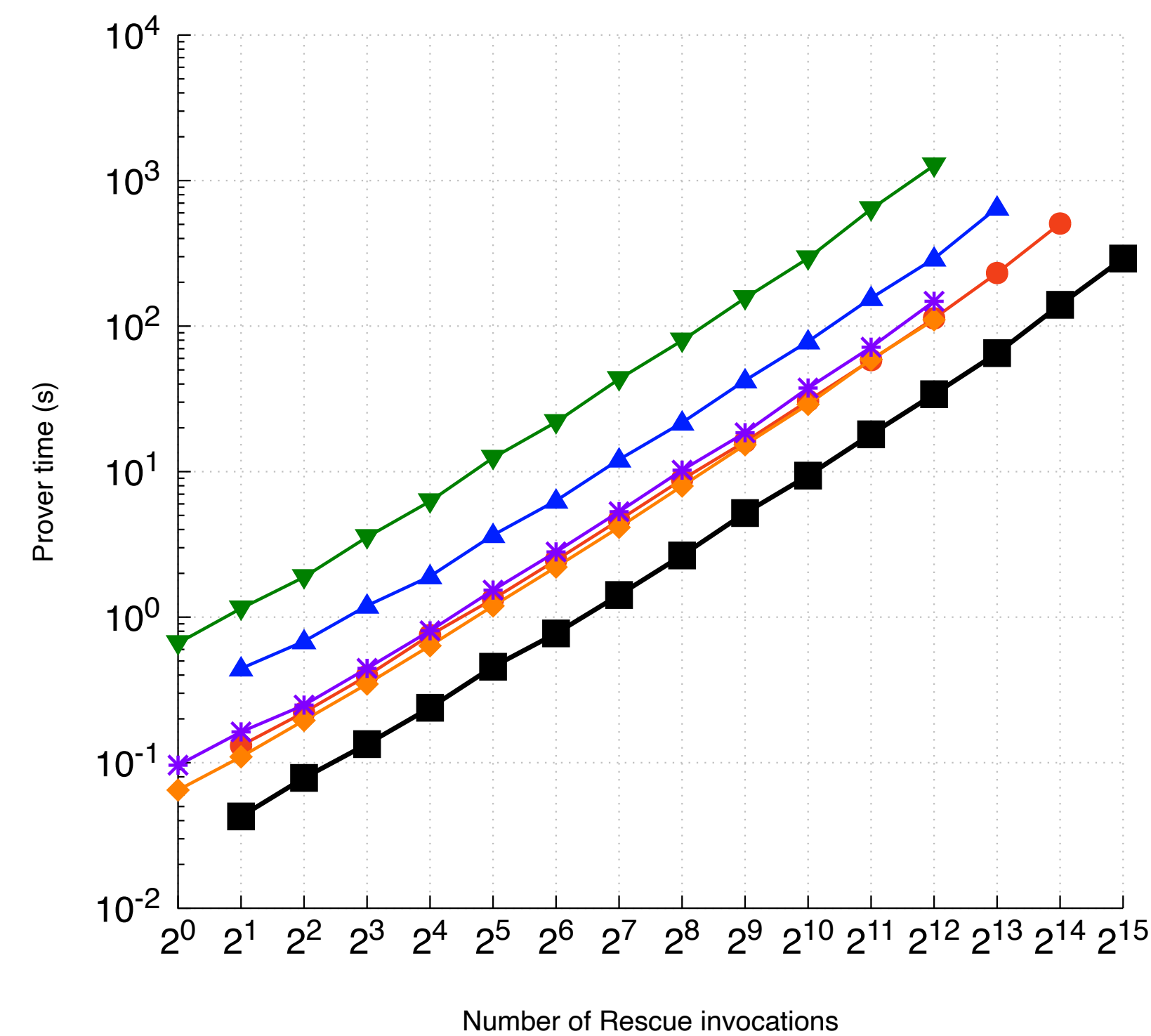
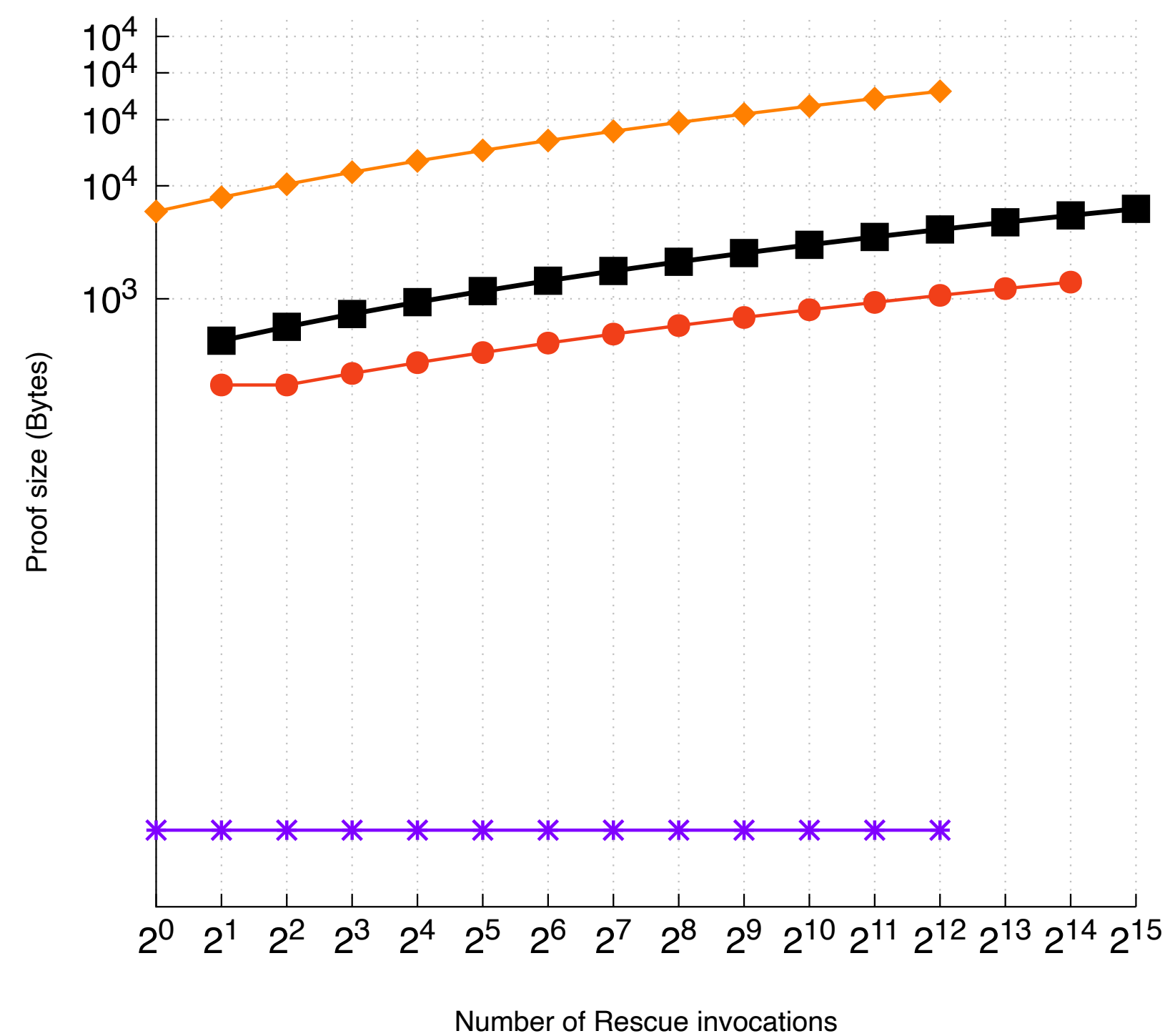
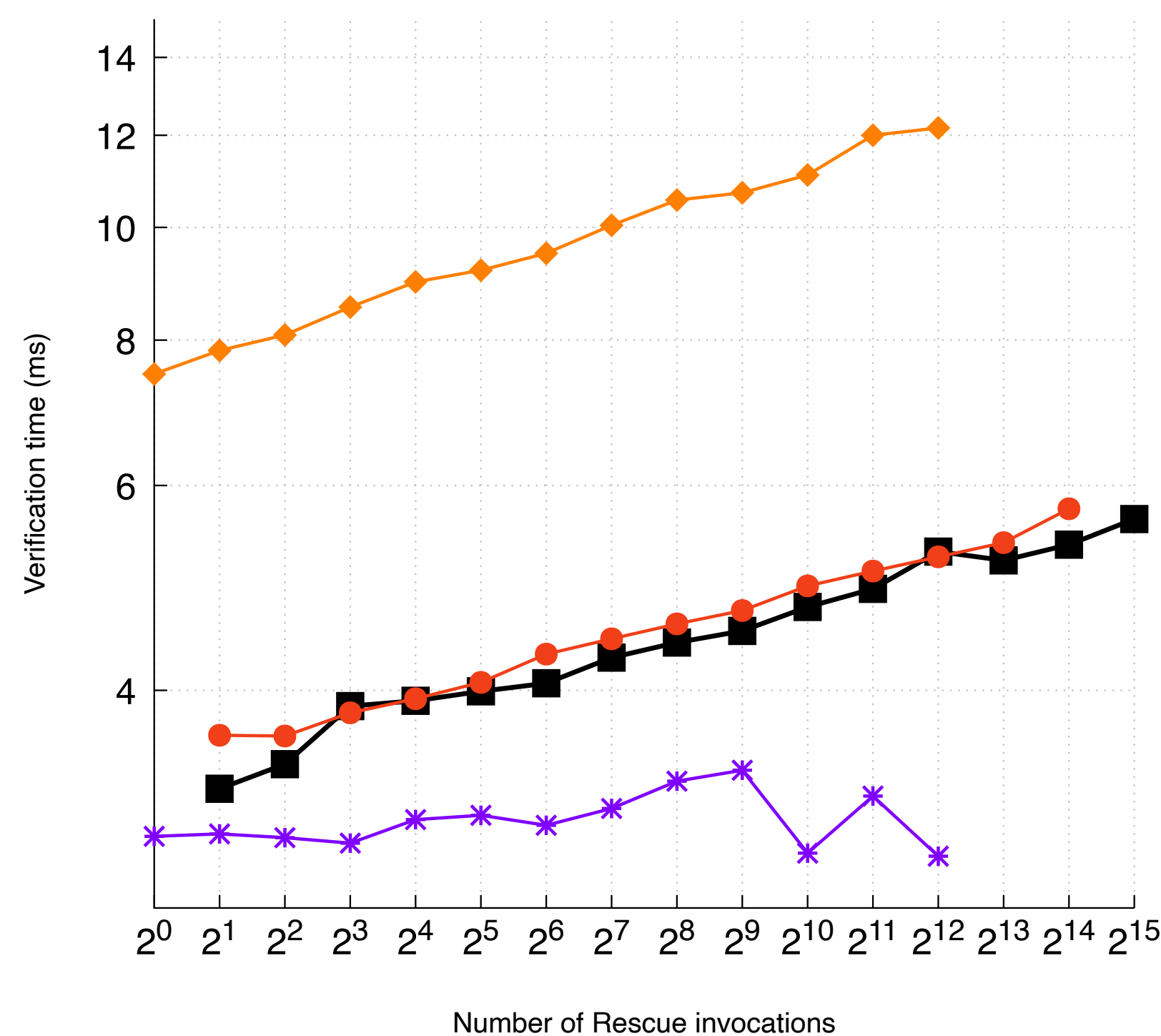
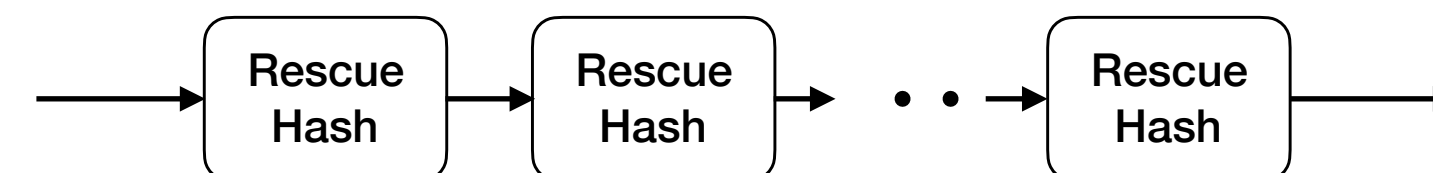


Garuda (R1CS) ●
Garuda (GR1CS) ■
Spartan (R1CS) ▼

Evaluation for Garuda

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Hyperplonk (Plonkish) ◆
SuperSpartan (CCS) ▲

Same Hash-Chain circuit

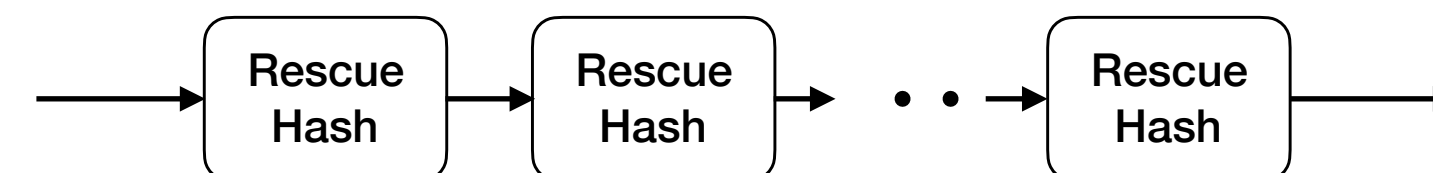


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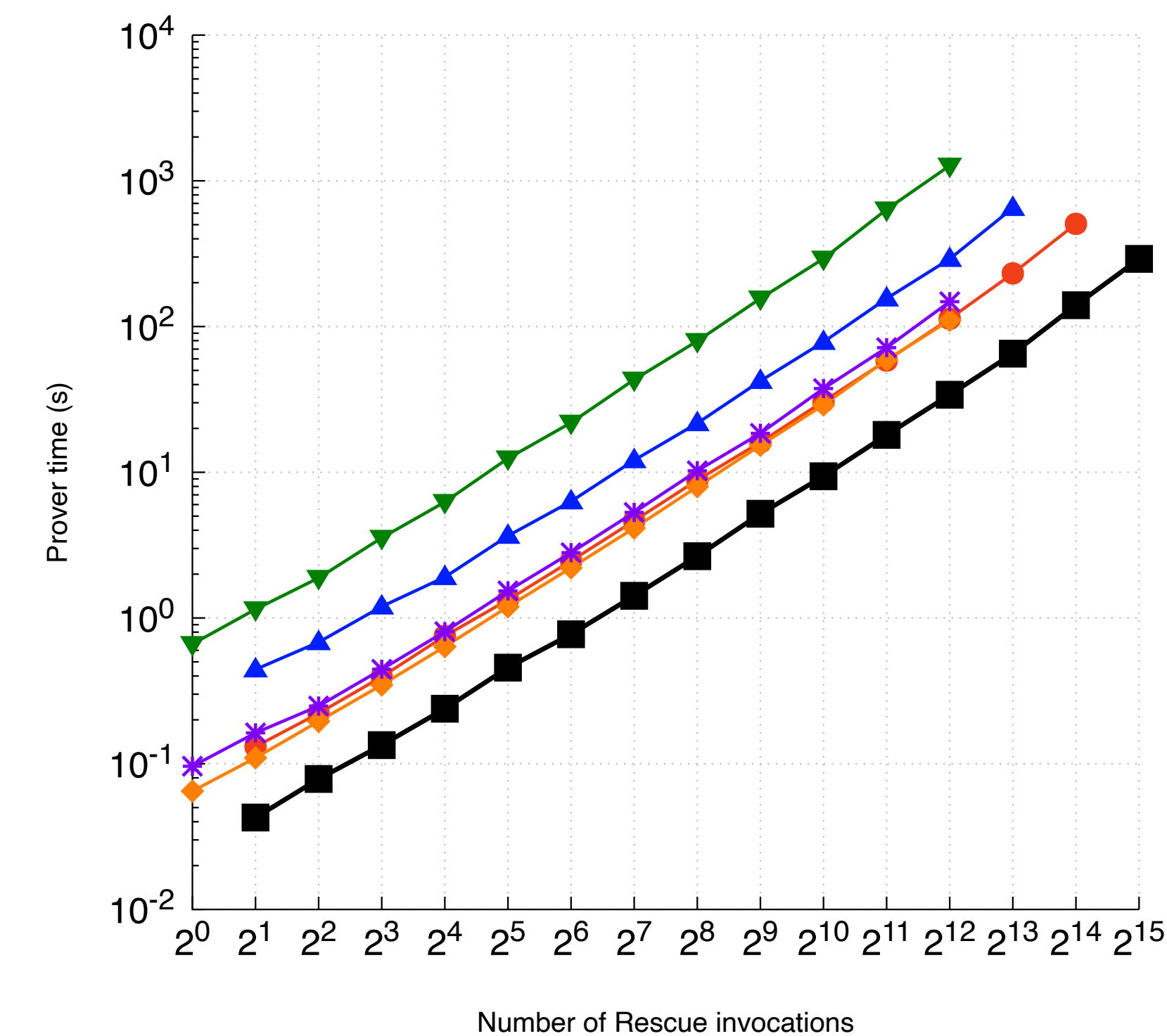
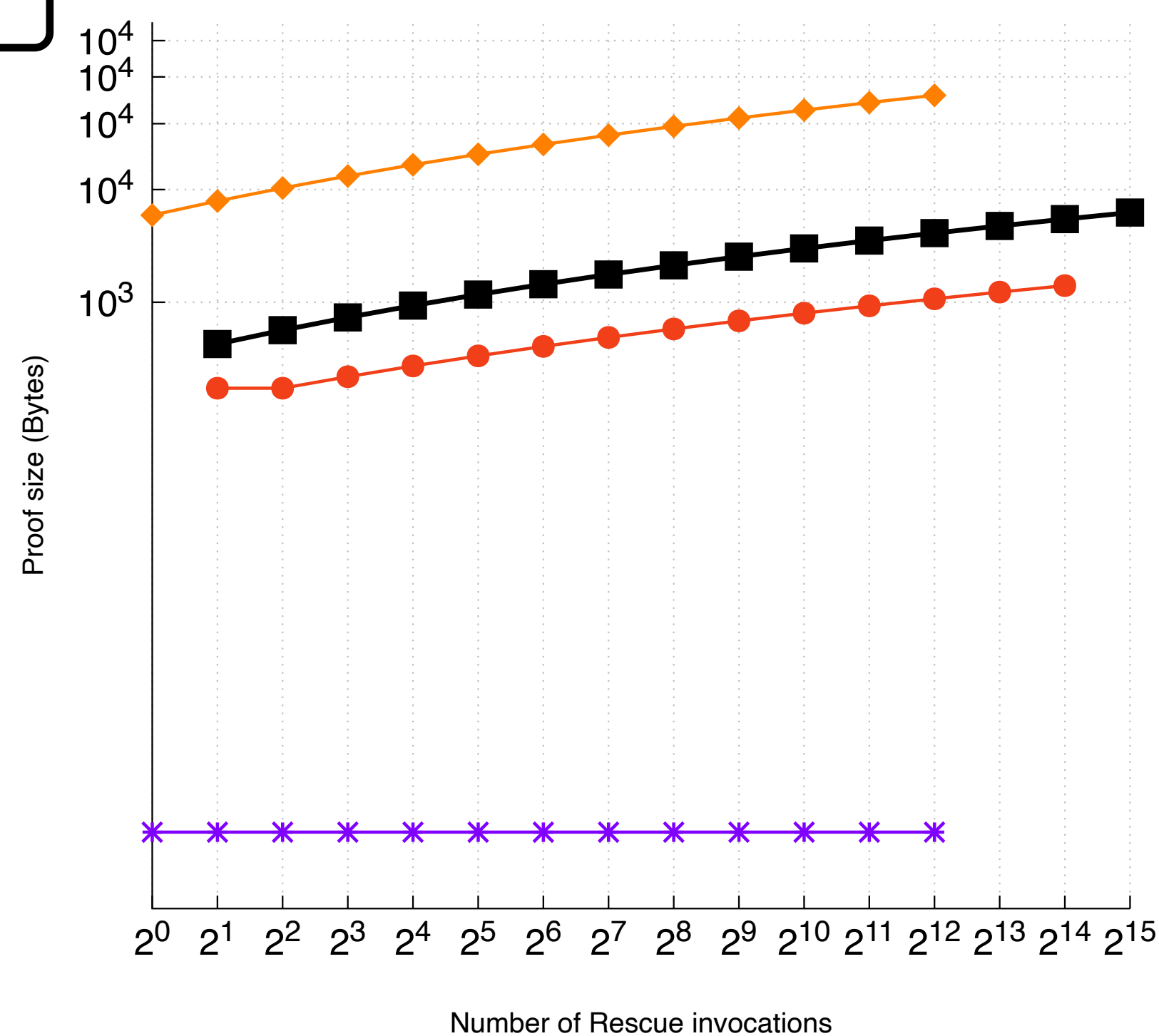
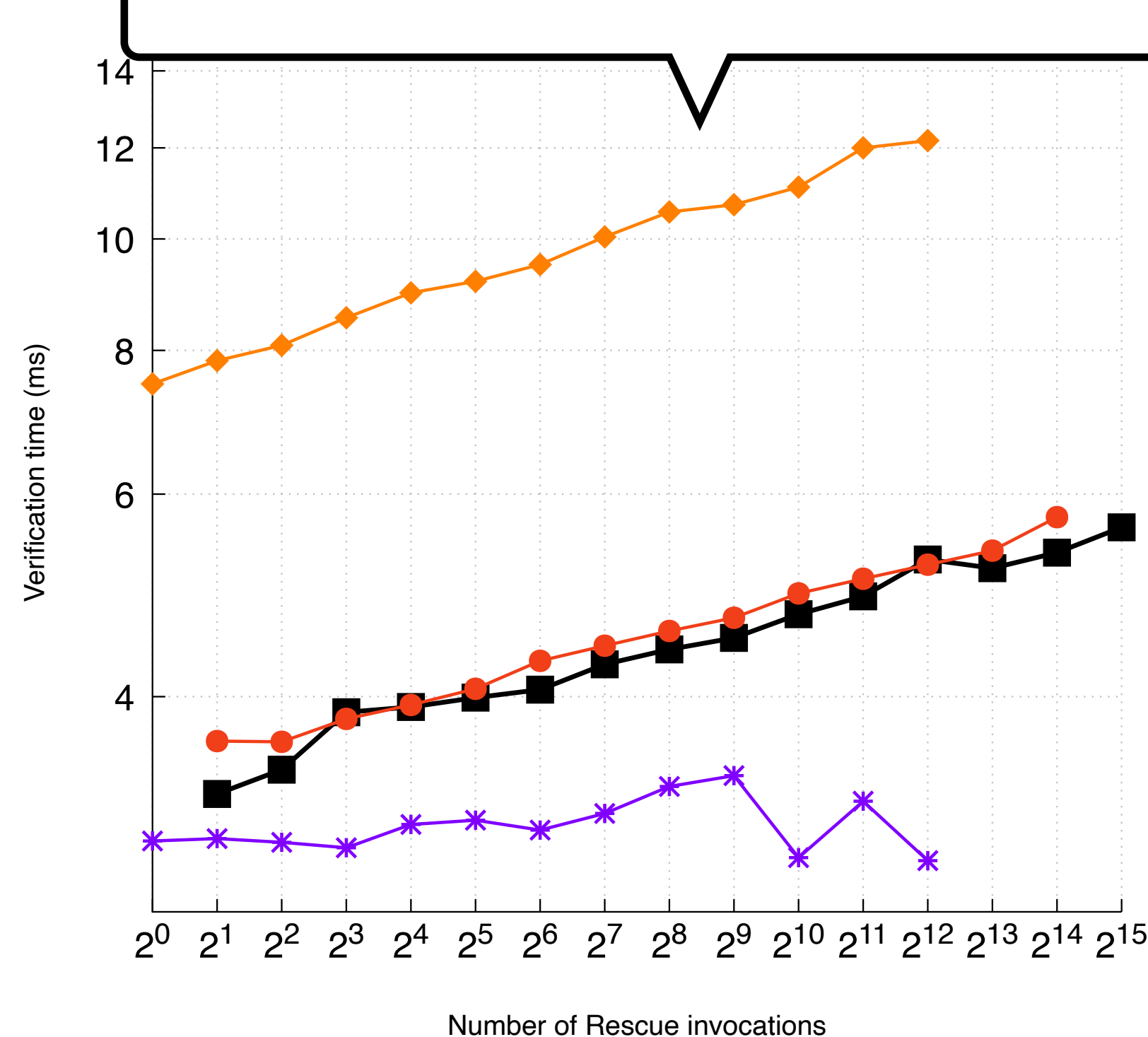


Comparison with Hyperplonk:

~ **2x faster verifier**

Comparison with Groth16:

~ **2x slower verifier**

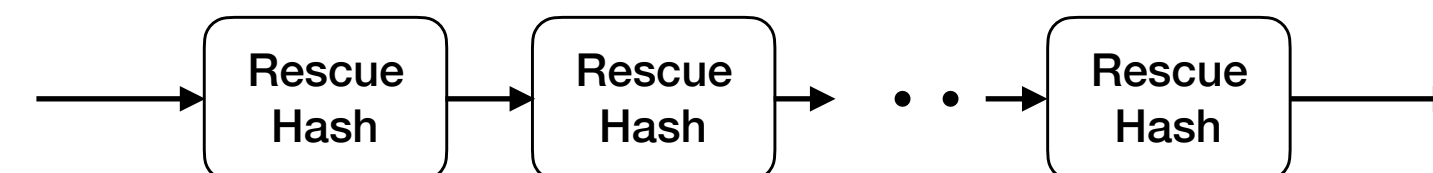


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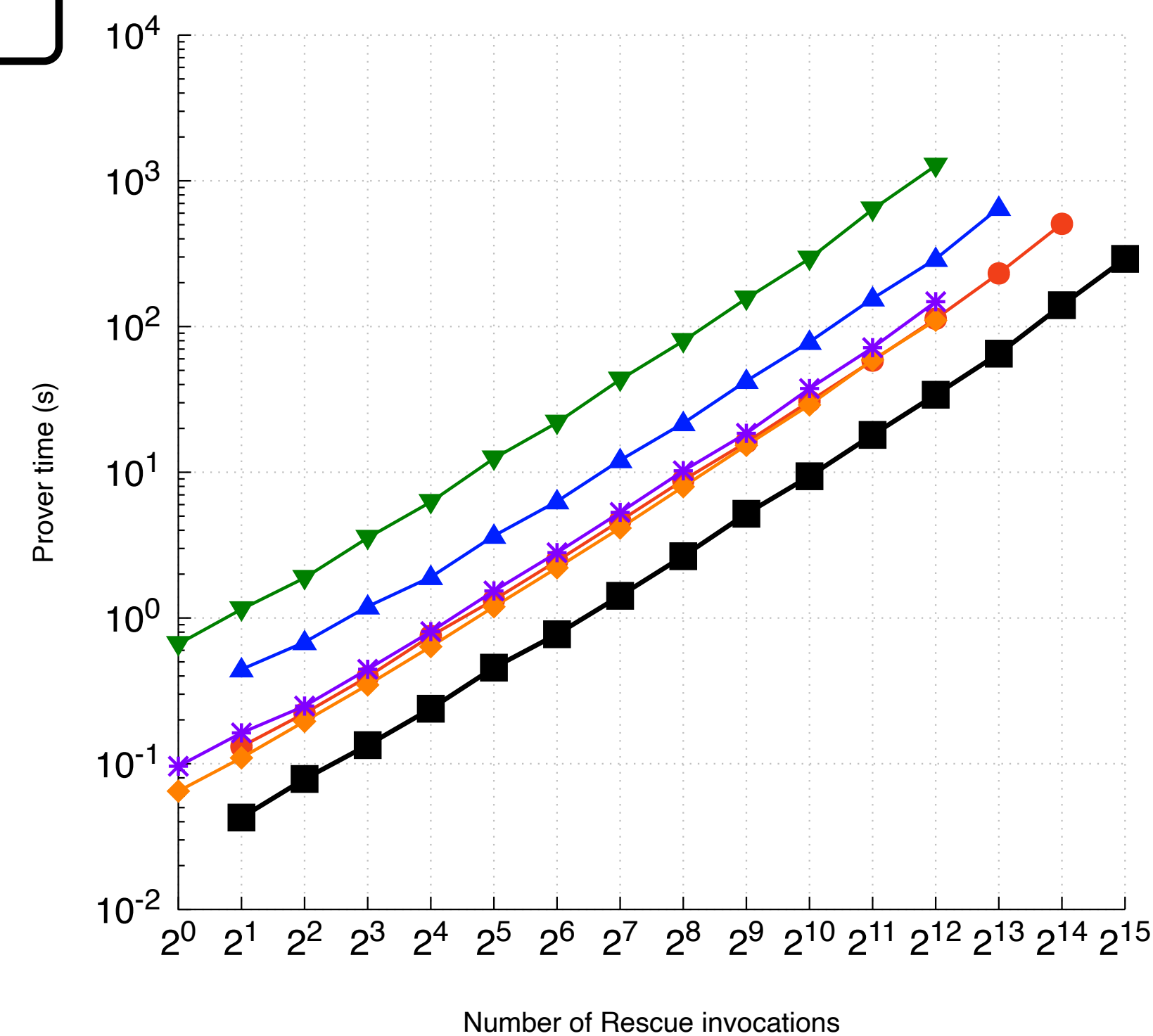
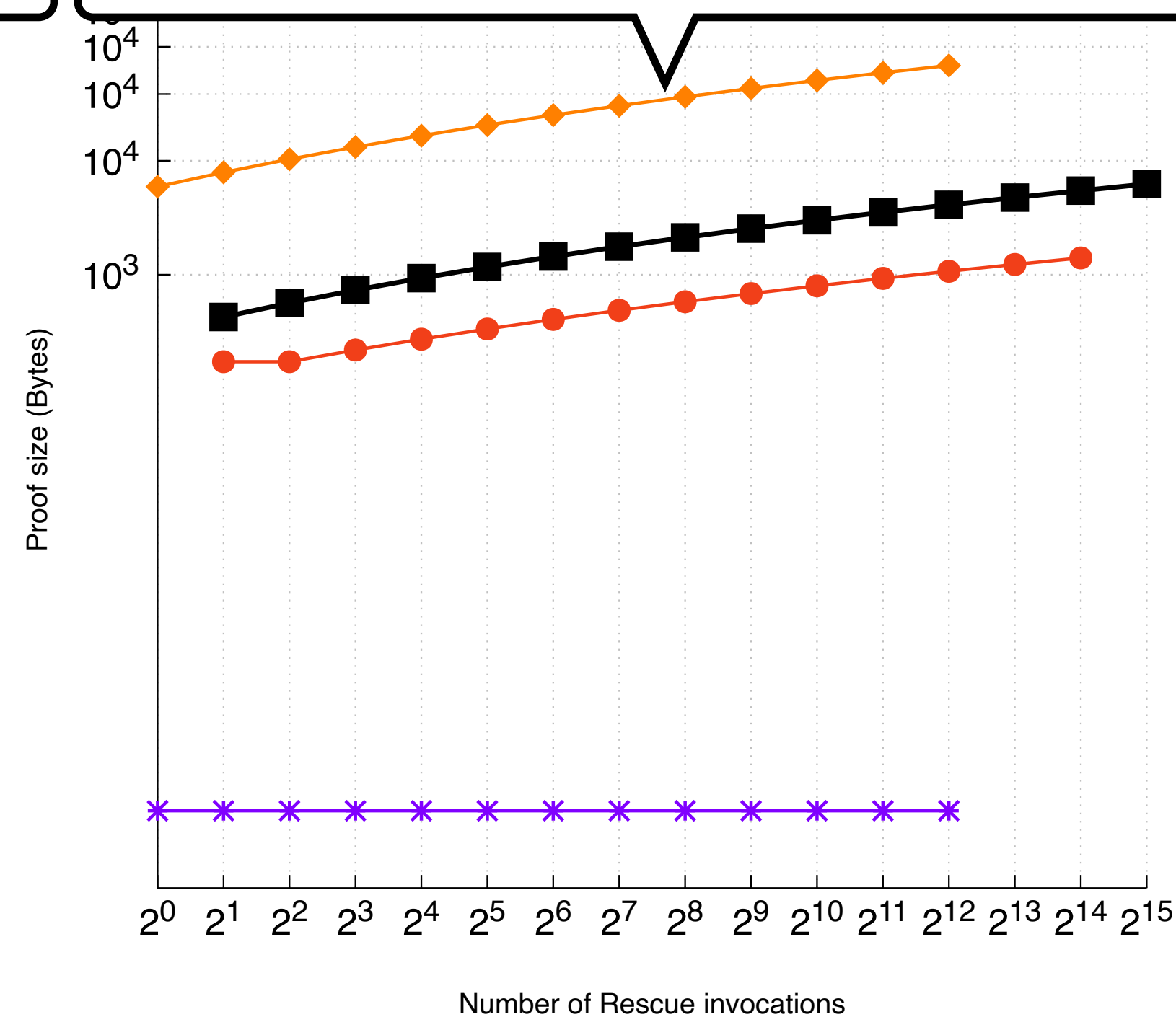
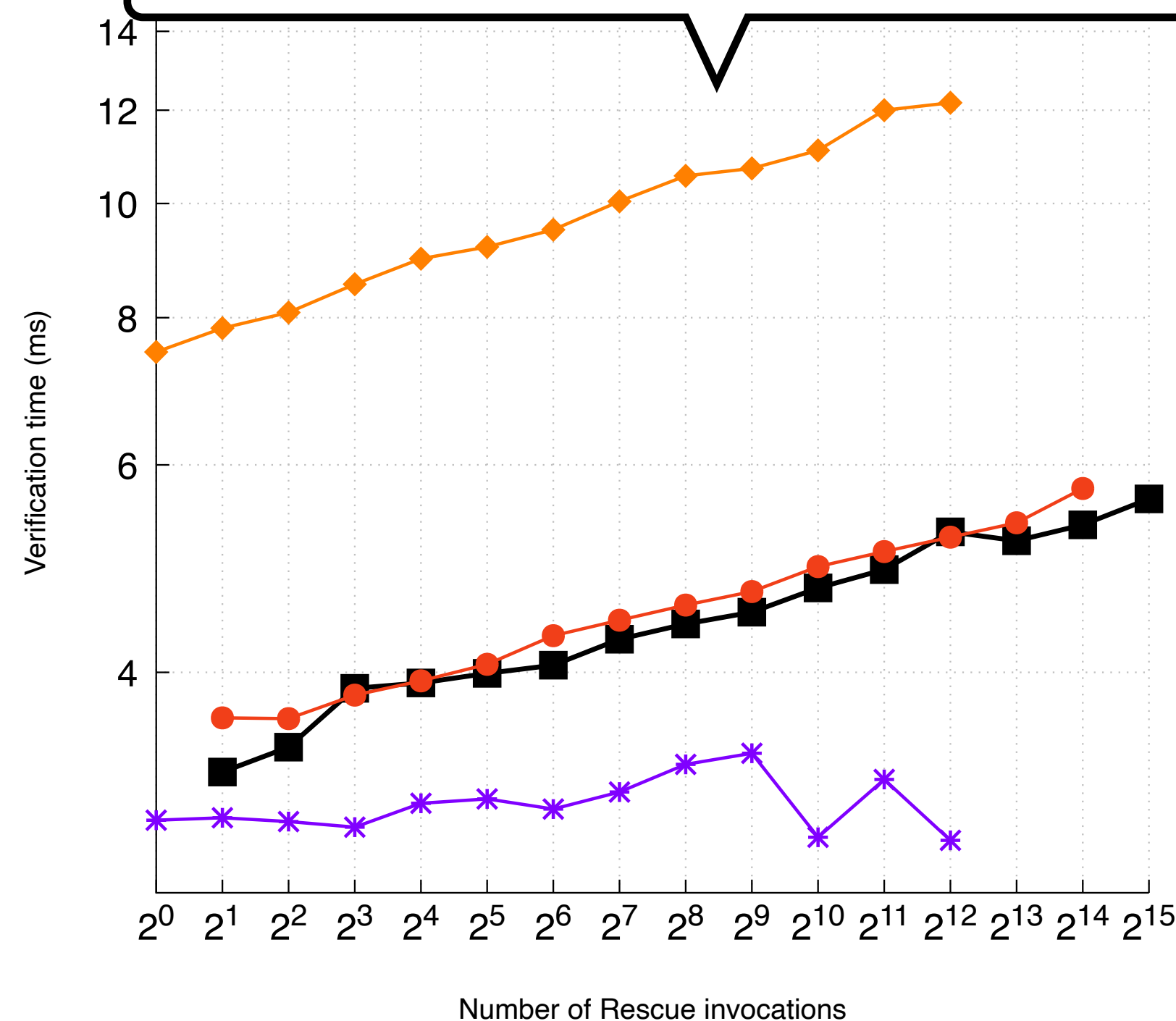
~ **2x slower verifier**

Comparison with Groth16:

~ **30x bigger**

Comparison with Polymath:

~ **2x smaller**

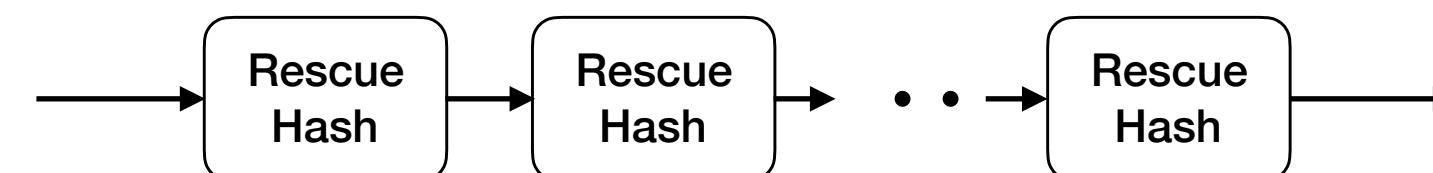


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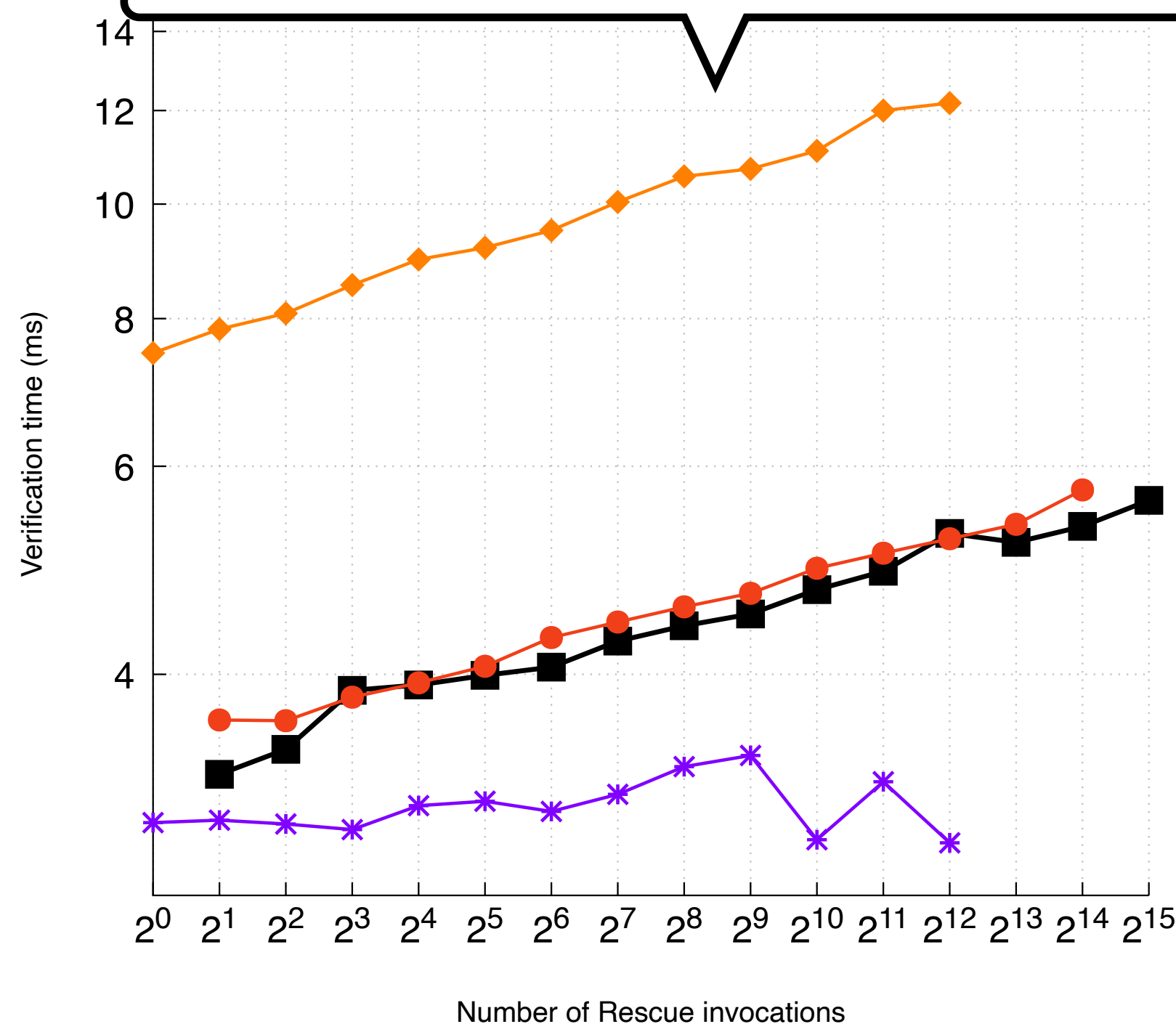
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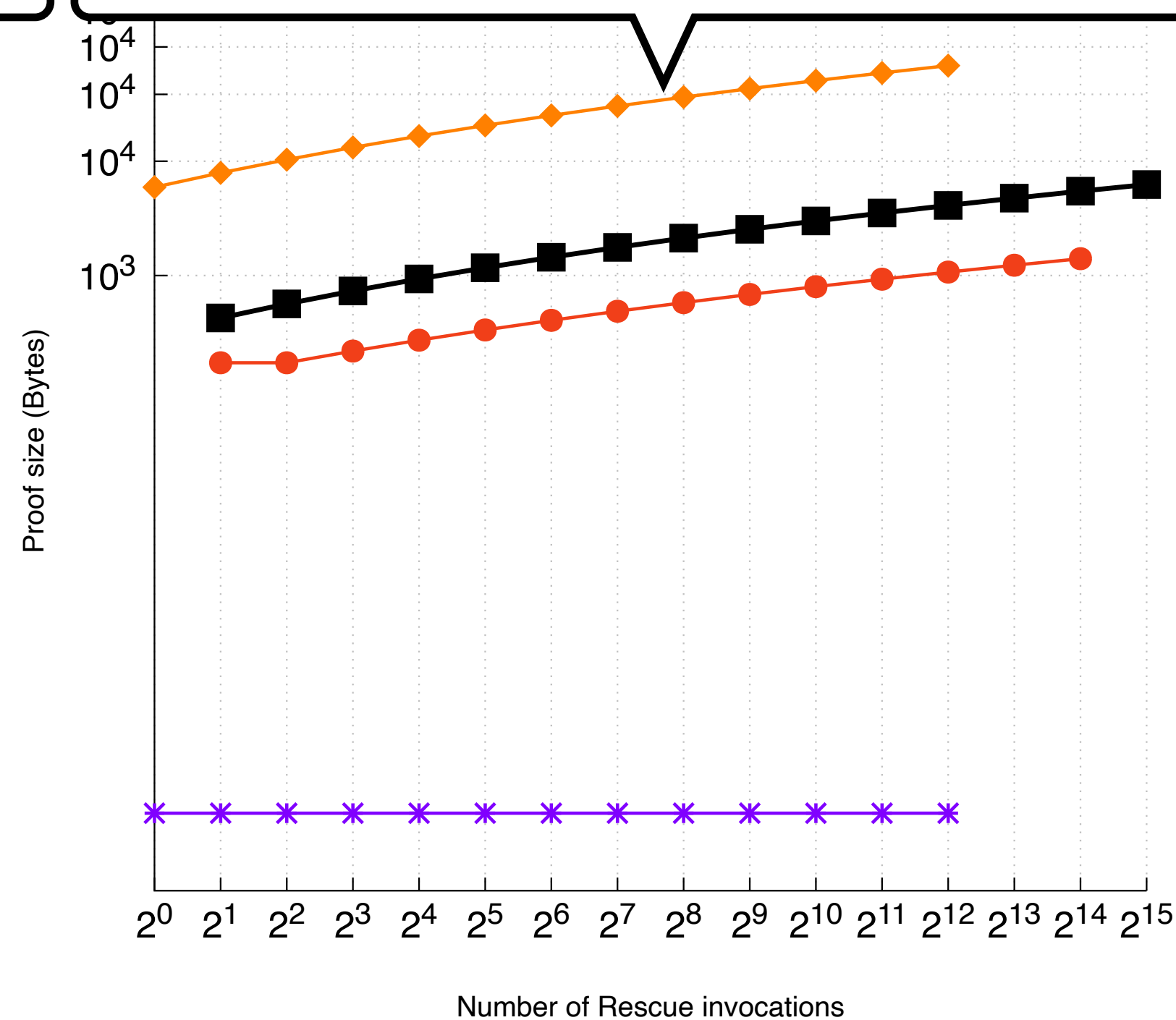
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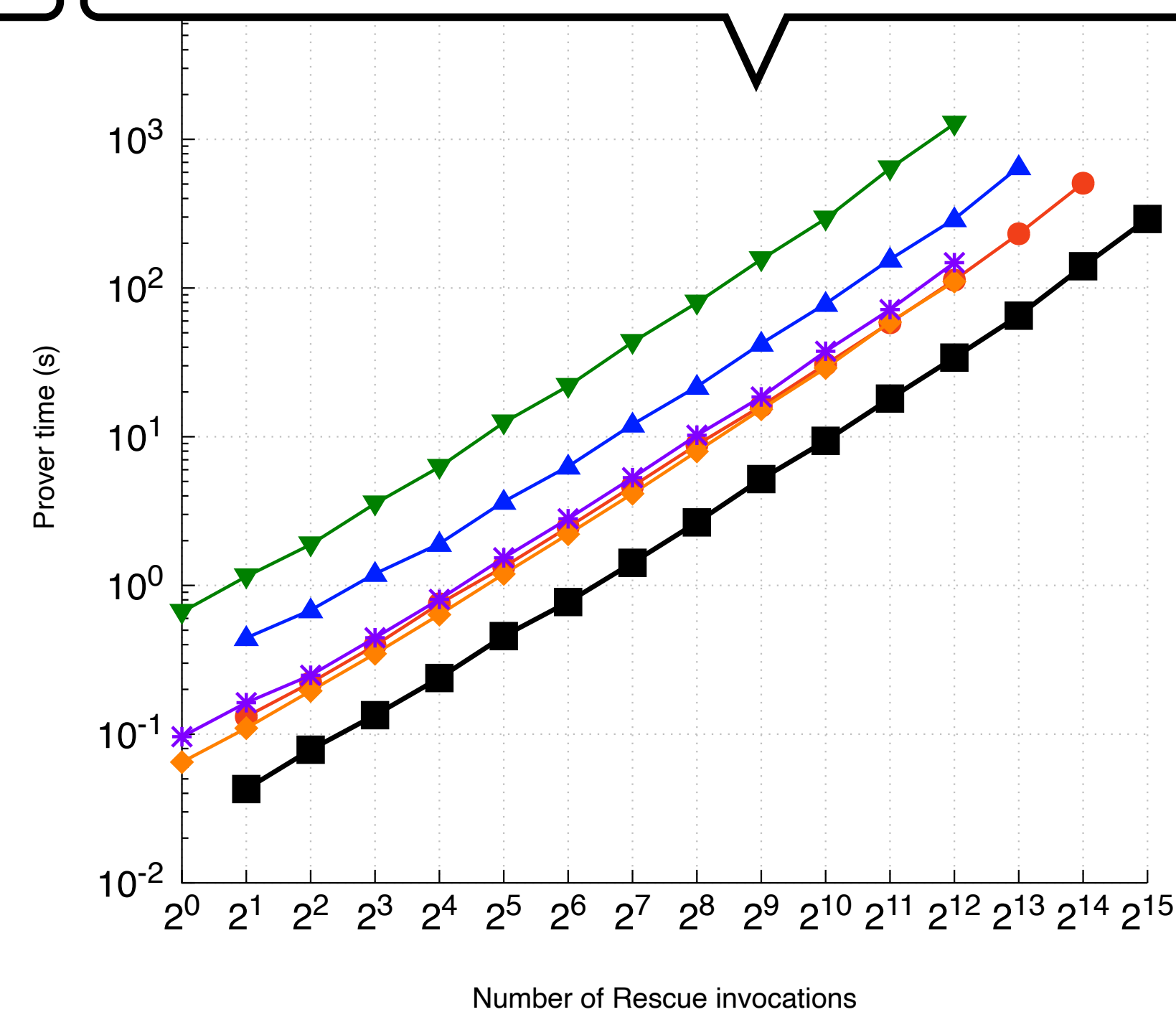
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Comparison with Groth16:
3x faster

Comparison with Polymath:
2x faster



Thanks!

github: github.com/alireza-shirzad/garuda-pari

Open questions

- Our EPC constructions imply circuit-specific setup
 - Q:** can we construct EPC schemes that achieve universal setup?
- What other applications of EPC schemes can we find?
 - Ideas:** Verifiable Secret Sharing, Accumulators, etc?
- Our SNARKs don't achieve ZK.
 - Q:** How can we demonstrate ZK without increasing the proof size?

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ePrint: <https://eprint.iacr.org/2024/1245>

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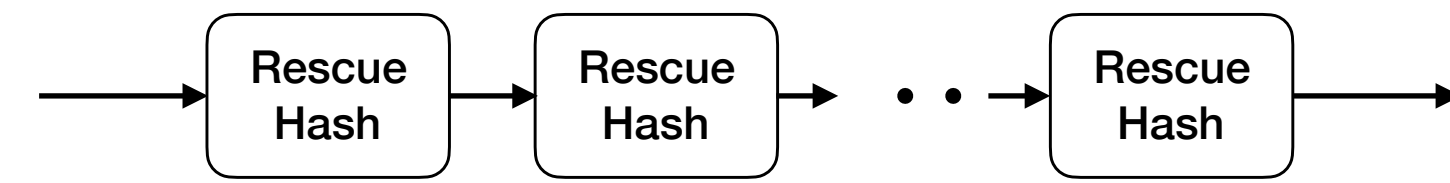
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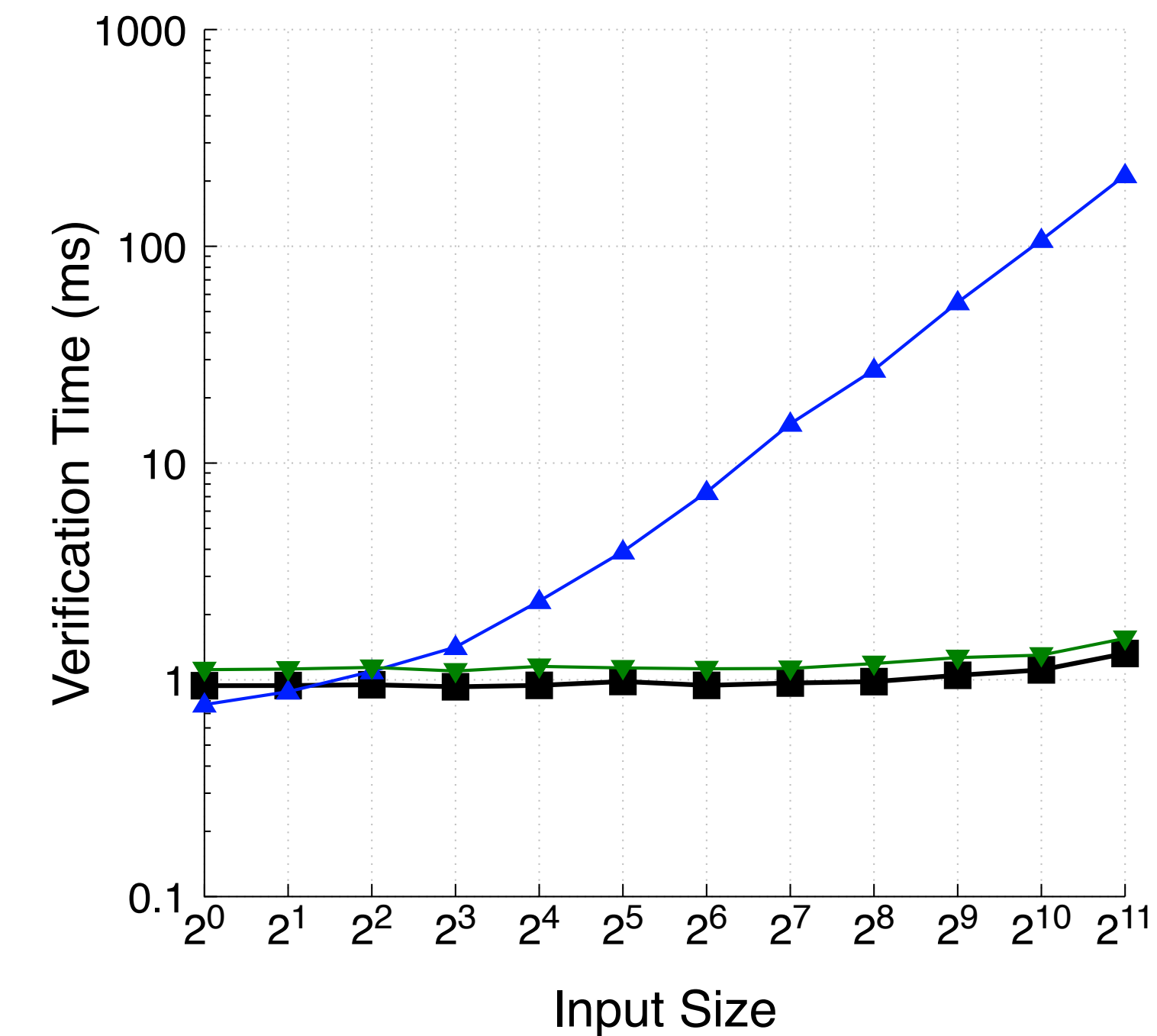
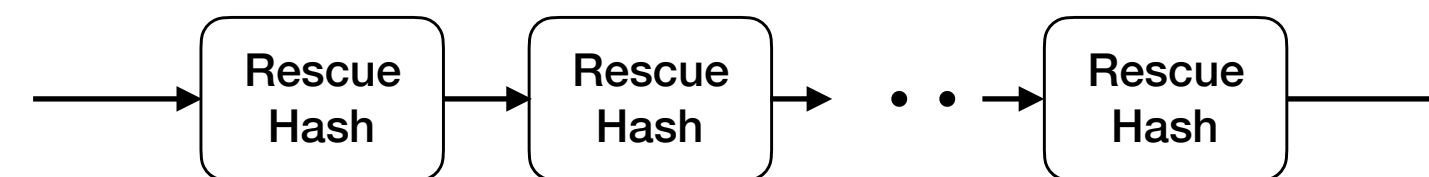
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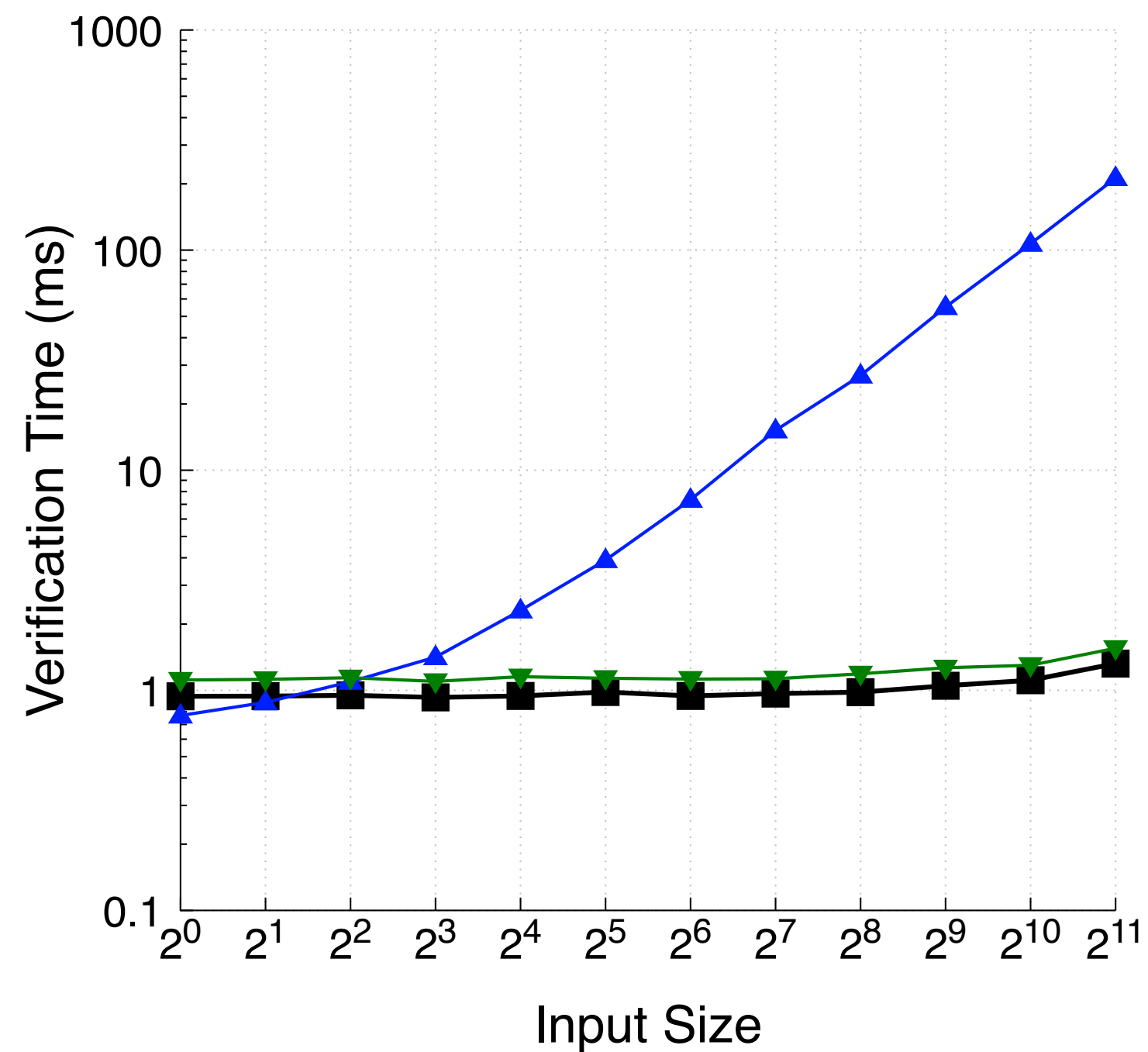
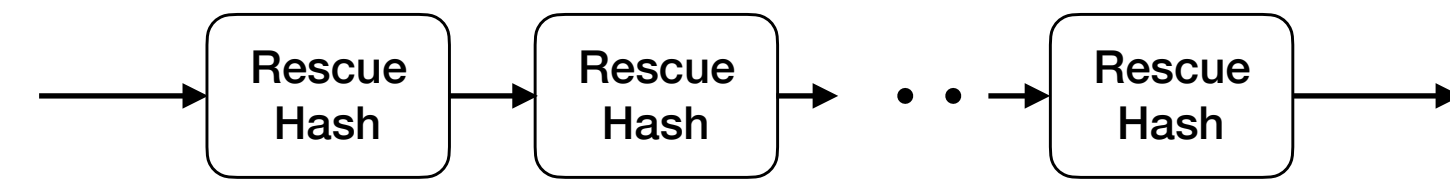
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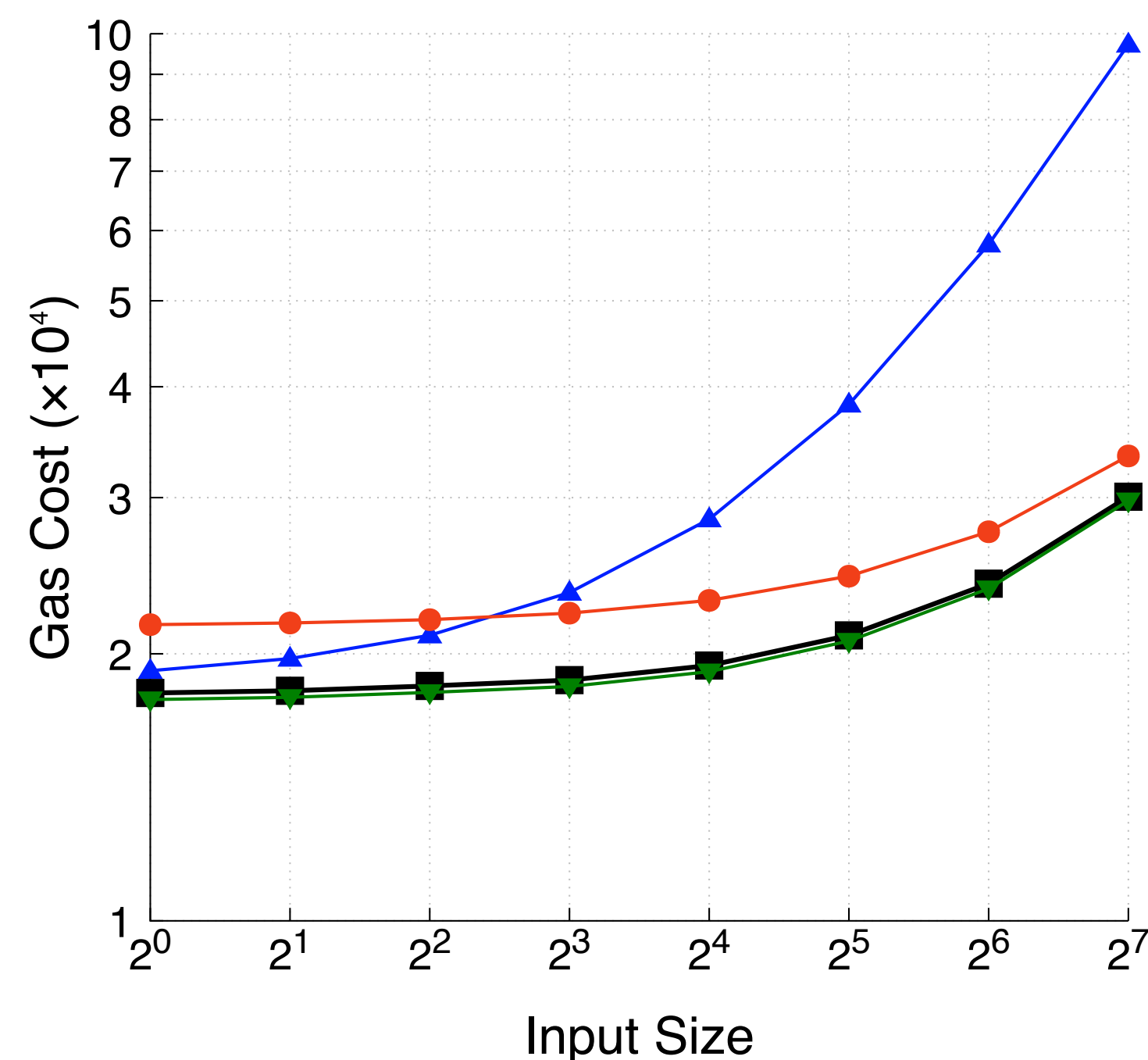
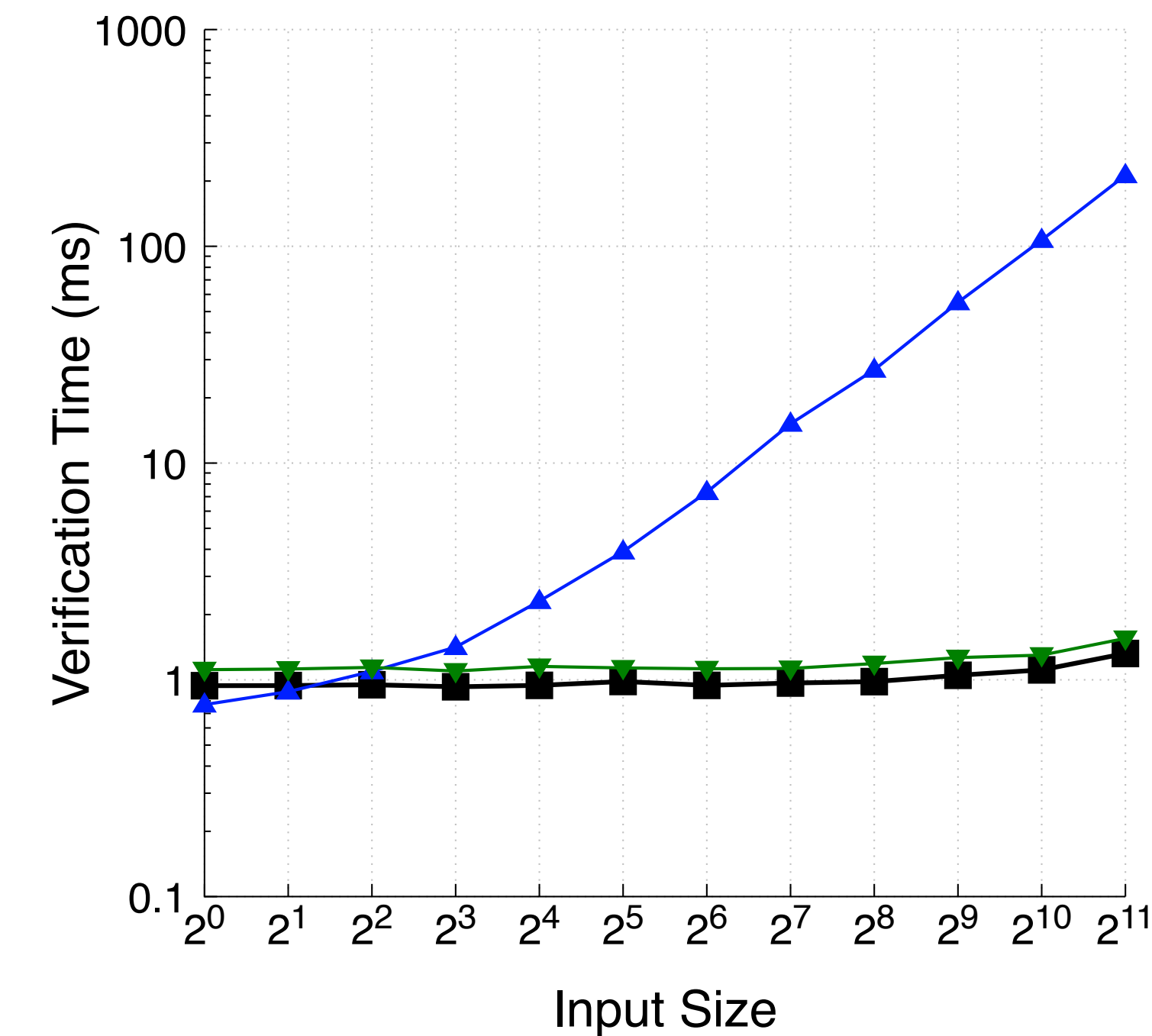
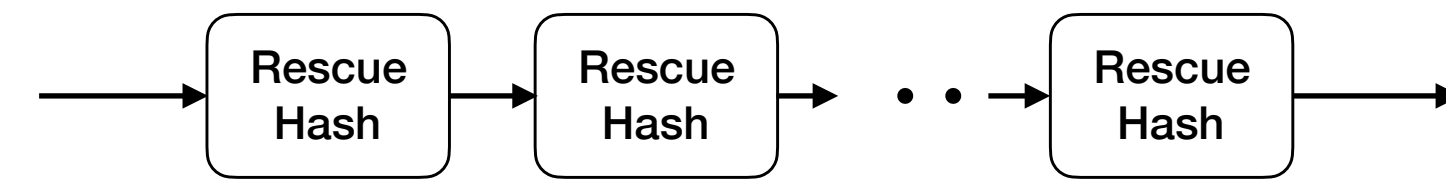
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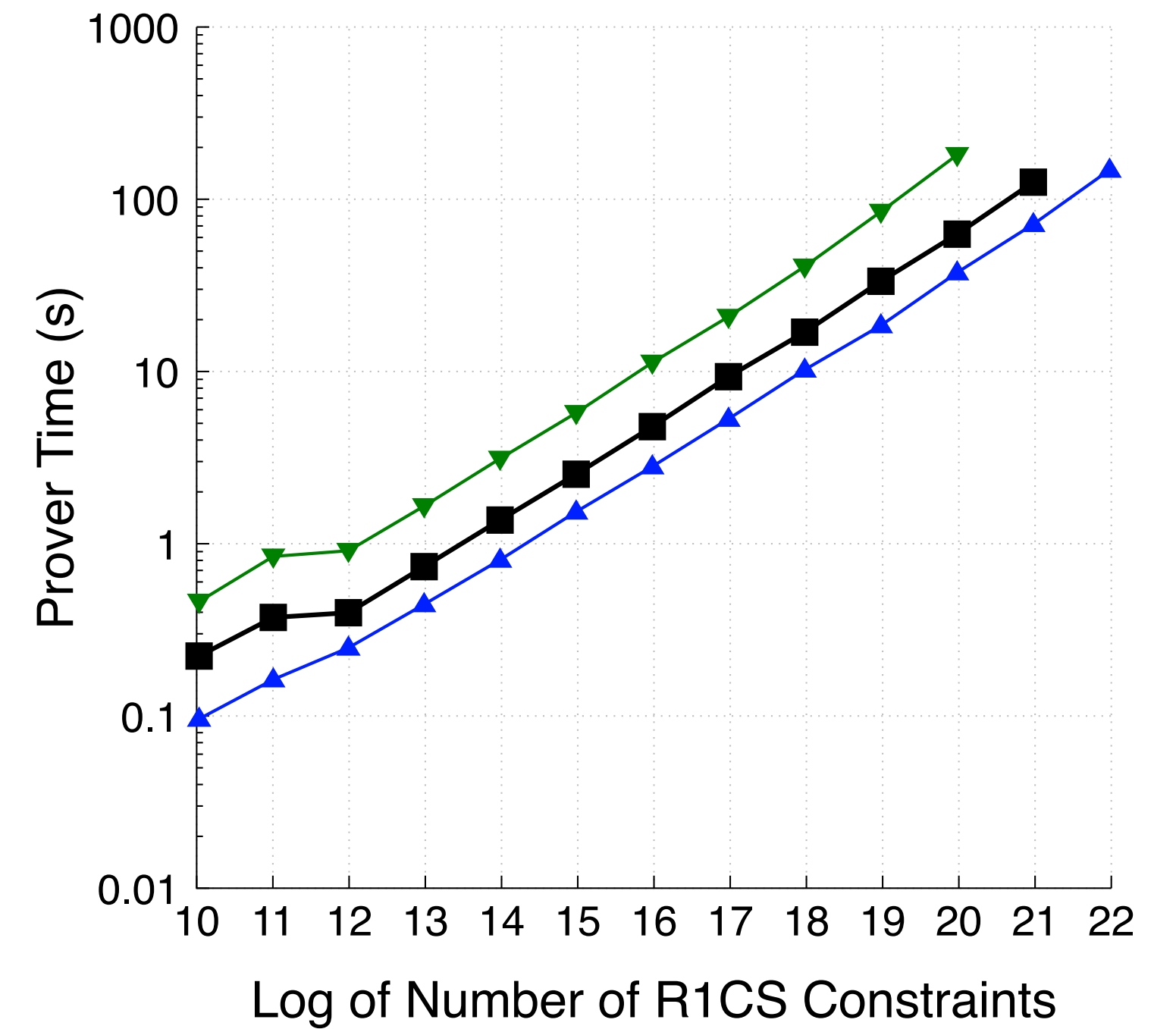
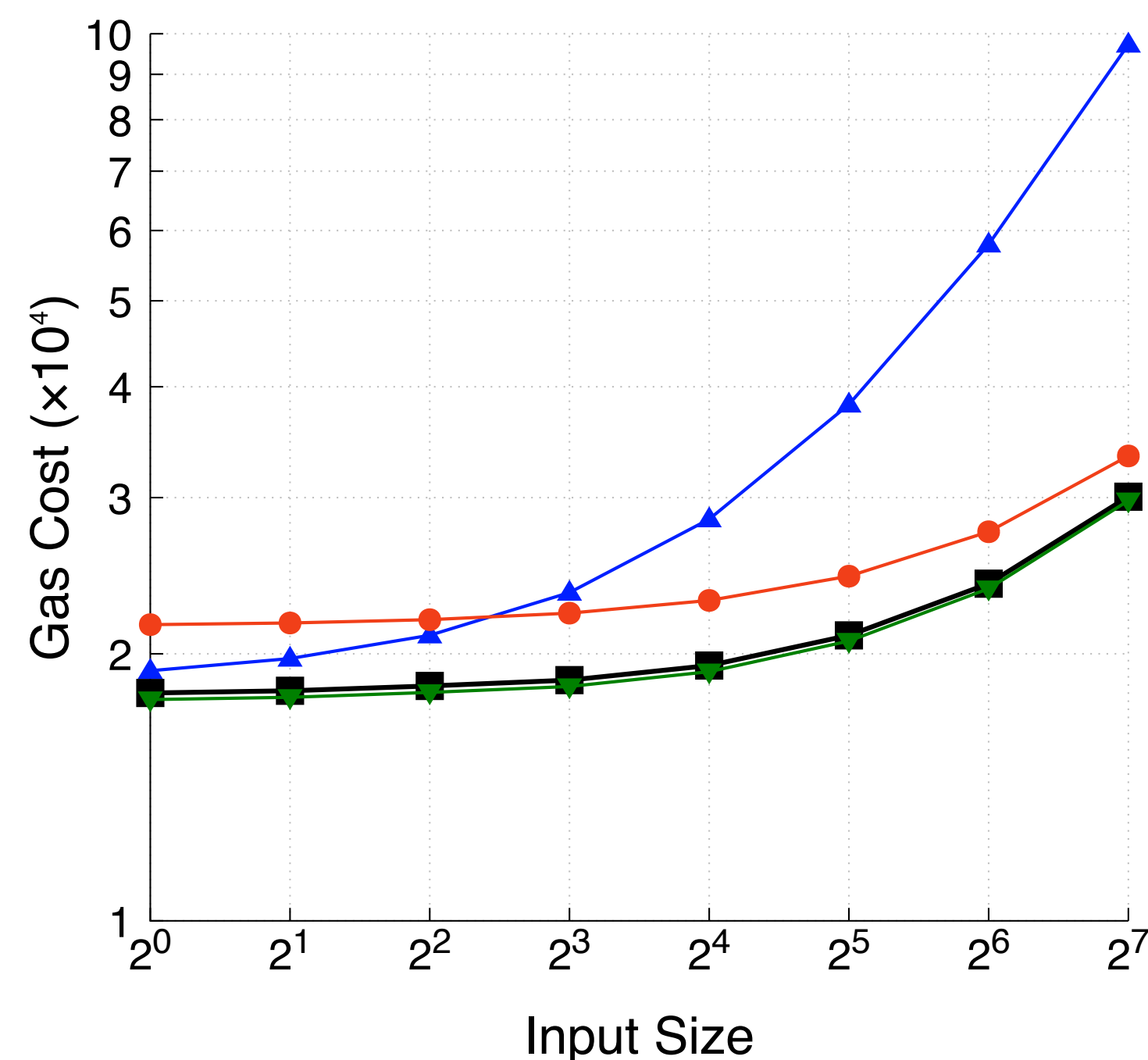
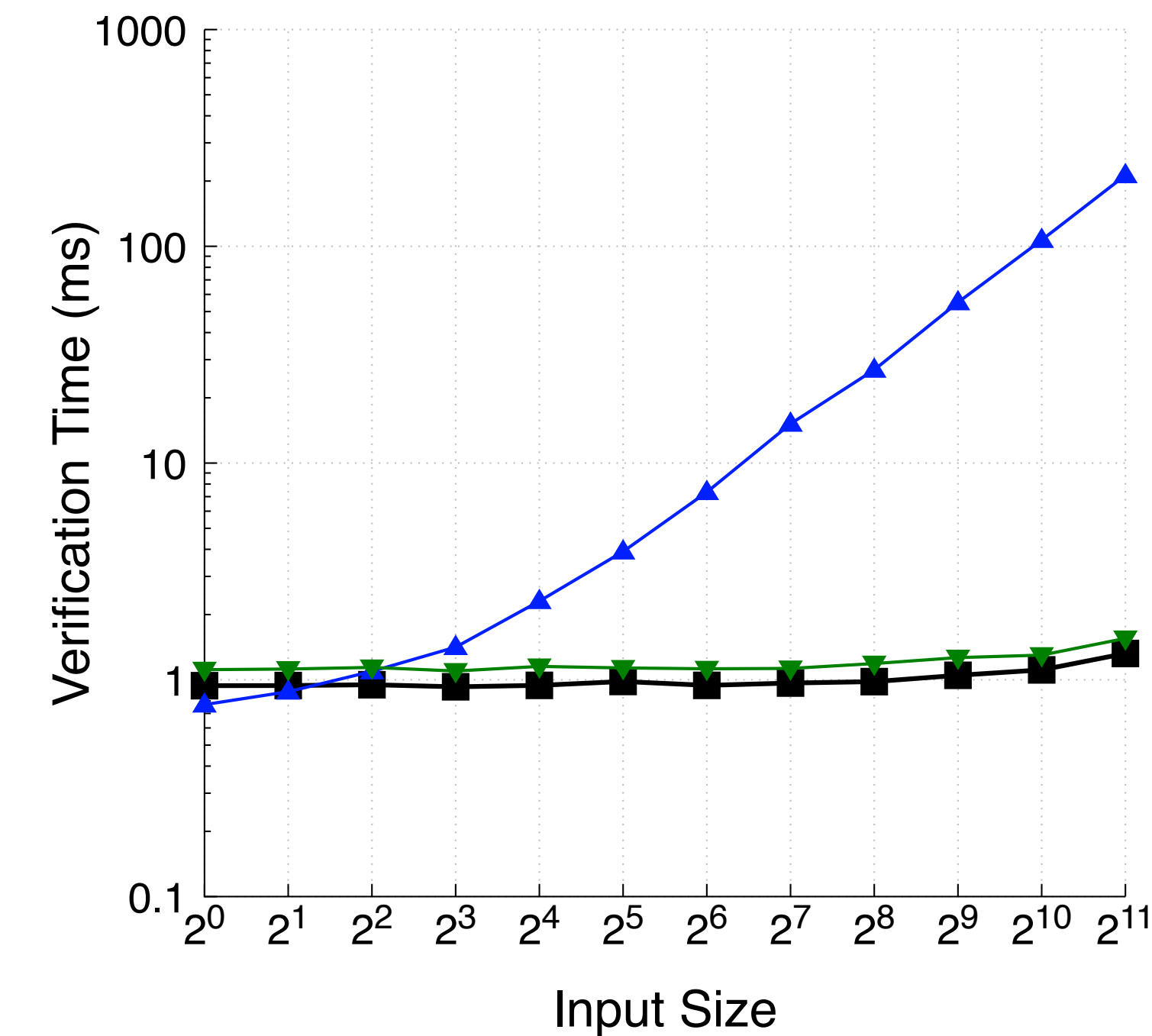
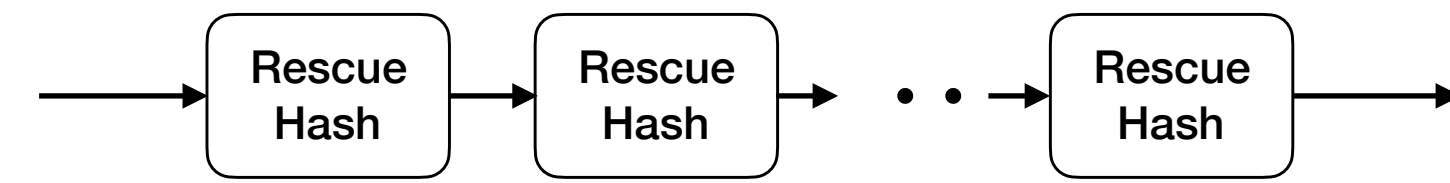
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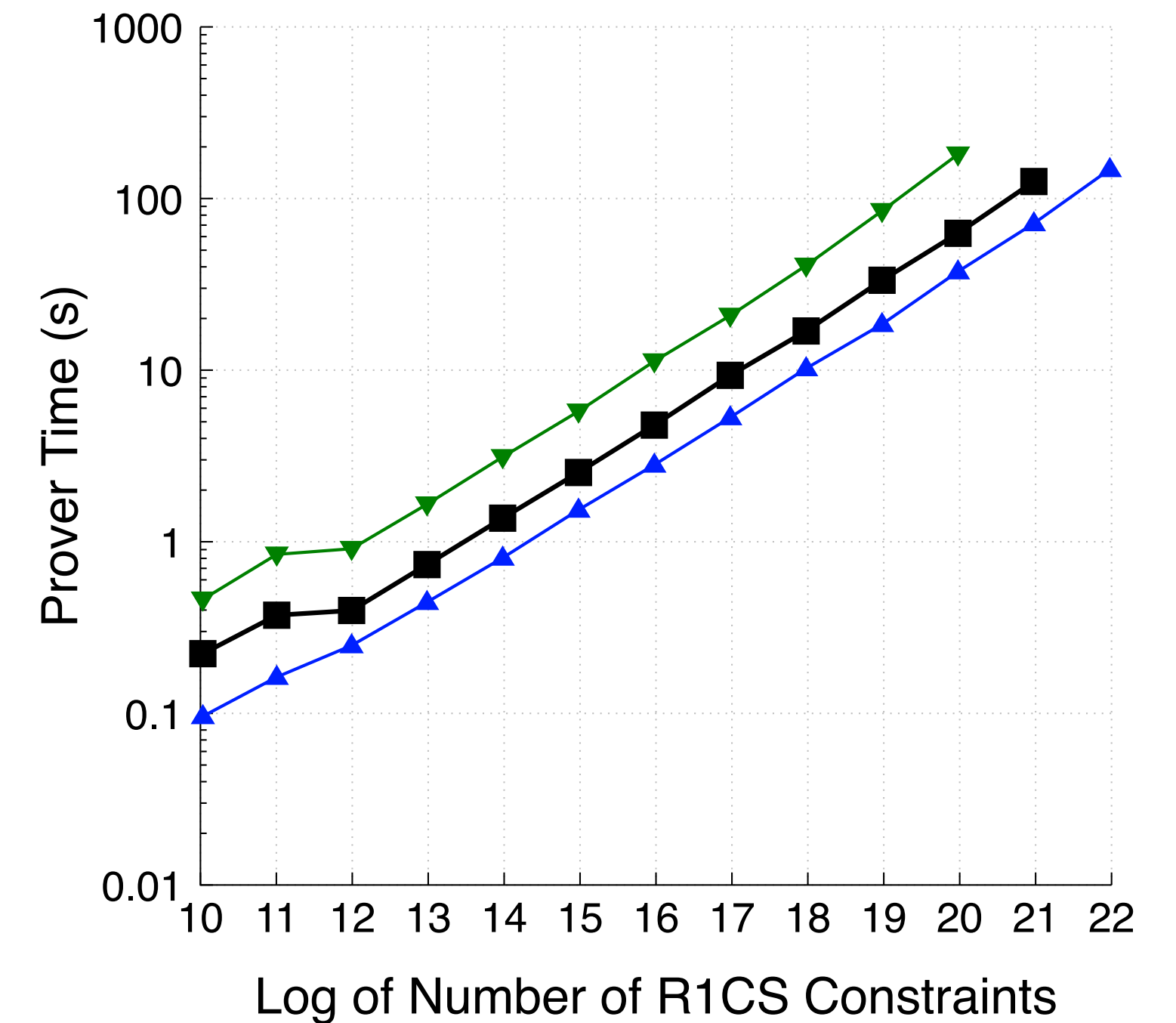
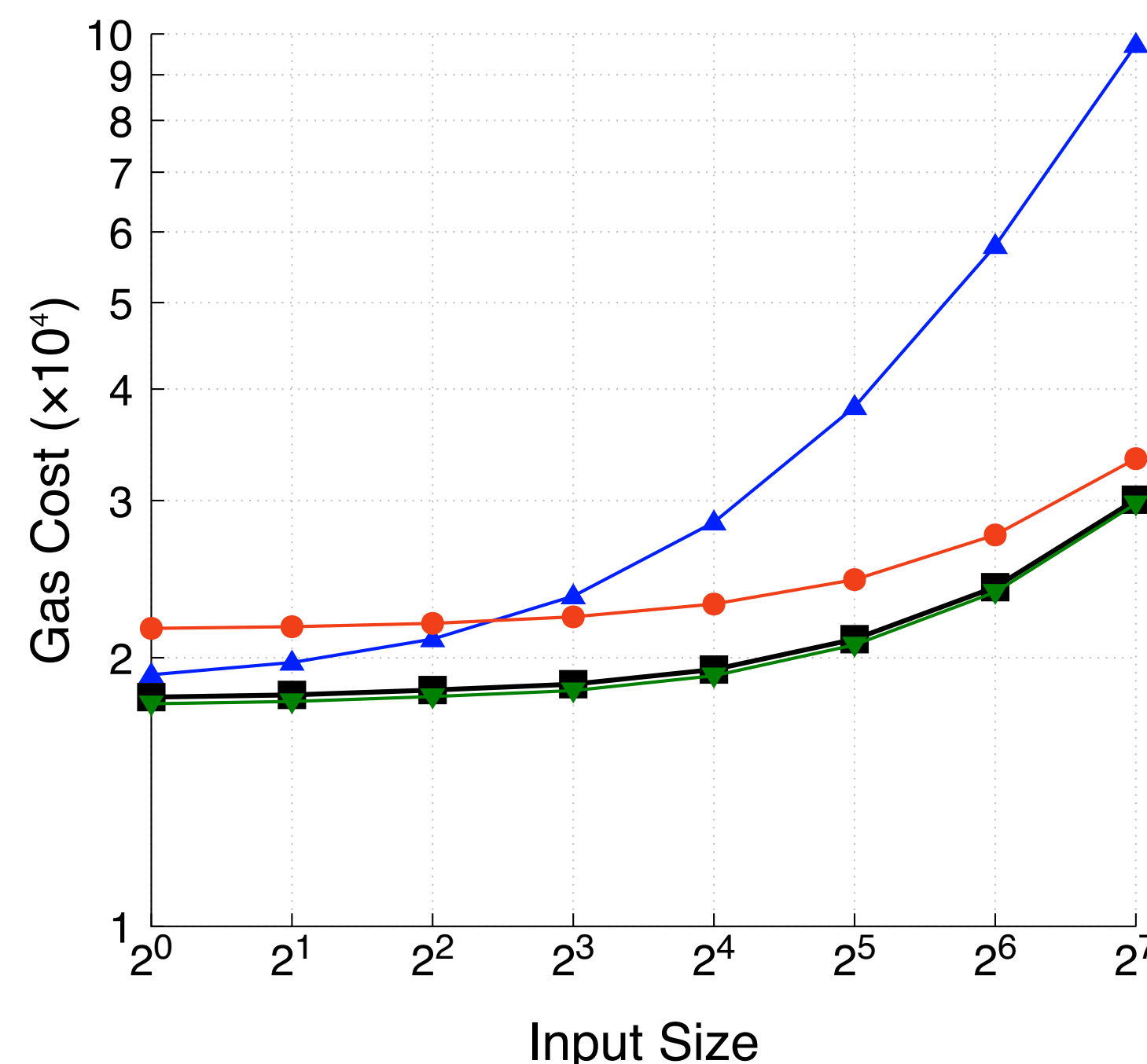
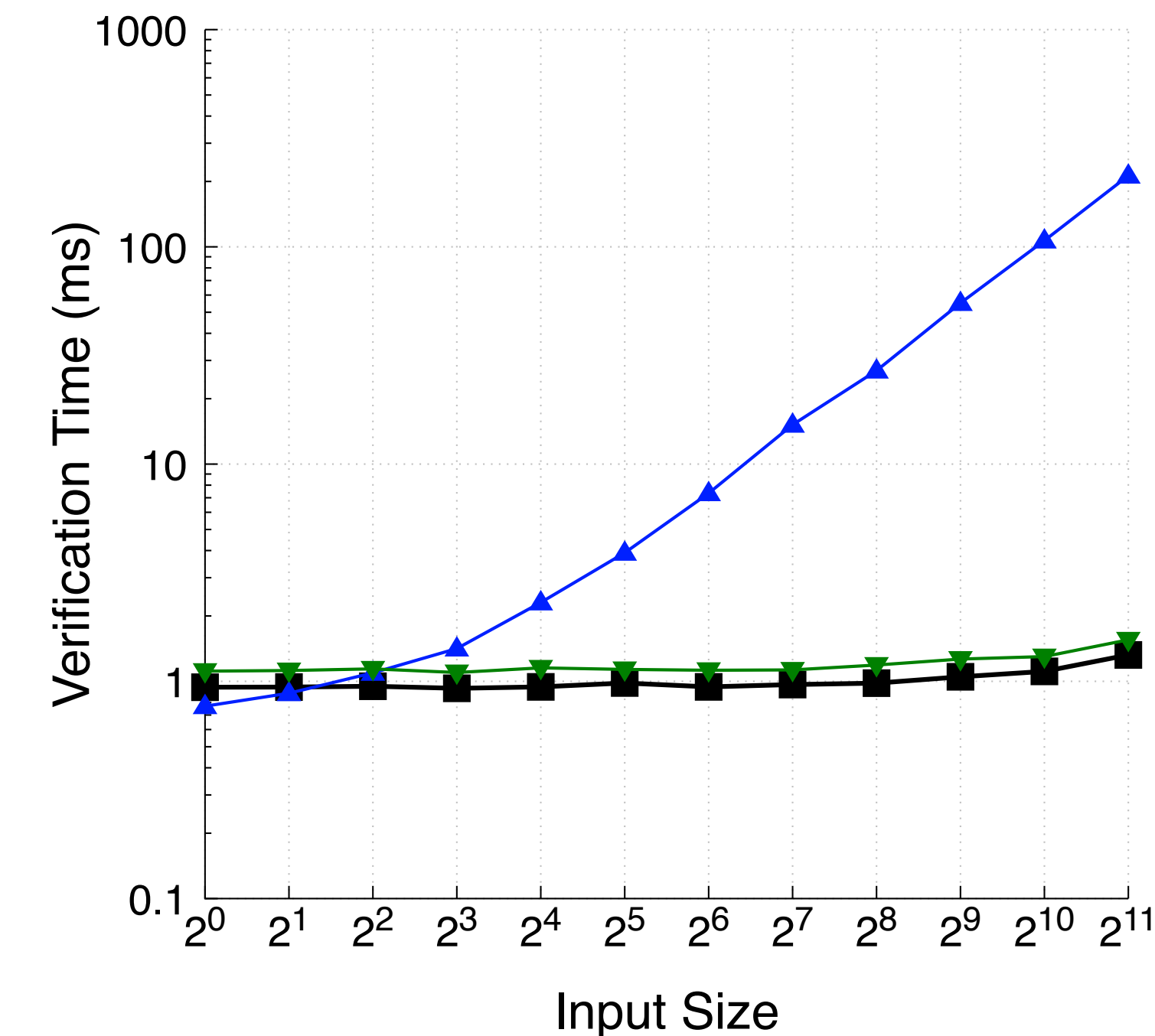
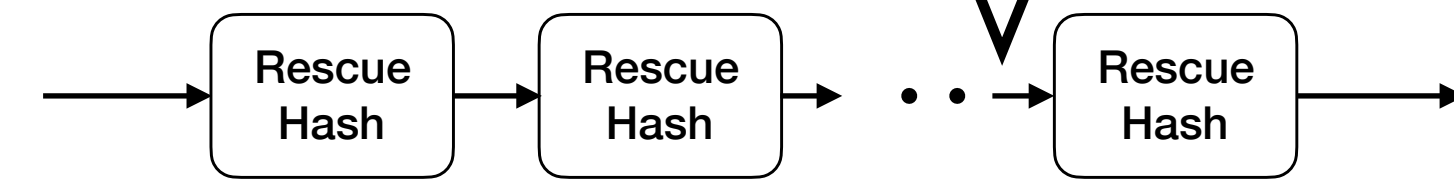
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KZG-based EPC Construction

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Step 3: Next, we take a random linear combination of these committer keys to get the “consistency” committer key!

KZG-based EPC Construction

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$$\mathbf{ck}^* = \alpha \cdot \mathbf{ck}_A + \beta \cdot \mathbf{ck}_B + \gamma \cdot \mathbf{ck}_C =$$

KZG-based EPC Construction

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$$\text{ck}^* = \alpha \cdot \text{ck}_A + \beta \cdot \text{ck}_B + \gamma \cdot \text{ck}_C = \begin{matrix} \alpha \cdot \left[\hat{a}_1(\tau) G, & \hat{a}_2(\tau) G, & \dots & \hat{a}_n(\tau) G \right] + \\ \beta \cdot \left[\hat{b}_1(\tau) G, & \hat{b}_2(\tau) G, & \dots & \hat{b}_n(\tau) G \right] + \\ \gamma \cdot \left[\hat{c}_1(\tau) G, & \hat{c}_2(\tau) G, & \dots & \hat{c}_n(\tau) G \right] \end{matrix}$$

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These are random numbers in $\{1, \dots, n\}$

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⋮

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⋮

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KZG-based EPC Construction

Step 3: Next, we take a random linear combination of these committer keys to get the “consistency” committer key!

$$\text{ck}^* = \alpha \cdot \text{ck}_A + \beta \cdot \text{ck}_B + \gamma \cdot \text{ck}_C = \begin{bmatrix} (\alpha \cdot a_1(\tau) + \beta \cdot b_1(\tau) + \gamma \cdot c_1(\tau)) \cdot G \\ \vdots \\ (\alpha \cdot a_n(\tau) + \beta \cdot b_n(\tau) + \gamma \cdot c_n(\tau)) \cdot G \end{bmatrix}$$

These are random numbers in $\{1, \dots, n\}$

Now, commit to z , which recall is the equal-coefficient representation of \hat{z}_A , \hat{z}_B , and \hat{z}_C

$$c^* = \langle z, \text{ck}^* \rangle = (\alpha \cdot \hat{z}_A(\tau) + \beta \cdot \hat{z}_B(\tau) + \gamma \cdot \hat{z}_C(\tau)) \cdot G$$

This is the consistency commitment!

Pari

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Note: For simplicity, we assume that public input length is 0.

KZG Polynomial Commitment Scheme for p_1 and p_2

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$$e(c_1, H) \stackrel{?}{=} e(\pi_1, \tau H - zH) \cdot e(p_1(z)G, H)$$

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KZG-based EPC (Setup and Specialize)

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$$\mathcal{A} = (a_i(x))_{i=1}^n$$

$$\mathcal{B} = (b_i(x))_{i=1}^n$$

$$\text{Specialize}(\text{pp}, E = (\mathcal{A}, \mathcal{B})) \rightarrow (\text{ck}, \text{vk})$$

Sample $\alpha, \beta \in \mathbb{F}$

$$\text{ck} = (G, \tau G, \tau^2 G, \dots, \tau^n G) \cup \left((\alpha a_i(\tau) + \beta b_i(\tau)) G \right)_{i=1}^n$$

$$\text{vk} := \tau H, \alpha H, \beta H$$

KZG-based EPC (Commit)

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Equifficient

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**Consistency
Commitment**

$$c^* = (\alpha p_1(\tau) + \beta p_2(\tau))G$$

$$= \langle p_1, \left((\alpha a_i(\tau) + \beta b_i(\tau))G \right)_{i=1}^n \rangle$$

$$= \langle p_2, \left((\alpha a_i(\tau) + \beta b_i(\tau))G \right)_{i=1}^n \rangle$$

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- $\text{Commit}(ck, \mathbf{p} = (p_1(X), p_2(X))) \rightarrow \text{cm} = (c_1, c_2, c^*)$
- $\text{Open}(ck, \mathbf{p}, z) \rightarrow \pi = (\pi_1, \pi_2)$
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2 \mathbb{G} elements for opening

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PIOP to check that for three polynomials $\hat{z}_a(X), \hat{z}_b(X), \hat{z}_c(X)$

it holds that for each $i \in \{1, \dots, m\}$: $\hat{z}_a(i) * \hat{z}_b(i) = \hat{z}_c(i)$

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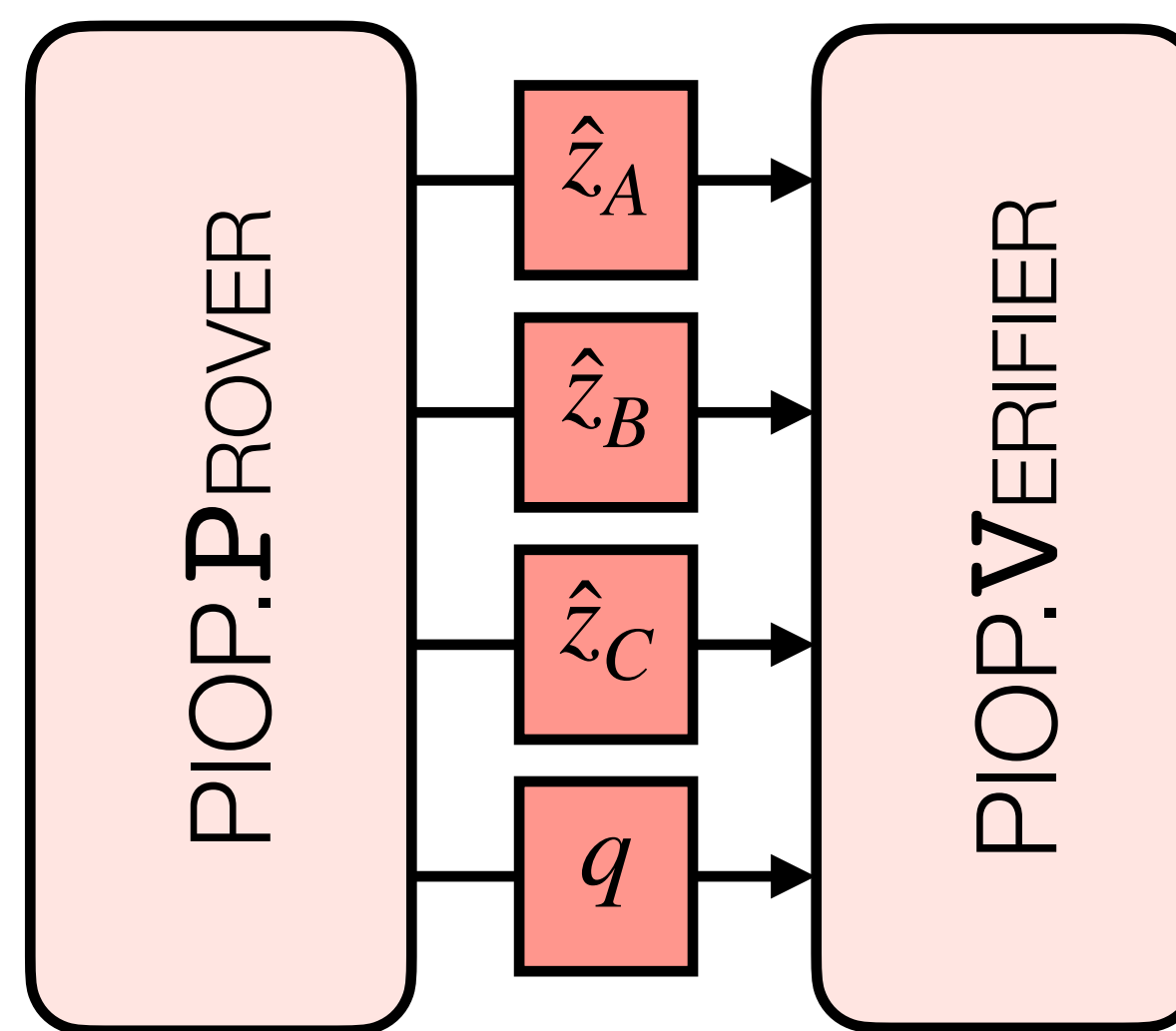
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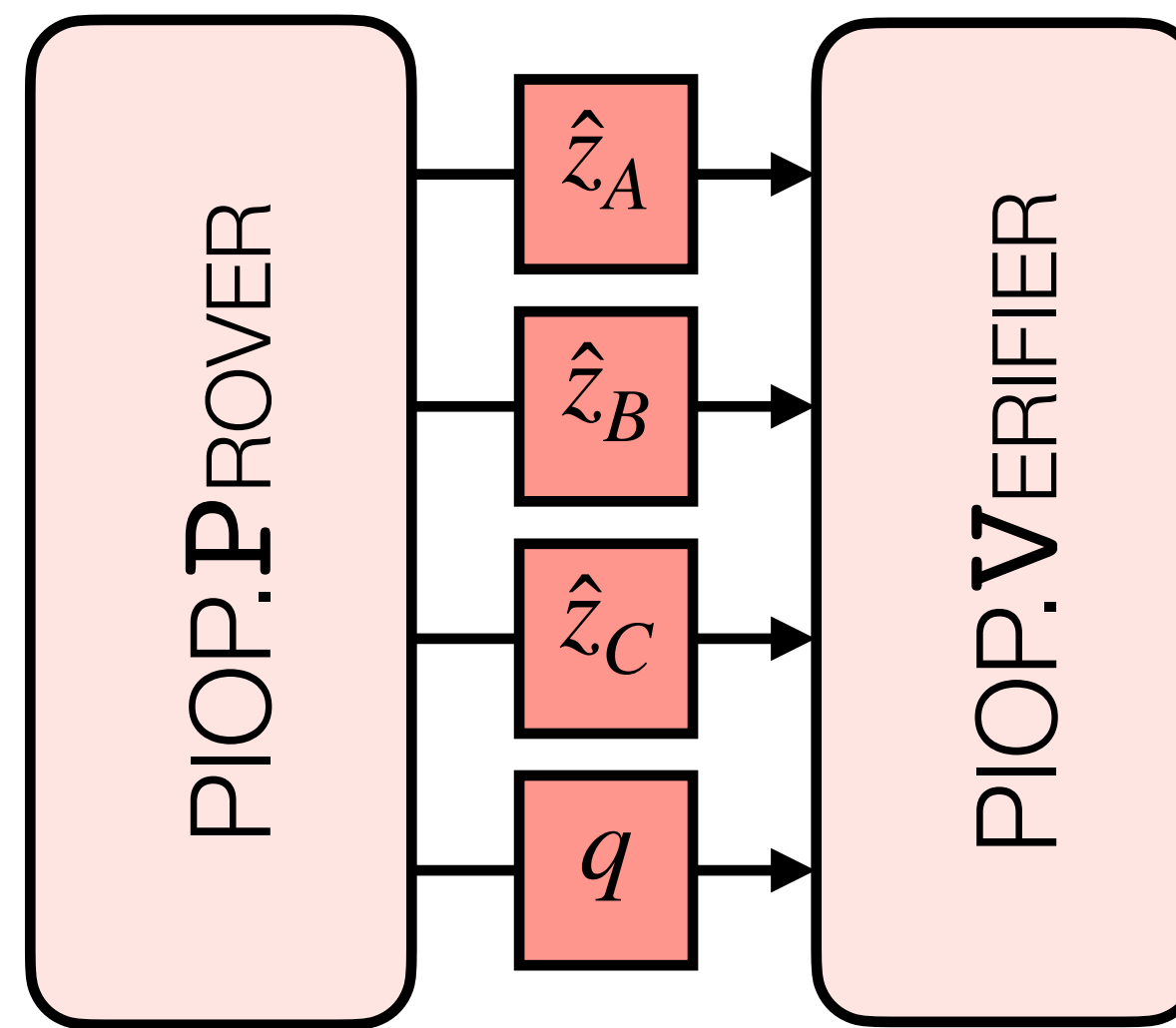
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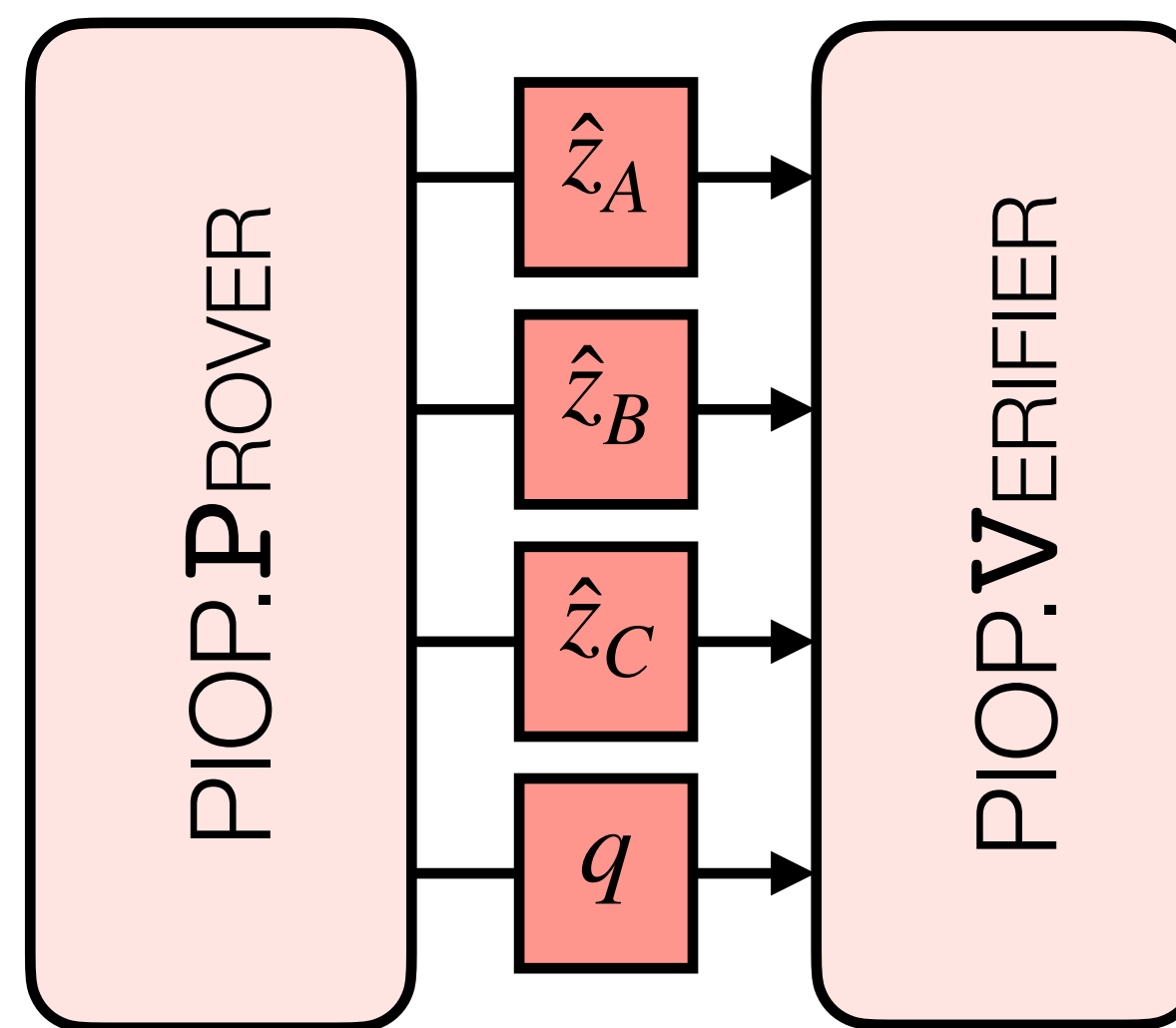
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Note: In practice, We replace $\{1, \dots, m\}$ with a smooth multiplicative subgroup

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$$\text{Hence: } |\pi| = 2\mathbb{G} + 2\mathbb{F}$$

Garuda

Diff of Pari and Garuda

Pari	Garuda
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We can extend this to an arbitrary expression of the form like:

- High degree gate: $Az \circ Bz \circ (Cz)^4 - (Dz)^3 \circ (Ez)^4 \circ Fz + 7 = 0$
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A GR1CS instance is composed of local predicates

$$\mathcal{C} = \left(\mathcal{L}_i : \mathbb{F}^{t_i} \rightarrow \{0,1\}, (M_{i,1}, \dots, M_{i,t_i}) \right)_{i \in [c]}$$

We say \mathcal{C} is satisfied iff for all $i \in [c]$: $\mathcal{L}_i(M_{i,1}z, \dots, M_{i,t_i}z) = 0$

Grand multivariate zerocheck

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Grand multivariate zerocheck

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We stack these matrices on top of each other

$$\mathcal{L}^* \rightarrow M_{1,1}^*, \dots, M_{1,t}^*$$

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The Rowcheck PIOP checks the following grand multivariate zerocheck:

$$\begin{aligned}&\mathcal{L}^*(M_{1,1}^*z, \dots, M_{1,t}^*z) \\ &= S_1 \cdot \mathcal{L}_1(M_{1,1}^*z, \dots, M_{1,t}^*z) + S_c \cdot \mathcal{L}_c(M_{1,1}^*z, \dots, M_{1,t}^*z) = 0\end{aligned}$$

Grand multivariate zerocheck

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We stack these matrices on top of each other

$$\mathcal{L}^* \rightarrow M_{1,1}^* \ , \dots, \ M_{1,t}^*$$

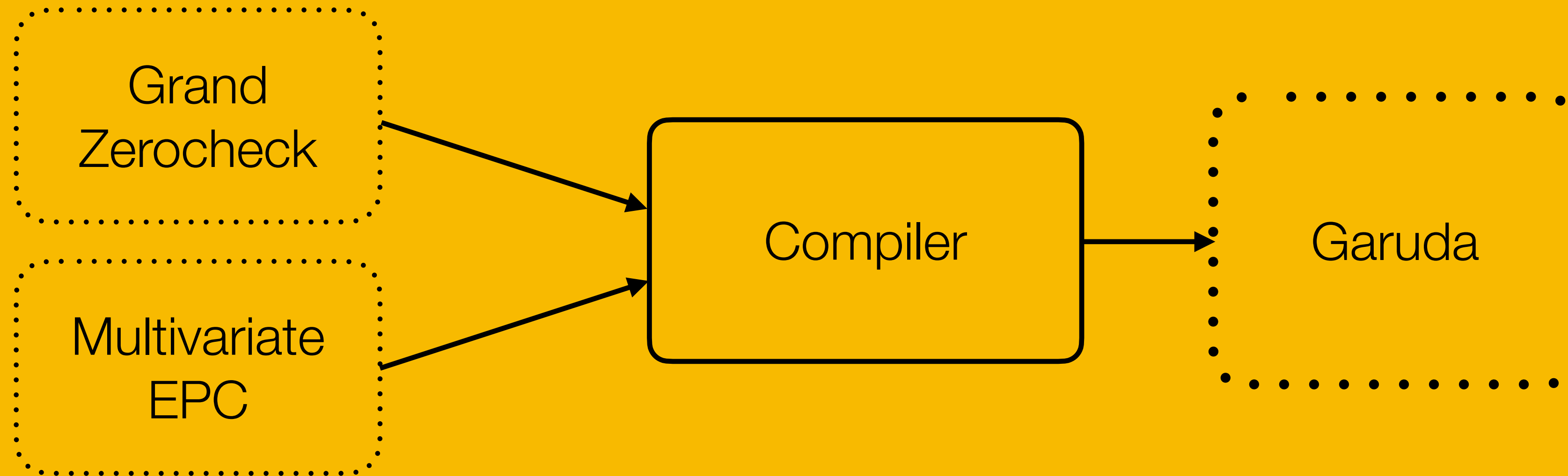
The Rowcheck PIOP checks the following grand multivariate zerocheck:

$$\begin{aligned}&\mathcal{L}^*(M_{1,1}^*z, \dots, M_{1,t}^*z) \\ &= S_1 \cdot \mathcal{L}_1(M_{1,1}^*z, \dots, M_{1,t}^*z) + S_c \cdot \mathcal{L}_c(M_{1,1}^*z, \dots, M_{1,t}^*z) = 0\end{aligned}$$

Selector for the 1st predicate

Selector for the c -th predicate

Garuda



$$= S_1 \cdot \mathcal{L}_1(M_{1,1}^* z, \dots, M_{1,t}^* z) + S_c \cdot \mathcal{L}_c(M_{1,1}^* z, \dots, M_{1,t}^* z) = 0$$

Selector for the 1st predicate

Selector for the c -th predicate

Thanks!

Open questions

- Our EPC constructions imply circuit-specific setup
Q: can we construct EPC schemes that achieve universal setup?
- What other applications of EPC schemes can we find?
Ideas: Verifiable Secret Sharing, Accumulators, etc?
- Our SNARKs don't achieve ZK.
Q: How can we demonstrate ZK without increasing the proof size?

Thanks!

ePrint: <https://eprint.iacr.org/2024/1245>

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